Unit in mm

TOSHIBA Photocoupler GaAlAs IRED + Photo IC

# TLP705F

Plasma Display Panel Industrial Inverter IGBT/Power MOSFET Gate Drive

TLP705F consists of a GaAlAs light emitting diode and a integrated photodetector.

This unit is 6-lead SDIP package. TLP705F is 50% smaller than 8PIN DIP and has suited the safety standard reinforced insulation class. So mounting area in safety standard required equipment can be reduced.

TLP705F is suitable for gate driving circuit of IGBT or power MOS FET. Especially TLP705F is capable of "direct" gate drive of lower Power IGBTs. Absolute Maximum ratings and electrical characteristics are the same as The TI P705 technical data sheets.

Peak output current : ±0.45 A (max) Operating frequency : 250kHz (max) Guaranteed performance over temperature: -40 to 100°C Supply current : 3mA (max) Power supply voltage : 10 to 20 V

Threshold input current : I<sub>FLH</sub> = 8 mA (max) : 200 ns (max) Switching time (tpLH / tpHL) Common mode transient immunity : ±10 kV/µs(min) Isolation voltage : 5000 Vrms(min)

**UL** Recognized :UL1577, File No.E67349

Construction Mechanical Rating

Creepage Distance	8.0 mm (min)
Clearance	8.0 mm (min)
Insulation Thickness	0.4 mm (min)
	0.4 mm (min)

Option (D4)

TÜV approved : EN60747-5-2

Certificate No. R50033433

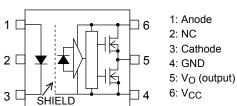
Maximum operating insulation voltage : 1140 Vpk Highest permissible over voltage : 8000 Vpk

## $25 \pm ^{+0.10}_{-0.05}$ 1.27±0.2 0.75±0.25 0.4±0.1 11.7±0.3 11-5J101 TOSHIBA 11-5J101 Weight: 0.26 g (typ.)

7.62±0.25

4.58±0.25

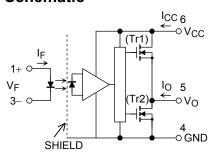
### Pin Configuration (top view)



#### **Truth Table**

Input	LED	Tr1	Tr2	Output
Н	ON	ON	OFF	Н
L	OFF	OFF	ON	L

#### **Schematic**



A 0.1 µF bypass capacitor must be connected between pin 6 and 4. (See Note 6)

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20070701-EN

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