TOSHIBA Photocoupler PHOTORELAY

TLP3122

Measurement Instruments
Logic Testers / Memory Testers
Board Testers / Scanners
Power Line Control
FA (Factory Automation)

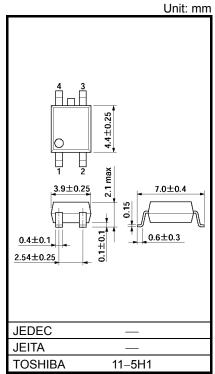
The TOSHIBA TLP3122 consists of a gallium arsenide infrared emitting diode optically coupled to a photo-MOS FET in a plastic SOP package. The TLP3122 is a bi-directional switch, which can replace mechanical relays in many applications. And its high on-state current maximum rating is suitable to control a power line.

Features

• 4 pin SOP (2.54SOP4) : 2.1 mm high, 2.54 mm pitch

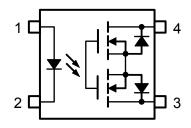
• 1-Form-A

Peak off-state voltage
 G0 V (Min.)
 Trigger LED current
 3 mA (Max.)
 On-State current
 1A (Max.)
 On-state resistance
 0.25 Ω (Typ.)
 Off-state capacitance
 90 pF (Typ.)
 Off-state current
 100nA (Max.)
 Isolation voltage
 1500 Vrms (Min.)



Weight: 0.1 g (Typ.)

Pin configuration (top view)



- 1 : Anode
- 2 : Cathode
- 3: Drain
- 4 : Drain

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	l _F	50	mA
Ē	Forward current derating (Ta ≧ 25°C)	ΔI _F /°C	-0.5	mA/°C
_ =	Reverse voltage	V _R	5	V
	Junction temperature	Tj	125	°C
	Off-state output terminal voltage	V _{OFF}	60	V
Detector	On-state current	I _{ON}	1	Α
Dete	On-state current derating (Ta ≧ 50°C)	∆l _{ON} /°C	-13.3	mA/°C
	Junction temperature	Tj	125	°C
Stora	ge temperature range	T _{stg}	-40~125	°C
Oper	ating temperature range	T _{opr}	-20~85	°C
Lead	soldering temperature (10 s)	T _{sol}	260	°C
Isolat	ion voltage (AC, 1 minute, R.H. \leq 60%) (Note 1)	BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1): Device considered a two-terminal device: LED side pins shorted together, and detector side pins shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V_{DD}	_	_	48	V
Forward current	Ιϝ	5	10	20	mA
Operating temperature	T _{opr}	25	_	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μА
	Capacitance	C _T	V = 0, f = 1 MHz	_	15	_	pF
ctor	Off-state current	l _{OFF}	V _{OFF} = 60 V	_	0.2	100	nA
Detector	Capacitance	C _{OFF}	V = 0, f = 1 MHz	_	90	_	pF

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Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	I _{ON} = 100 mA	_	1	3	mA
Return LED current	I _{FC}	I _{OFF} = 100 μA	0.1	0.8	_	mA
On-state resistance	R _{ON}	$I_{ON} = 1 \text{ A}, I_F = 5 \text{ mA}$		0.25	0.7	Ω

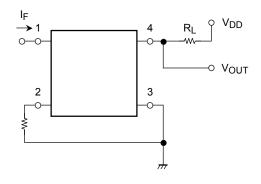
Isolation Characteristics (Ta = 25°C)

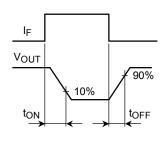
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5 × 10 ¹⁰	10 ¹⁴	_	Ω
		AC, 1 minute	1500	_	_	Vrms
Isolation voltage	BV_S	AC, 1 second (in oil)	_	3000	_	VIIIIS
		DC, 1 minute (in oil)	_	3000	_	Vdc

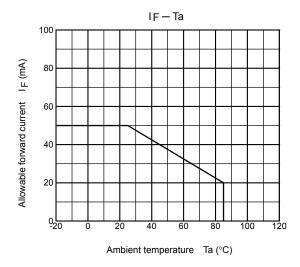
Switching Characteristics (Ta = 25°C)

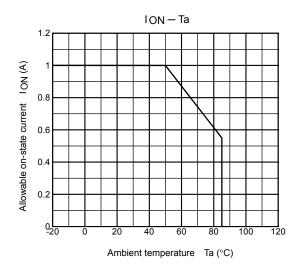
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Turn-on time	t _{ON}	$R_L = 200 \Omega$ (Note 2)	_	1.4	3	ms
Turn-off time	t _{OFF}	$V_{DD} = 20 \text{ V}, I_F = 5\text{mA}$	_	0.6	1	1115

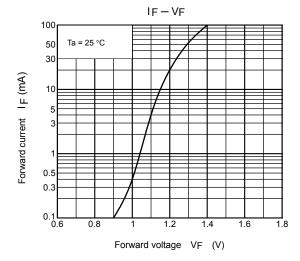
(Note 2): switching time test circuit

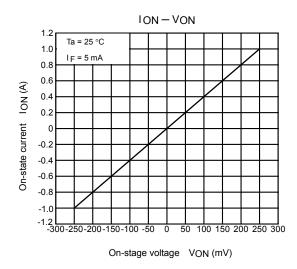


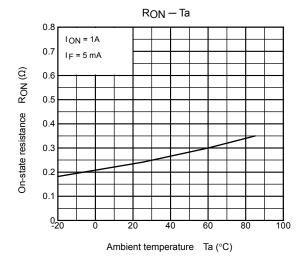


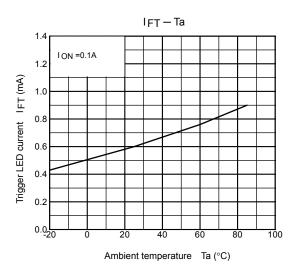




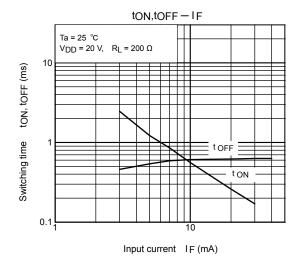


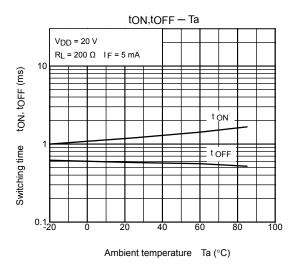


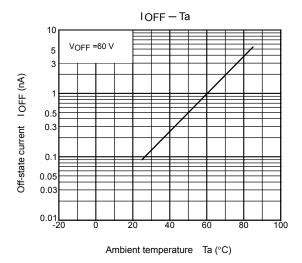




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