TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRANSISTOR

TLP291-4

Programmable Controllers Power Supplies

The Toshiba TLP291-4 consists of photo transistor, optically coupled to a gallium arsenide infrared emitting diode. TLP291-4 is housed in the SO16 package, very small and thin coupler.

Since TLP291-4 are guaranteed wide operating temperature (Ta=-55 to 110 °C), it's suitable for high-density surface mounting applications such as programmable controllers and hybrid ICs.

Collector-Emitter Voltage : 80 V (min)
 Current Transfer Ratio : 50% (min)
 Rank GB : 100% (min)
 Isolation Voltage : 2500 Vrms (min)
 Guaranteed performance over -55 to 110 °C

UL approved : UL1577 , File No. E67349

cUL approved : CSA Component Acceptance Service

No.5A

Option (V4) type

VDE approved : EN60747-5-2

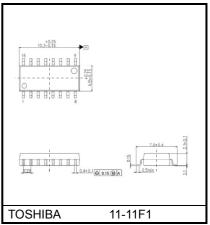
(Note): When a EN60747-5-2 approved type is needed,

Please designate "Option(V4)"

Construction mechanical rating

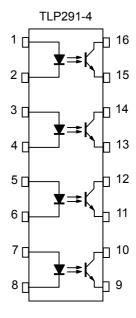
Creepage distance : 5.0 mm(min)
Clearance : 5.0 mm(min)
Insulation thickness : 0.1 mm(min)

Unit in mm



Weight: 0.19 g (typ.)

Pin Configuration



1,3,5,7 :ANODE 2,4,6,8 :CATHODE 9,11,13,15 :EMITTER 10,12,14,16 :COLLECTOR

Current Transfer Ratio

| | Classification | | sfer Ratio (%) / I _F) | |
|-----------|----------------|---|--------------------------------------|---------------------------|
| TYPE | (Note1) | I _F = 5 mA, V _{CE} = 5 V, Ta = 25°C | | Marking of Classification |
| | | Min | Max | |
| TLP291-4 | Blank | 50 | 400 | Blank |
| 121 201 4 | Rank GB | 100 | 400 | GB |

Note1: ex. Rank GB: TLP291-4 (GB)

Application type name for certification test, please use standard product type name, i.e.

TLP291-4 (GB,E: TLP291-4

2

Absolute Maximum Ratings (Ta = 25°C)

| CHARACTERISTIC | | SYMBOL | RATING | UNIT |
|---|--|---------------------|-----------------|--------|
| | Forward Current | I _{F(RMS)} | 50 | mA |
| | Forward Current Derating | ΔI _F /°C | −0.67 (Ta≥50°C) | mA /°C |
| ED | Pulse Forward Current (Note2) | IFP | 1 | Α |
| | Reverse Voltage | V _R | 5 | V |
| | Junction Temperature | Tj | 125 | °C |
| | Collector-Emitter Voltage | V _{CEO} | 80 | V |
| | Emitter-Collector Voltage | V _{ECO} | 7 | V |
| OR | Collector Current | IC | 50 | mA |
| DETECTOR | Collector Power Dissipation (1 Circuit) | PC | 100 | mW |
| | Collector Power Dissipation Derating(Ta≥25°C) (1 Circuit) | ΔP _C /°C | -1.0 | mW /°C |
| | Junction Temperature | Tj | 125 | °C |
| Оре | erating Temperature Range | T _{opr} | −55 to 110 | °C |
| Storage Temperature Range | | T _{stg} | −55 to 125 | °C |
| Lead Soldering Temperature | | T _{sol} | 260 (10s) | °C |
| Total Package Power Dissipation (1 Circuit) | | P _T | 170 | mW |
| Total Package Power Dissipation Derating (Ta≥25°C) (1 Circuit) | | ΔP _T /°C | -1.7 | mW /°C |
| Isolation Voltage (Note3) | | BVS | 2500 | Vrms |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width \leq 100 μ s, frequency 100Hz

Note3: AC, 1 minute, R.H.≤60%, Device considered a two terminal device : LED side pins shorted together and DETECTOR side pins shorted together.

Individual Electrical Characteristics (Ta = 25°C)

| CHARACTERISTIC | | SYMBOL | TEST CONDITION | MIN | TYP. | MAX | UNIT |
|----------------|-------------------------------------|-----------------------|-----------------------------------|-----|------|-----|------|
| | Forward Voltage | V _F | I _F = 10 mA | 1.1 | 1.20 | 1.4 | V |
| LED | Reverse Current | I _R | V _R = 5 V | - | _ | 10 | μA |
| | Capacitance | C _T | V = 0, f = 1 MHz | _ | 30 | _ | pF |
| | Collector-Emitter Breakdown Voltage | V _(BR) CEO | I _C = 0.5 mA | 80 | _ | _ | V |
| OR | Emitter-Collector Breakdown Voltage | V _{(BR) ECO} | I _E = 0.1 mA | 7 | _ | _ | V |
| ECT | Collector Dark Current | lone | V _{CE} = 48 V, | _ | 0.01 | 0.1 | μA |
| DET | (Note5) | ICEO | V _{CE} = 48 V, Ta = 85°C | _ | 2 | 50 | μΑ |
| | Capacitance (Collector to Emitter) | C _{CE} | V = 0, f = 1 MHz | | 10 | _ | pF |

Coupled Electrical Characteristics (Ta = 25°C)

| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN | TYP. | MAX | UNIT |
|--------------------------------------|---------------------------------------|--|-----|------|-----|------|
| Current Transfer Ratio | I _C / I _F | I _F = 5 mA, V _{CE} = 5 V | 50 | _ | 400 | % |
| Current Transfer Natio | IC / IF | Rank GB | 100 | _ | 400 | /0 |
| Saturated CTR | I _C / I _{F (sat)} | I _F = 1 mA, V _{CE} = 0.4 V | 1 | 60 | _ | % |
| Saturated CTR | | Rank GB | 30 | _ | _ | /0 |
| | | I _C = 2.4 mA, I _F = 8 mA | 1 | _ | 0.4 | |
| Collector-Emitter Saturation Voltage | V _{CE} (sat) | I _C = 0.2 mA, I _F = 1 mA | _ | 0.2 | _ | V |
| | | Rank GB | _ | _ | 0.4 | |
| Off-State Collector Current | I _{C (off)} | V _F = 0.7 V, V _{CE} = 48 V | _ | _ | 10 | μA |

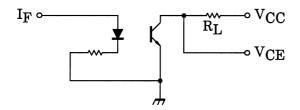
Isolation Characteristics (Ta = 25°C)

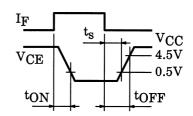
| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN | TYP. | MAX | UNIT |
|----------------------------------|----------------|----------------------------------|--------------------|------------------|-----|--------|
| Capacitance (Input to Output) | CS | V _S = 0 V, f = 1 MHz | _ | 0.8 | 1 | pF |
| Isolation Resistance | R _S | V _S = 500 V, R.H.≤60% | 1×10 ¹² | 10 ¹⁴ | - | Ω |
| | | AC , 1 minute | 2500 | _ | _ | Vrms |
| Isolation Voltage | BV_S | AC , 1 second, in OIL | _ | 5000 | _ | VIIIIS |
| | | DC , 1 minute, in OIL | _ | 5000 | _ | Vdc |

Switching Characteristics (Ta = 25°C)

| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN | TYP. | MAX | UNIT |
|----------------|------------------|--|-----|------|-----|------|
| Rise Time | tr | | _ | 2 | _ | |
| Fall Time | t _f | V _{CC} = 10 V, I _C = 2 mA | _ | 3 | _ | Ms |
| Turn-On Time | ton | $R_L = 100\Omega$ | _ | 3 | _ | IVIO |
| Turn-Off Time | t _{off} | | 1 | 3 | _ | |
| Turn-On Time | ton | | _ | 2 | _ | |
| Storage Time | ts | $R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V}, I_F = 16 \text{ mA}$ | | 25 | _ | Ms |
| Turn-Off Time | tOFF | | _ | 40 | _ | |

(Fig.1) Switchin Time Test Circuit





Soldering and Storage

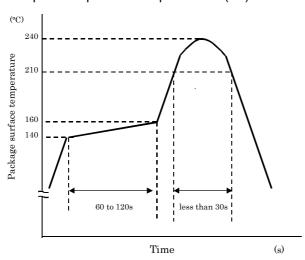
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

1) Using solder reflow

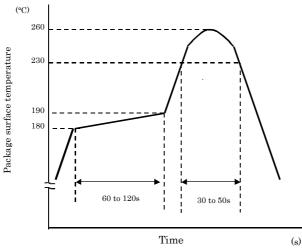
·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

- 2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)
 - Please preheat it at 150°C between 60 and 120 seconds.
 - · Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

Option:Specification for Embossed-Tape Packing (TP) for Mini-Flat Coupler

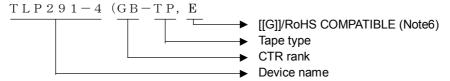
1. Applicable Package

| Package Name | Product Type |
|--------------|-------------------|
| SO16 | Mini-Flat Coupler |

2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

(Example)



3. Tape Dimensions

3.1 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.

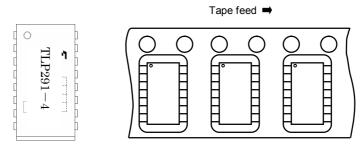


Figure 1 Device Orientation

- 3.2 Tape Packing Quantity: 2000 devices per reel
- 3.3 Empty Device Recesses are as Shown in Table 1.

Table 1 Empty Device Recesses

| | Standard | Remarks | |
|---|-------------------------|--|--|
| Occurrences of 2 or more successive empty device recesses | 0 | Within any given 40-mm section of tape, not including leader and trailer | |
| Single empty device recesses | 6 device (max) per reel | Not including leader and trailer | |

3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 2 and table 2.

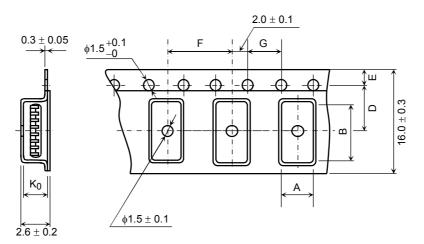


Figure 2 Tape Forms

Table2 Tape Dimensions

Unit: mm Unless otherwise specified: ±0.1

| Symbol | Dimension | Remark |
|----------------|-----------|---|
| Α | 7.5 | _ |
| В | 10.5 | _ |
| D | 7.5 | Center line of indented square hole and sprocket hole |
| E | 1.75 | Distance between tape edge and hole center |
| F | 12.0 | Cumulative error +0.1 (max) per 10 feed holes |
| G | 4.0 | Cumulative error +0.1 (max) per 10 feed holes |
| Κ ₀ | 2.2 | Internal space |

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3.6 Reel

- (1) Material: Plastic
- (2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 3.

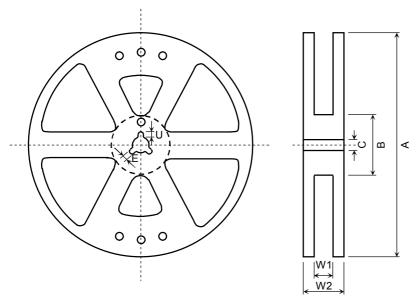


Table 3 Reel Dimensions

Unit: mm

| Symbol | Dimension | |
|---------------|-------------------|--|
| Α | $\phi 330 \pm 2$ | |
| В | φ80 ± 1 | |
| С | $\phi 13 \pm 0.5$ | |
| E | 2.0 ± 0.5 | |
| U | 4.0 ± 0.5 | |
| W1 17.5 ± 0.5 | | |
| W2 21.5 ± 1.0 | | |

Figure 3 Reel Forms

4. Packing

Either one reel or five reels of photocouplers are packed in a shipping carton.

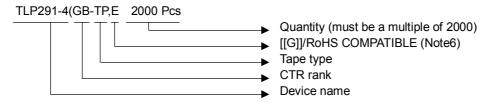
5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

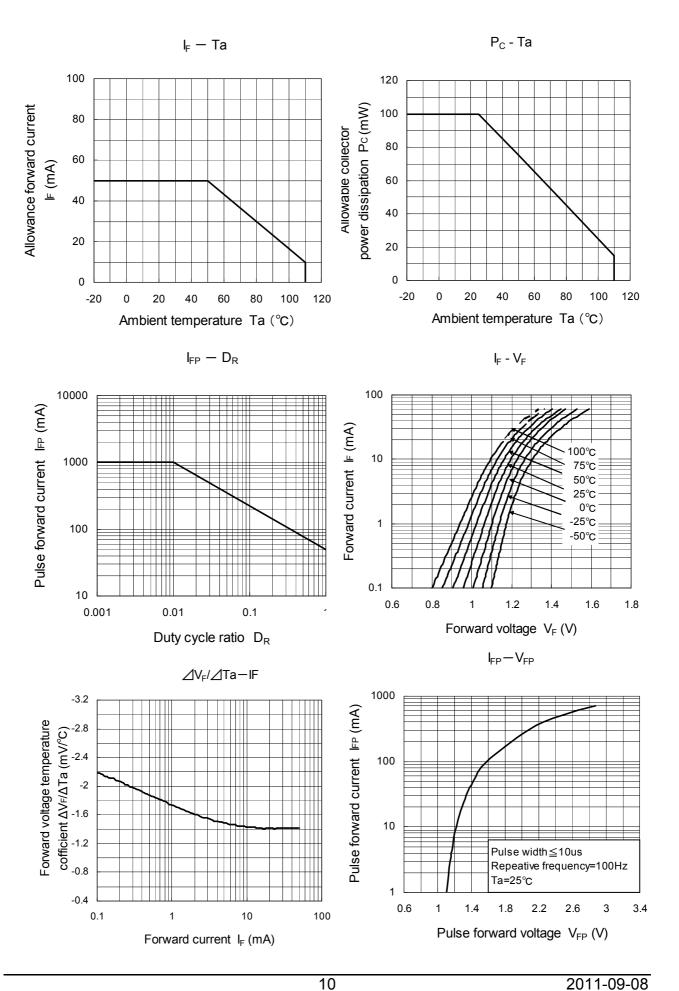
When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.

(Example)



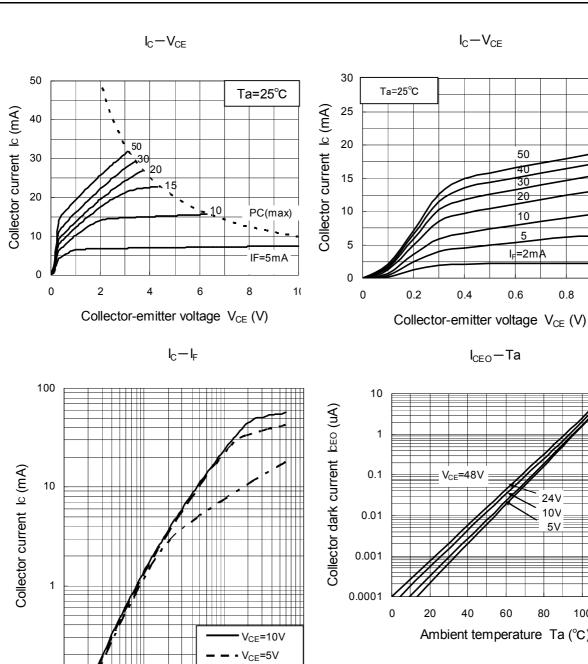
Note6:Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.



0.1

0.1

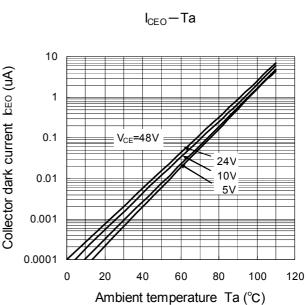


V_{CE}=0.4V

10

Forward current I_F (mA)

100



0.4

 $I_{C} - V_{CE}$

50

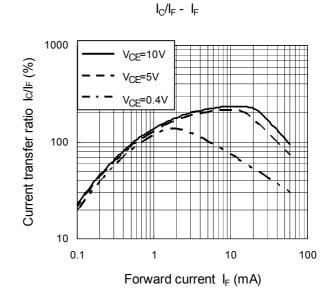
20

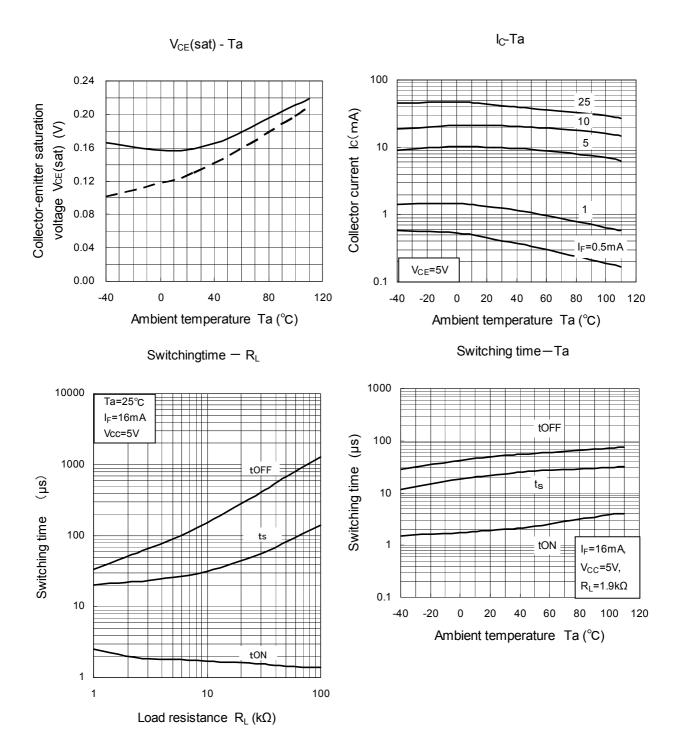
10

5

I_F=2mA

0.6





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