

MOSFETs Silicon N-channel MOS (U-MOS IV)

TK80E06K3A

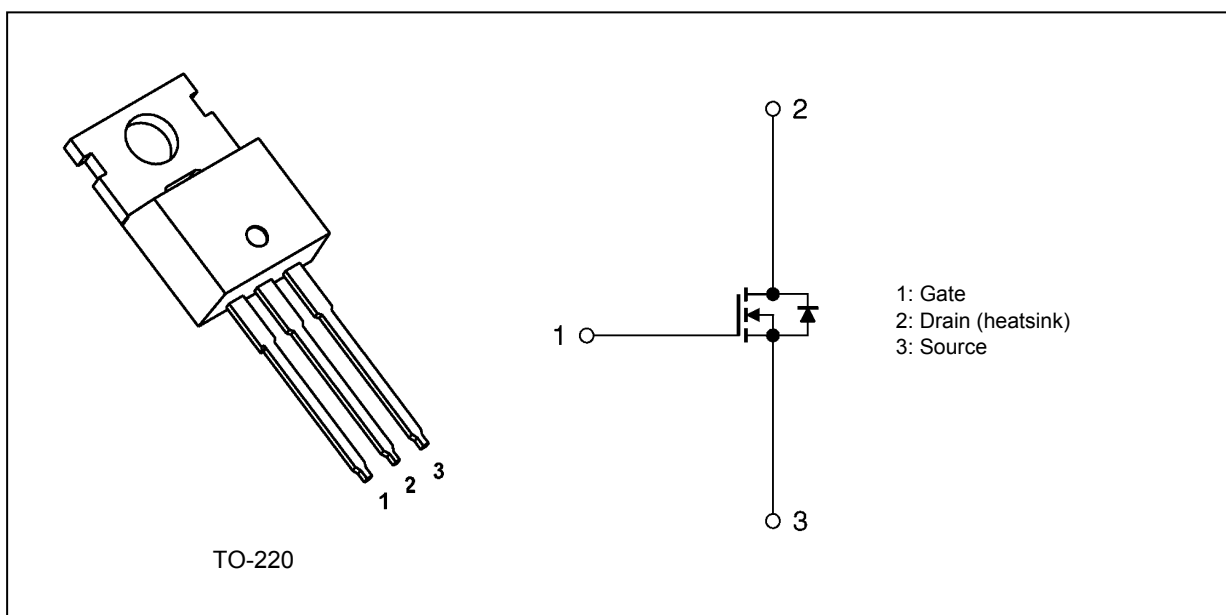
1. Applications

- Switching Voltage Regulators

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 4.8 \text{ m}\Omega$ (typ.)
- (2) Low leakage current: $I_{DSS} = 10 \text{ }\mu\text{A}$ (max) ($V_{DS} = 60 \text{ V}$)
- (3) Enhancement mode: $V_{th} = 3.0 \text{ to } 4.0 \text{ V}$ ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	60	V
Gate-source voltage	V_{GSS}	± 20	
Drain current (DC)	I_D	80	A
Drain current (pulsed)	I_{DP}	240	
Power dissipation	P_D	125	W
Single-pulse avalanche energy	E_{AS}	76	mJ
Avalanche current	I_{AR}	80	A
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	$R_{th(ch-c)}$	1.00	°C/W
Channel-to-ambient thermal resistance	$R_{th(ch-a)}$	83.3	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25\text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 16.3\ \mu\text{H}$, $R_G = 1.2\ \Omega$, $I_{AR} = 80\ \text{A}$

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

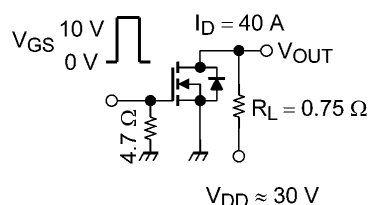
6. Electrical Characteristics

6.1. Static Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I_{GSS}	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	—	—	± 1	μA
Drain cut-off current	I_{DSS}	$V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}$	—	—	10	
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D = 10\text{ mA}, V_{GS} = 0\text{ V}$	60	—	—	V
	$V_{(BR)DSX}$	$I_D = 10\text{ mA}, V_{GS} = -20\text{ V}$	35	—	—	
Gate threshold voltage	V_{th}	$V_{DS} = 10\text{ V}, I_D = 1\text{ mA}$	3.0	—	4.0	
Drain-source on-resistance	$R_{DS(ON)}$	$V_{GS} = 10\text{ V}, I_D = 40\text{ A}$	—	4.8	5.8	$\text{m}\Omega$

6.2. Dynamic Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Input capacitance	C_{iss}	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	—	4500	—	μF
Reverse transfer capacitance	C_{rss}		—	600	—	
Output capacitance	C_{oss}		—	800	—	
Switching time (rise time)	t_r	See Figure 6.2.1.	—	26	—	ns
Switching time (turn-on time)	t_{on}		—	56	—	
Switching time (fall time)	t_f		—	26	—	
Switching time (turn-off time)	t_{off}		—	74	—	



Duty $\leq 1\%$, $t_w = 10\ \mu\text{s}$

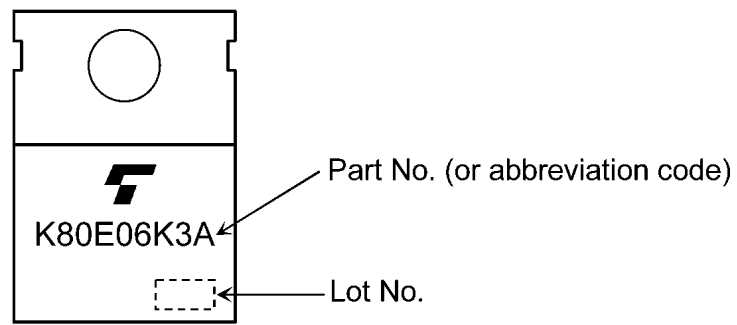
Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Q_g	$V_{DD} \approx 48\text{ V}, V_{GS} = 10\text{ V}, I_D = 80\text{ A}$	—	90	—	nC
Gate-source charge 1	Q_{gs1}		—	27	—	
Gate-drain charge	Q_{gd}		—	37	—	

6.4. Source-Drain Characteristics ($T_a = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Reverse drain current (DC) (Note 1)	I_{DR}	—	—	—	80	A
Reverse drain current (pulsed) (Note 1)	I_{DRP}	—	—	—	240	
Diode forward voltage	V_{DSF}	$I_{DR} = 80\text{ A}, V_{GS} = 0\text{ V}$	—	—	-1.2	V
Reverse recovery time	t_{rr}	$I_{DR} = 80\text{ A}, V_{GS} = 0\text{ V}$ $-dI_{DR}/dt = 100\text{ A}/\mu\text{s}$	—	44	—	ns
Reverse recovery charge	Q_{rr}		—	66	—	nC

7. Marking**Fig. 7.1 Marking**

8. Characteristics Curves (Note)

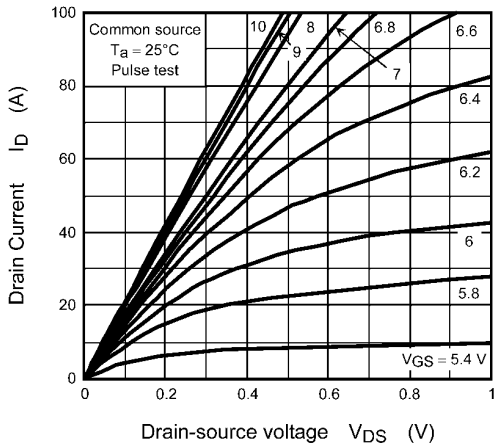


Fig. 8.1 $I_D - V_{DS}$

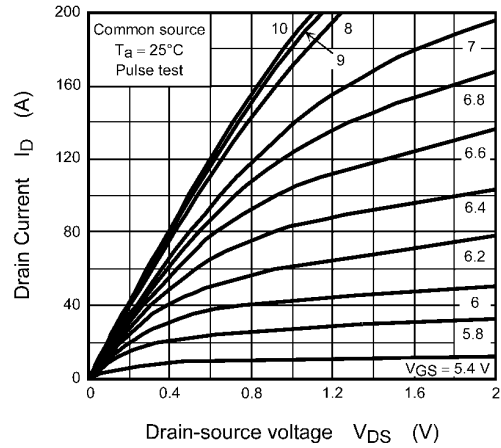


Fig. 8.2 $I_D - V_{DS}$

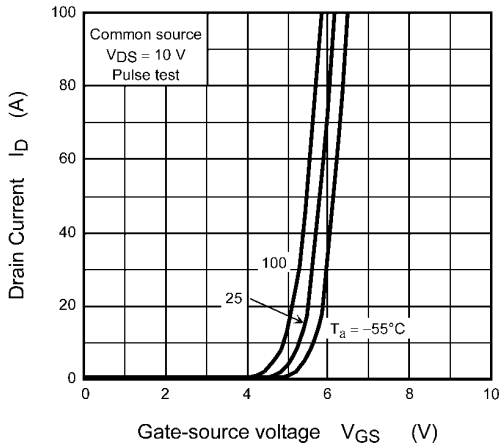


Fig. 8.3 $I_D - V_{GS}$

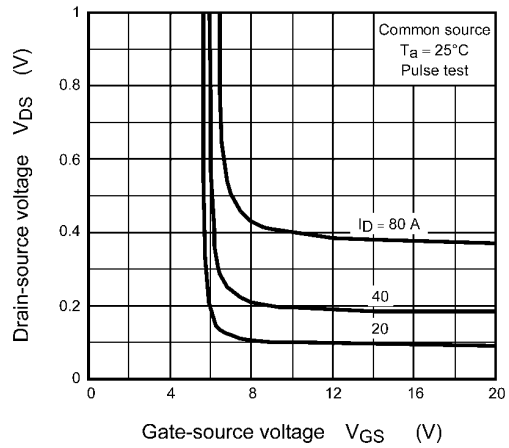


Fig. 8.4 $V_{DS} - V_{GS}$

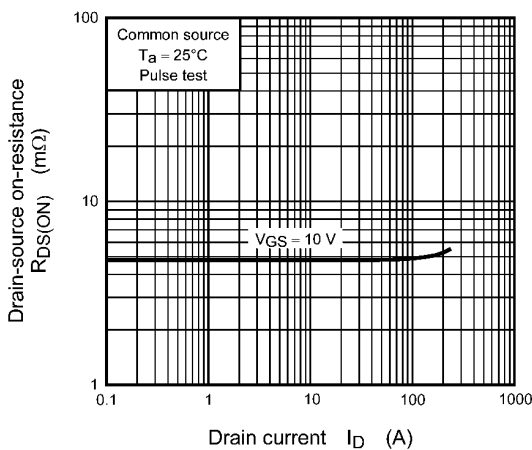


Fig. 8.5 $R_{DS(ON)} - I_D$

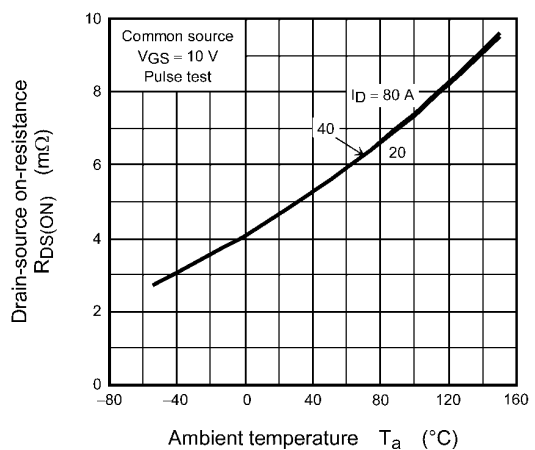


Fig. 8.6 $R_{DS(ON)} - T_a$

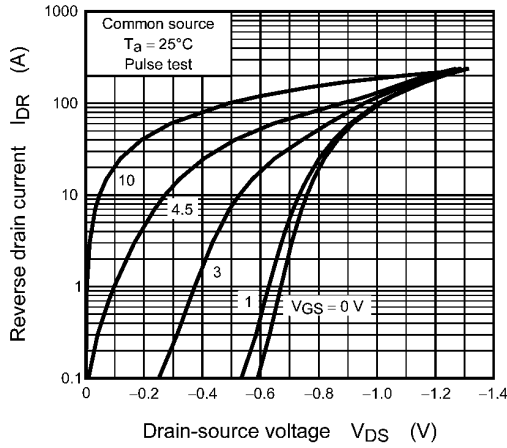


Fig. 8.7 $I_{DR} - V_{DS}$

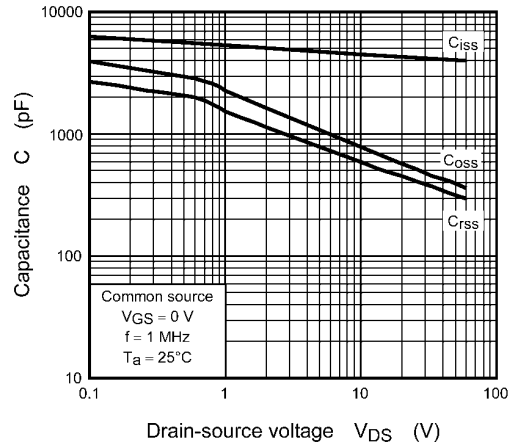


Fig. 8.8 $C - V_{DS}$

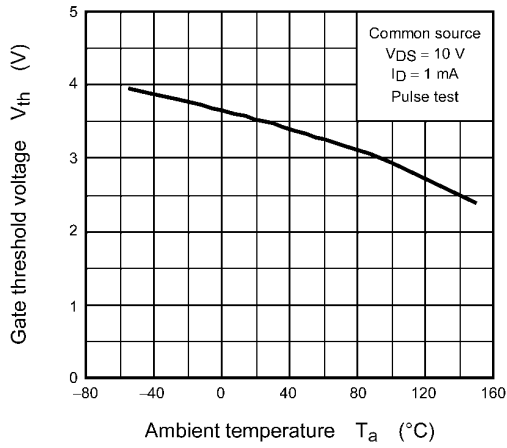


Fig. 8.9 $V_{th} - T_a$

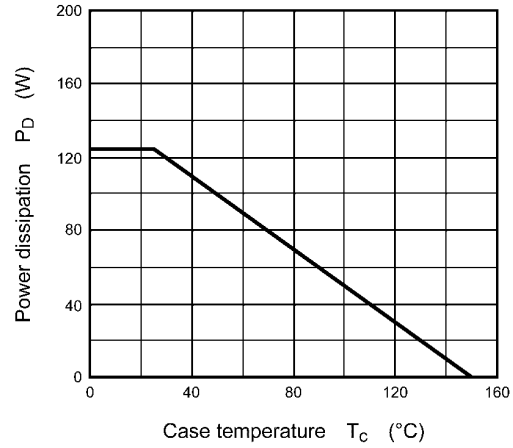


Fig. 8.10 $P_D - T_c$
 (Guaranteed Maximum)

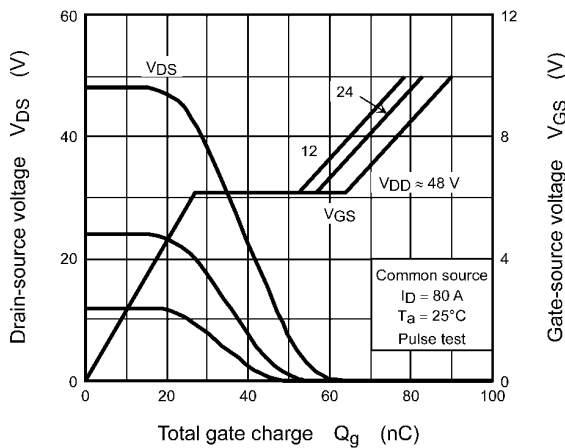
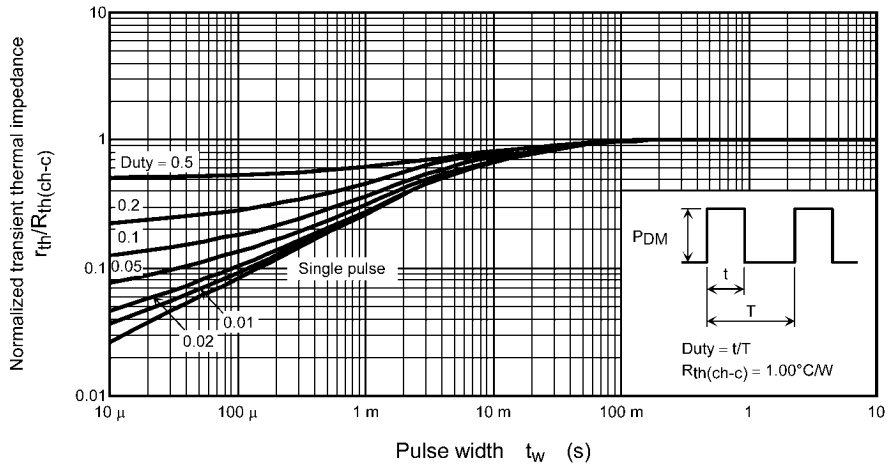
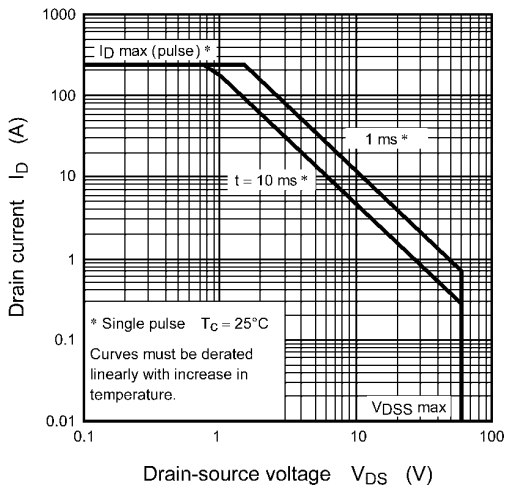


Fig. 8.11 Dynamic Input/Output Characteristics



**Fig. 8.12 $r_{th}/R_{th(ch-c)} - t_w$
(Guaranteed Maximum)**

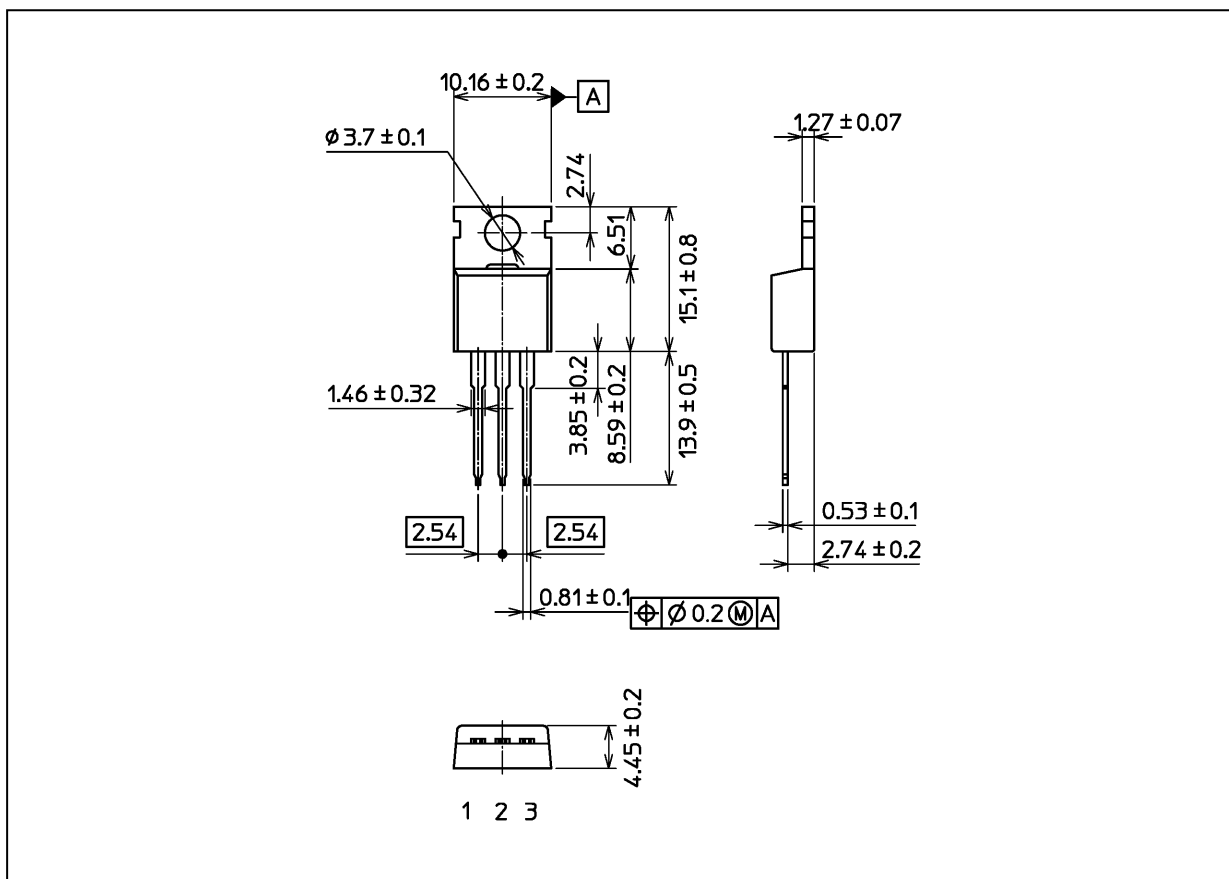


**Fig. 8.13 Safe Operating Area
(Guaranteed Maximum)**

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

Unit: mm



Weight: 1.93 g (typ.)

Package Name(s)
TOSHIBA: 2-10X1A
Nickname: TO-220

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