

• FEATURES

# iscN-Channel MOSFET Transistor

## TK5P60W

#### Low drain-source on-resistance: $R_{DS}(ON) = 0.77\Omega$ (typ.) · Easy to control Gate switching · Enhancement mode: Vth = 2.7 to 3.7V (VDs = 10 V, ID=0.27mA) 100% avalanche tested pin 1.Gate · Minimum Lot-to-Lot variations for robust device 2.Drain performance and reliable operation 3. Source DPAK TO-252 package DESCRITION Switching Voltage Regulators C 0.5 K ABSOLUTE MAXIMUM RATINGS(Ta=25°C) С UNIT SYMBOL PARAMETER VALUE D Drain-Source Voltage 600 V VDSS V<sub>GS</sub> Gate-Source Voltage $\pm 30$ V 3.0 $I_D$ Drain Current-Continuous 5.4 А F Drain Current-Single Pulsed 21.6 A **I**DM G H H $\mathbf{P}_{\mathsf{D}}$ Total Dissipation @Tc=25°C 30 W mm 150 °C Tj Max. Operating Junction Temperature MIN MAX DIM 6.40 6.60 A 5.20 5.40 B Storage Temperature -55~150 °C Tstg 1.15 1.35 5.70 6.10 D 0.65 THERMAL CHARACTERISTICS 0 75 G 2.10 н 2 50 SYMBOL PARAMETER MAX UNIT 2.10 2.40 K 0.40 0.60 0.90 Channel-to-case thermal resistance 1.10 °C/W Rth(ch-c) 4.17 9.90 0 10.

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# **TK5P60W**

### **ELECTRICAL CHARACTERISTICS**

#### $T_{\texttt{C}}\text{=}25^{\circ}\!\!\!\mathbb{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	МАХ	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 10mA	600			V
V <sub>GS</sub> (th)	Gate Threshold Voltage	V <sub>DS</sub> = 10V; I <sub>D</sub> =0.27mA	2.7		3.7	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =2.7A		770	900	mΩ
lgss	Gate-Source Leakage Current	V <sub>GS</sub> = ±30V;V <sub>DS</sub> =0V			±1	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 600V; V <sub>GS</sub> = 0V			10	μA
VSDF	Diode forward voltage	I <sub>DR</sub> =5.4A, V <sub>GS</sub> = 0 V			1.7	V



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