TOSHIBA

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX14F,TC74LCX14FN,TC74LCX14FT,TC74LCX14FK

Low-Voltage Hex Schmitt Inverter with 5-V Tolerant Inputs and Outputs

The TC74LCX14 is a high-performance CMOS schmitt inverter. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

Pin configuration and function are the same as the TC74LCX04 but the inputs have hysteresis and with Schmitt trigger function, the TC74LCX14F/FN/FT can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge.

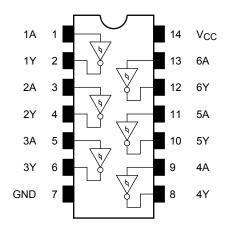
Features

- Low-voltage operation: V_{CC} = 2.0 to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns} (\text{max}) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$ •
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA} (\min) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: -500 mA
- Available in JEDEC SOP, JEITA SOP, TSSOP and VSSOP (US)
- · Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 14 type

Japan. TC74LCX14F SOP14-P-300-1.27A TC74LCX14FN SOL14-P-150-1.27 TC74LCX14FT TSSOP14-P-0044-0.65A TC74LCX14FK VSSOP14-P-0030-0.50 Weight SOP14-P-300-1.27A : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Note: xxxFN (JEDEC SOP) is not available in

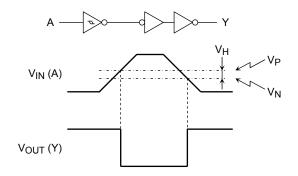
Pin Assignment (top view)



Truth Table

Inputs	Outputs
А	Y
L	Н
Н	L

System Diagram and waveform



IEC Logic Symbol

1A (1)	Л	(2) (1) 1Y
$2A - \frac{(3)}{(5)}$		(4) (6) 2Y
3A - (9)		(8) 3Y
4A - (11) = 5A - (12)		(10) 4Y
6A (13)		(12) 6Y

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	–0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	I _{IK}	-50	mA
Output diode current	IOK	±50 (Note 4)	mA
DC output current	IOUT	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 2: $V_{CC} = 0 V$
- Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	2.0 to 3.6	V
Tower supply voltage	vcc	1.5 to 3.6 (Note 2)	v
Input voltage	V _{IN}	0 to 5.5	V
	V _{OUT}	0 to 5.5 (Note 3)	V
Output voltage		0 to V_{CC} (Note 4)	v
Output ourroat	leu/leu	±24 (Note 5)	mA
Output current	IOH/IOL	±12 (Note 6)	ША
Operating temperature	T _{opr}	-40 to 85	°C

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

- Note 3: $V_{CC} = 0 V$
- Note 4: High or low state
- Note 5: $V_{CC} = 3.0$ to 3.6 V
- Note 6: $V_{CC} = 2.7$ to 3.0 V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristics		Symbol	Test Condition			Min	Мах	Unit
Characteria	51105	Gymbol	1031	Condition	V _{CC} (V)	IVIIII	IVIAA	Offic
Threshold voltage	H-level	VP		—	3.0	1.2	2.2	v
Threshold voltage	L-level	V _N		_	3.0	0.6	1.5	v
Hysteresis voltage	•	V _H		_	3.0	0.4	1.2	V
			I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_		
	H-level	VOH	$V_{IN} = V_{IL}$	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_	V
				I _{OH} = -18 mA	3.0	2.4		
Output voltage	Output voltage			I _{OH} = -24 mA	3.0	2.2	_	
-			VIN = VIH	$I_{OL} = 100 \ \mu A$	2.7 to 3.6	_	0.2	
	L-level	Mai		$I_{OL} = 12 \text{ mA}$	2.7	_	0.4	
	L-level	V _{OL}		$I_{OL} = 16 \text{ mA}$	3.0	_	0.4	
				$I_{OL} = 24 \text{ mA}$	3.0	_	0.55	
Input leakage curren	nt	I _{IN}	$V_{IN} = 0$ to 5.5 V		2.7 to 3.6	_	±5.0	μA
Power-off leakage of	urrent	I _{OFF}	$V_{IN}/V_{OUT} = 5.5 V$		0	_	10.0	μA
Quiescent supply current		$V_{IN} = V_{CC}$ or GND		2.7 to 3.6		10.0		
	Icc	$V_{IN} = 3.6$ to 5.5 V		2.7 to 3.6		±10.0	μA	
Increase in Icc per i	nput	Δlcc	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6		500	

AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7	_	7.5	ns
	t _{pHL}		$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.5	115
Output to output skew	t _{osLH}	(Note)	2.7	_	_	-
	t _{osHL}		$\textbf{3.3}\pm\textbf{0.3}$		1.0	ns

Note: Parameter guaranteed by design. $(t_{osLH} = |t_{pLHm} - t_{pLHn}|, \ t_{osHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V_{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

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Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	—	3.3	7	pF
Output capacitance	C _{OUT}	_	0	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (Note	e) 3.3	25	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per gate)

AC Test Circuit

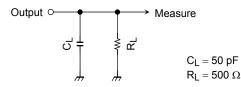


Figure 1

AC Waveform

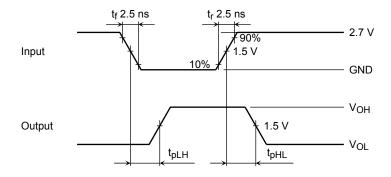


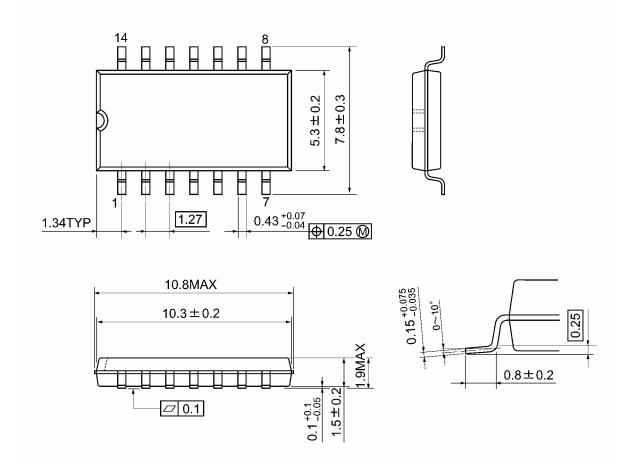
Figure 2 t_{pLH}, t_{pHL}



Package Dimensions

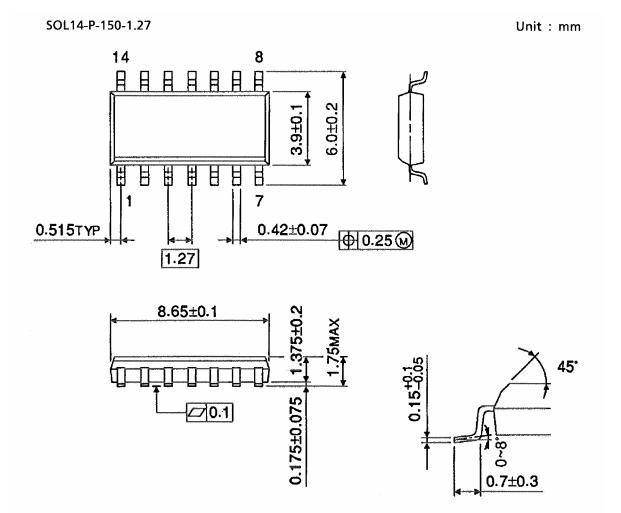
SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions (Note)



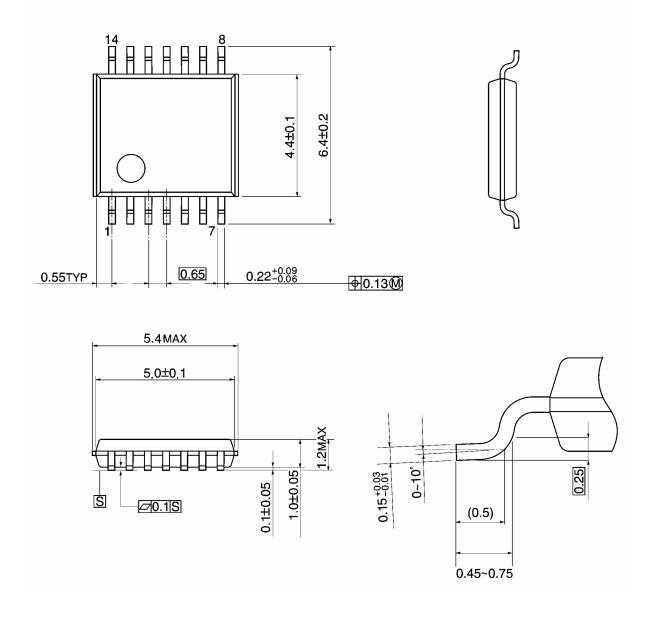
Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



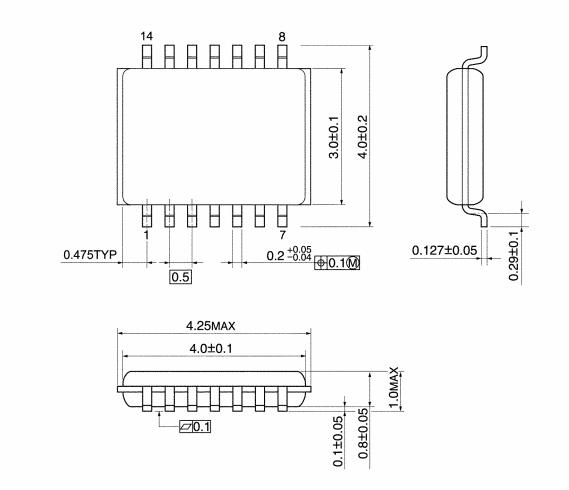
Weight: 0.06 g (typ.)

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Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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