TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX125F,TC74LCX125FN,TC74LCX125FT,TC74LCX125FK

Low-Voltage Quad Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX125 is a high-performance CMOS quad bus buffers. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

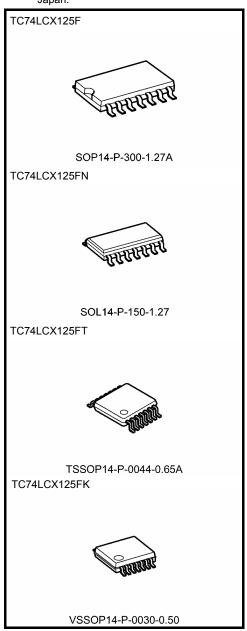
This device requires the 3-state control input \overline{OE} to be set high to place the output into the high impedance state.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: $V_{CC} = 2.0$ to 3.6 V
- High-speed operation: $t_{pd} = 6.0 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min) (V}_{CC} = 3.0 \text{ V)}$
- Latch-up performance: -500 mA
- Available in JEDEC SOP, JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection is provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 125 type

Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

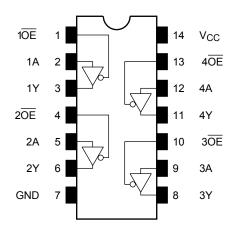
 SOP14-P-300-1.27A
 : 0.18 g (typ.)

 SOL14-P-150-1.27
 : 0.12 g (typ.)

 TSSOP14-P-0044-0.65A
 : 0.06 g (typ.)

 VSSOP14-P-0030-0.50
 : 0.02 g (typ.)

Pin Assignment (top view)



IEC Logic Symbol

1 OE 1 N	EN	\triangleright	∇	3 1Y
2 OE 4 N				6 2Y
3 OE 10 N				8 3Y
4 OE 13 AA 12				11 4Y

Truth Table

Inp	uts	Outputs
ŌĒ	Α	Y
Н	Х	Z
L	L	L
L	Н	Н

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	lικ	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating range (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Output in OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$



Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Davias avantuvalta sa	V _{CC}	2.0 to 3.6	V	
Power supply voltage	v CC	1.5 to 3.6 (Note 2)	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	Vout	0 to 5.5 (Note 3)	V	
		0 to V _{CC} (Note 4)	V	
Output current	I _{OH} /I _{OL}	±24 (Note 5)	mA	
Output current	IOH/IOL	±12 (Note 6)	ША	
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

Note 3: Output in OFF state

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 6: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$

Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Character	istics	Symbol	Test Condition $V_{CC}\left(V\right)$		Min	Max	Unit	
					V _{CC} (V)			
Input voltage	H-level	V _{IH}		_	2.7 to 3.6	2.0	_	V
input voitage	L-level	V _{IL}		_	2.7 to 3.6	_	0.8	٧
				I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_	
	H-level	VoH	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -12 mA	2.7	2.2	_	
				I _{OH} = -18 mA	3.0	2.4	_	
Output voltage				I _{OH} = -24 mA	3.0	2.2	_	V
,				I _{OL} = 100 μA	2.7 to 3.6	_	0.2	
.	\/-·	M M ===M	I _{OL} = 12 mA	2.7	_	0.4		
	L-level	V _{OL}		I _{OL} = 16 mA	3.0	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage curre	ent	I _{IN}	V _{IN} = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μА
2 state output OFF	-state output OFF state current		V _{IN} = V _{IH} or V _{IL}		2.7 to 3.6		15.0	
3-state output OFF	State current	loz	V _{OUT} = 0 to 5.5 V		2.7 10 3.6	_	±5.0	μΑ
Power-off leakage	current	loff	$V_{IN}/V_{OUT} = 5.5 V$		0	_	10.0	μА
Quiescent supply current I _{CC}		loo	V _{IN} = V _{CC} or GND		2.7 to 3.6	_	10.0	
Quiescent supply o	unent	Icc	V _{IN} /V _{OUT} = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μΑ
Increase in Icc per	input	Δlcc	V _{IH} = V _{CC} - 0.6 V		2.7 to 3.6	_	500	

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AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition	.,	Min	Max	Unit
			V _{CC} (V)			
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7		6.5	ns
Tropagation delay time	t _{pHL}	rigate 1, rigate 2	3.3 ± 0.3	1.5	6.0	
Output enable time	t _{pZL}	Figure 1, Figure 3	2.7		8.0	- ns
	t _{PZH}		3.3 ± 0.3	1.5	7.0	
Output disable time	t _{pLZ}	Figure 1, Figure 3	2.7		7.0	ns
Output disable time	t _{pHZ}		3.3 ± 0.3	1.5	6.0	115
Output to output skow	t _{osLH}	(Note)	2.7			ne
Output to output skew	t _{osHL}		3.3 ± 0.3	_	1.0	ns

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, \, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V_{OL}	V_{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V

Capacitive Characteristics (Ta = 25°C)

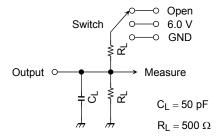
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_	3.3	7	pF
Output capacitance	C _{OUT}	_	3.3	8	pF
Power dissipation capacitance	C _{PD}	$f_{IN} = 10 \text{ MHz}$ (Note	3.3	25	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$

AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	6.0 V
t _{pHZ} , t _{pZH}	GND

Figure 1

AC Waveform

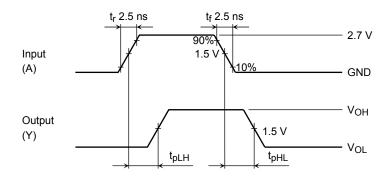


Figure 2 tpLH, tpHL

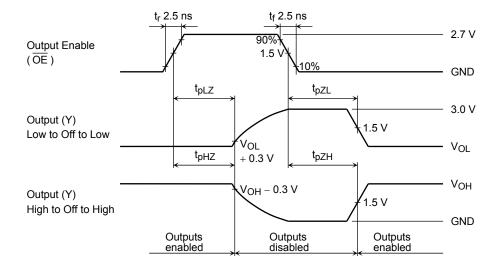
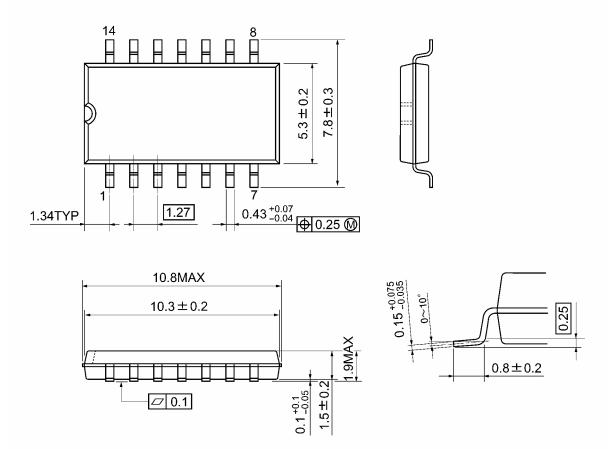


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

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Package Dimensions

SOP14-P-300-1.27A Unit: mm

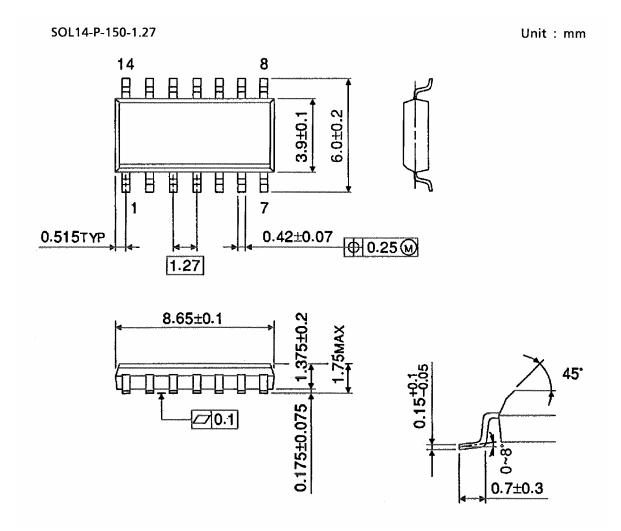


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Weight: 0.18 g (typ.)



Package Dimensions (Note)



Note: This package is not available in japan.

Weight: 0.12 g (typ.)

Package Dimensions

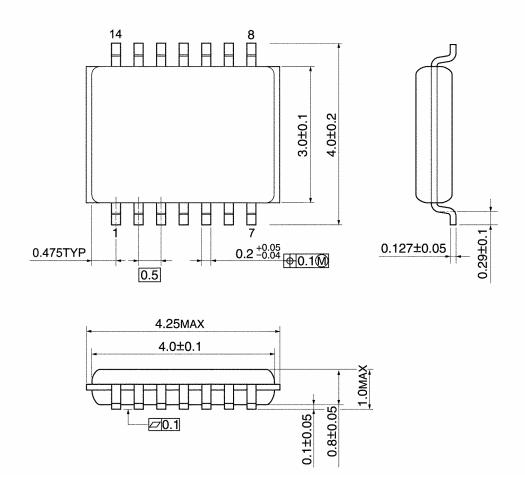
TSSOP14-P-0044-0.65A Unit: mm 6.4 ± 0.2 $0.22^{+0.09}_{-0.06}$ 0.65 0.55TYP **⊕**0.13**M** 5.4MAX 5.0±0.1 0~10 0.25 1.0±0.05 0.1±0.05 S Ø.1S (0.5)

Weight: 0.06 g (typ.)

0.45~0.75

Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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20070701-EN GENERAL

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