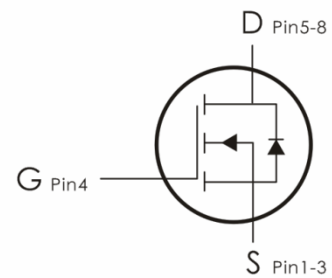
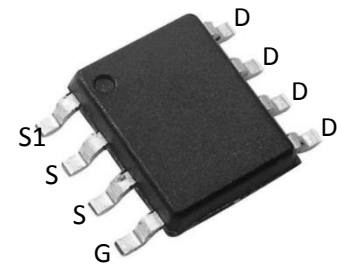


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=60V, I_D=8A, R_{DS(on)} < 20m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current - Continuous ($T_C=25^\circ\text{C}$)	8	A
	Drain Current - Continuous ($T_C=100^\circ\text{C}$)	5.6	
I_{DM}	Drain Current - Pulsed ¹	32	
P_D	Power Dissipation ($T_C=25^\circ\text{C}$)	2.1	W
	Power Dissipation - Derate above 25°C	---	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ\text{C}$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	---	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	60	

Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

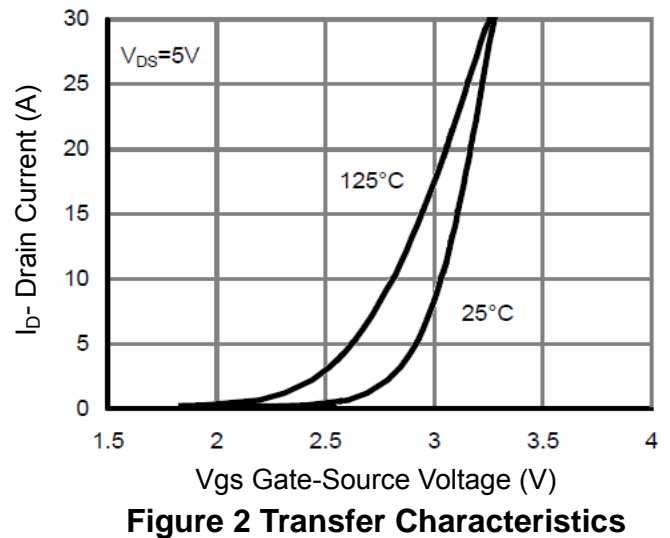
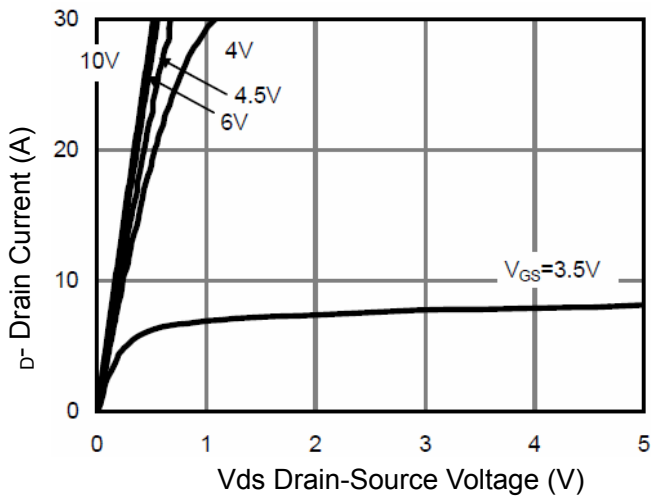
Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\ \mu\text{A}$	60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	---	---	V/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=30V, V_{GS}=0V,$ $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=0V, V_{GS}=0V,$ $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
On Characteristics						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\ \mu\text{A}$	1	1.6	2.2	V
$\Delta V_{GS(th)}$	VGS(th) Temperature Coefficient		---	---	---	mV/ $^\circ\text{C}$
$R_{DS(on)}$	Static Drain-Source On Resistance	$V_{GS}=10V, I_D=8A$	---	15.6	20	m Ω
		$V_{GS}=4.5V, I_D=8A$	---	20	28	
G_{FS}	Forward Transconductance	$V_{DS}=5V, I_D=8A$	18	---	---	S
Dynamic Characteristics⁴						
C_{iss}	Input Capacitance	$V_{DS}=30V, V_{GS}=0V, f=1\text{MHz}$	---	1600	---	pF
C_{oss}	Output Capacitance		---	112	---	
C_{rss}	Reverse Transfer Capacitance		---	98	---	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time ^{2,3}	$V_{DS}=30V, R_L=1\ \Omega$ $R_{GEN}=3\ \Omega, V_{GS}=10V$	---	---	---	ns
t_r	Rise Time ^{2,3}		---	5.5	---	ns
$t_{d(off)}$	Turn-Off Delay Time ^{2,3}		---	29	---	ns
t_f	Fall Time ^{2,3}		---	4.5	---	ns

Q_g	Total Gate Charge	V _{GS} =10V, V _{DS} =30V, I _D =8A	---	38.5	---	nC
Q_{gs}	Gate-Source Charge		---	4.7	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	10.3	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ³	V _{GS} =0V, I _S =8A, T _J =25°C	---	---	1.	V
I_S	Continuous Source Current	---	---	---	8	A
T_{rr}	Reverse Recovery Time	T _J = 25°C, I _F = 8A di/dt = 100A/μs ^(Note3)	---	28	---	Ns
Q_{rr}	Reverse Recovery Charge		---	40	---	Nc

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Characteristics: (T_c=25°C unless otherwise noted)



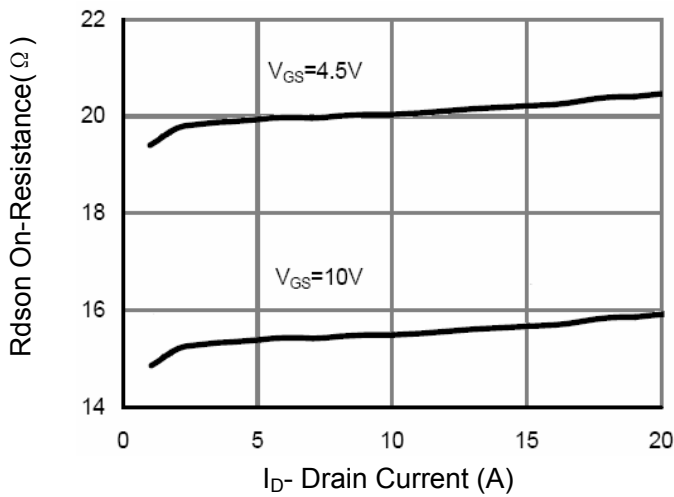


Figure 3 Rds(on)- Drain Current

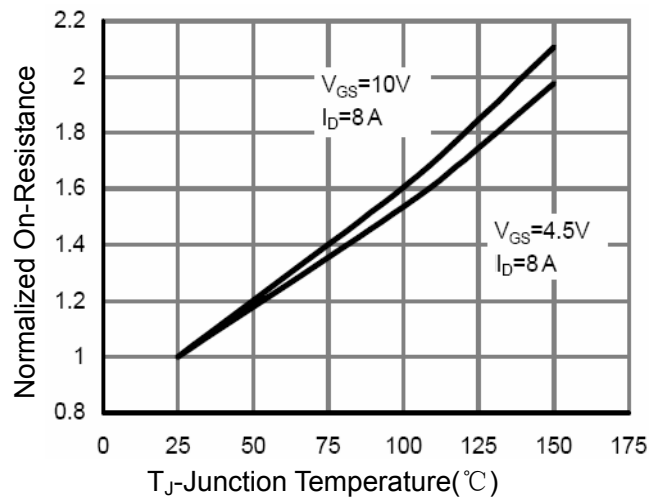


Figure 4 Rds(on)-Junction Temperature

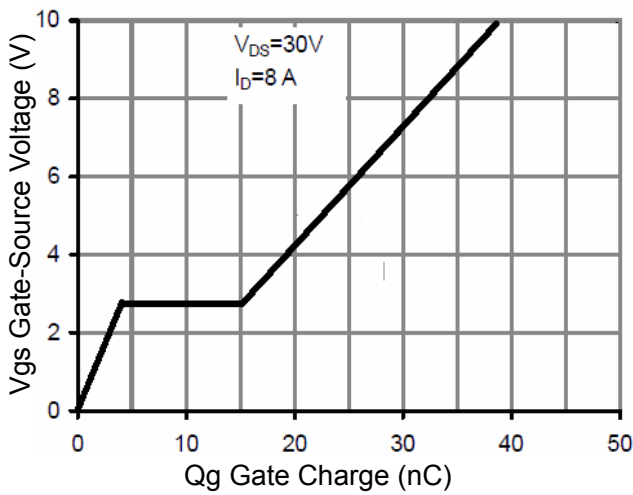


Figure 5 Gate Charge

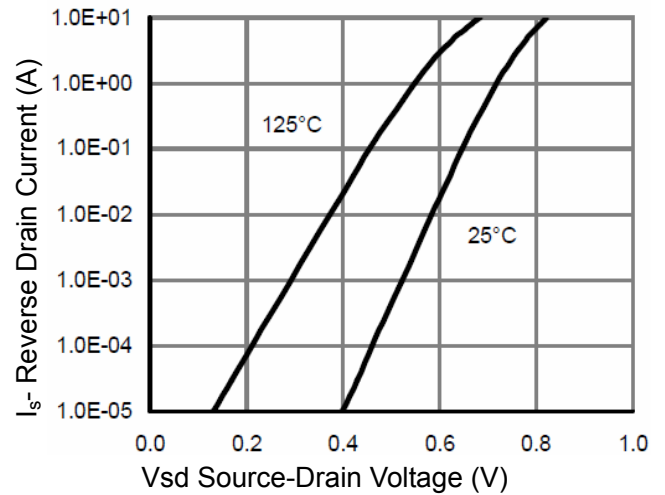


Figure 6 Source- Drain Diode Forward

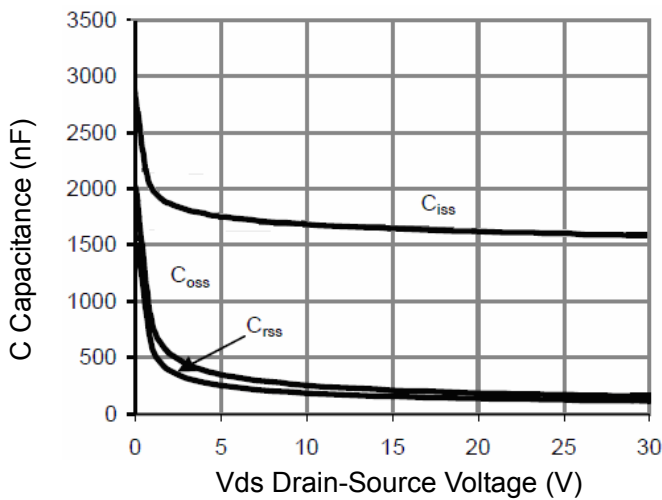


Figure 7 Capacitance vs Vds

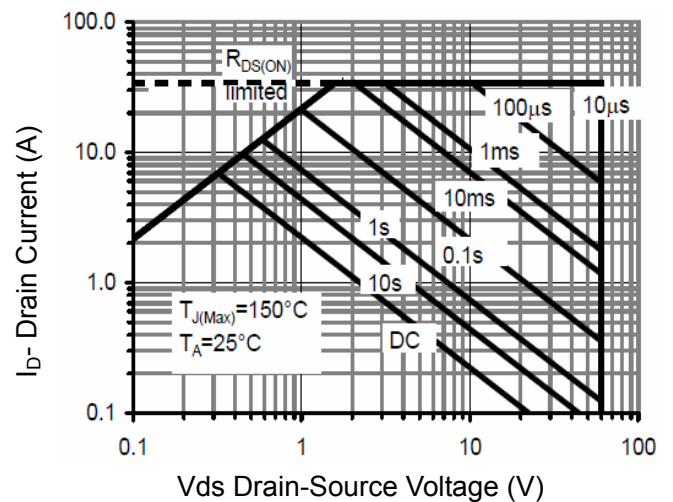


Figure 8 Safe Operation Area

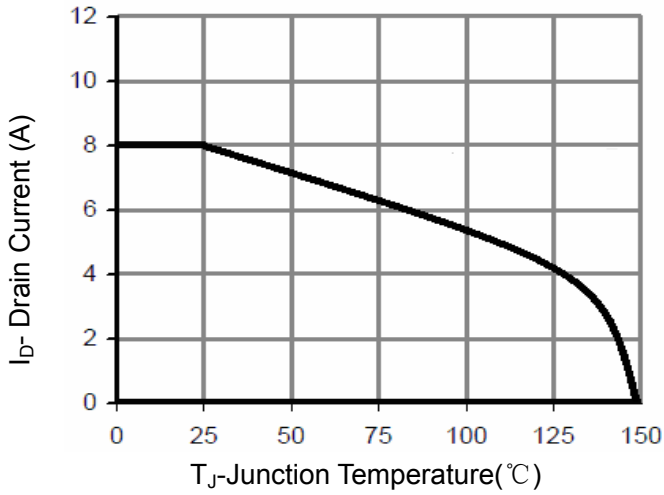


Figure 9 Current De-rating

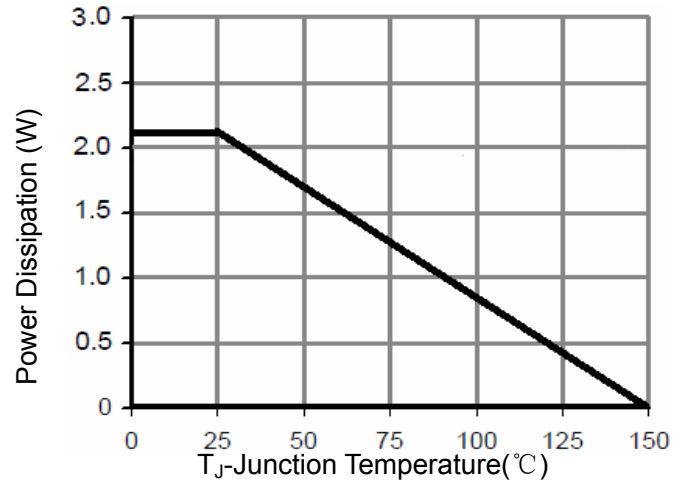


Figure 10 Power De-rating

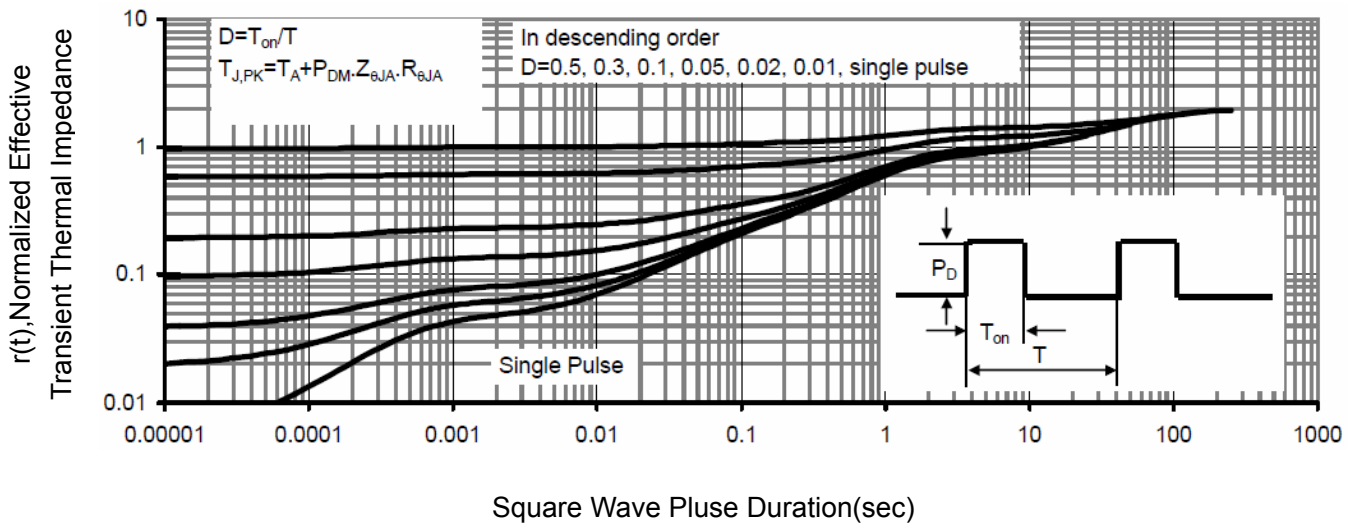


Figure 11 Normalized Maximum Transient Thermal Impedance



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