

Isc N-Channel MOSFET Transistor

STD10NM60N

• FEATURES

- With To-252(DPAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

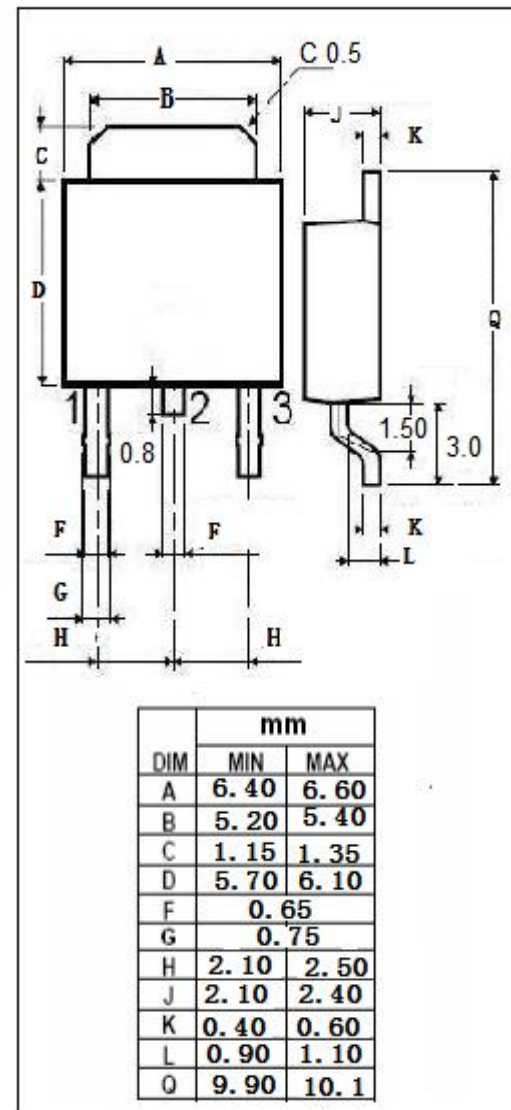
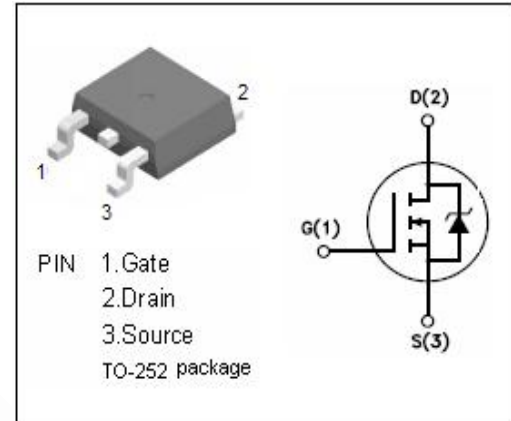
- Switching applications

• ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	600	V
V_{GSS}	Gate-Source Voltage	± 25	V
I_D	Drain Current-Continuous@ $T_c=25^{\circ}\text{C}$ ($T_j=175^{\circ}\text{C}$) $T_c=125^{\circ}\text{C}$	10 5	A
I_{DM}	Drain Current-Single Pulsed	32	A
P_D	Total Dissipation @ $T_c=25^{\circ}\text{C}$	70	W
T_{ch}	Max. Operating Junction Temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature	-55~150	$^{\circ}\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.79	$^{\circ}\text{C}/\text{W}$
$R_{th(ch-b)}$	Thermal resistance junction-pcb max	50	$^{\circ}\text{C}/\text{W}$



Isc N-Channel MOSFET Transistor**STD10NM60N****ELECTRICAL CHARACTERISTICS**T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D = 1mA	600			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = ±25V; I _D =0.25mA	2		4	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} = 10V; I _D =4A		60	65	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±25V; V _{DS} = 0V			±0.1	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} = 600V; V _{GS} = 0V; T _J =25°C T _J =125°C			1 100	μA
V _{SDF}	Diode forward voltage	I _{SD} =8A, V _{GS} = 0 V			1.3	V