TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

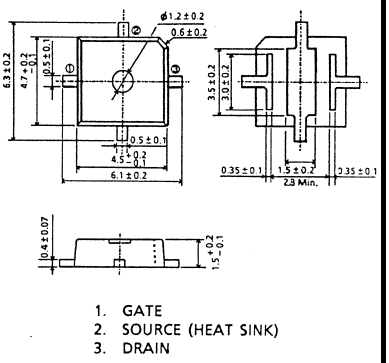
RFM07U7X

VHF- and UHF-band Amplifier Applications

(Note)The TOSHIBA products listed in this document are intended for high frequency Power Amplifier of telecommunications equipment. These TOSHIBA products are neither intended nor warranted for any other use. Do not use these TOSHIBA products listed in this document except for high frequency Power Amplifier of telecommunications equipment.

- Wide Band matching: f=450 to 530MHz
- Drain efficiency: η_D = 68% (typ.)
- Output power: P_O = 8.2 W (typ.)
- Gain: G_P = 12.5 dB (typ.)

Unit: mm



1. GATE
2. SOURCE (HEAT SINK)
3. DRAIN

PW-X	
JEDEC	-
JEITA	-
TOSHIBA	2-5N1A

Weight: 0.08 g (typ.)

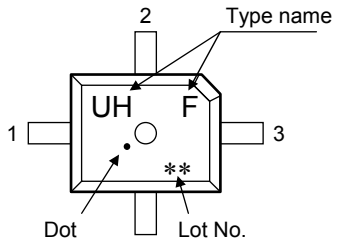
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	16	V
Gate-source voltage	V_{GSS}	3	V
Drain current	I_D	3	A
Power dissipation	P_D*	20	W
Channel temperature	T_{ch}	150	°C
Storage temperature range	T_{stg}	−45 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

*: Tc = 25°C (When mounted on a 1.6 mm glass epoxy PCB)

Marking



1. Gate
2. Source (heat sink)
3. Drain

Caution

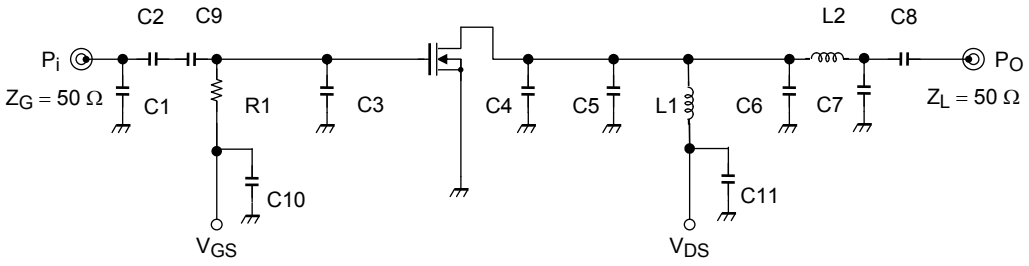
Please take care to avoid generating static electricity when handling this transistor.

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Drain cut-off current	I_{DSS}	V_{DS} = 7.2 V, V_{GS} = 0 V	–	–	10	μA
Gate-source leakage current	I_{GSS}	V_{GS} = 3 V	–	–	5	μA
Threshold voltage	V_{th}	V_{DS} = 7.2 V, I_D = 2 mA	0.4	0.9	1.4	V
Output power	P_O	V_{DS} = 7.2 V, I_{idle} = 500 mA (V_{GS} = adjust), f = 520 MHz, P_i = 0.5 W, Z_G = Z_L = 50 Ω	7.0	8.2	–	W
Drain efficiency	η_D		58	68	–	%
Power gain	G_P		11.5	12.5	–	dB
Load mismatch	–	V_{DS} = 10 V, P_O = 7 W (P_i = adjust), I_{idle} = 500 mA (V_{GS} = adjust), f = 520 MHz, VSWR LOAD 20:1 all phase	No degradation			–

Note 1: These characteristic values are measured using measurement tools specified by Toshiba.

Output Power Test Fixture
(Test Condition: f = 450 to 530 MHz, V_{DS} = 7.2 V, I_{idle} = 500 mA, P_i = 0.5 W)

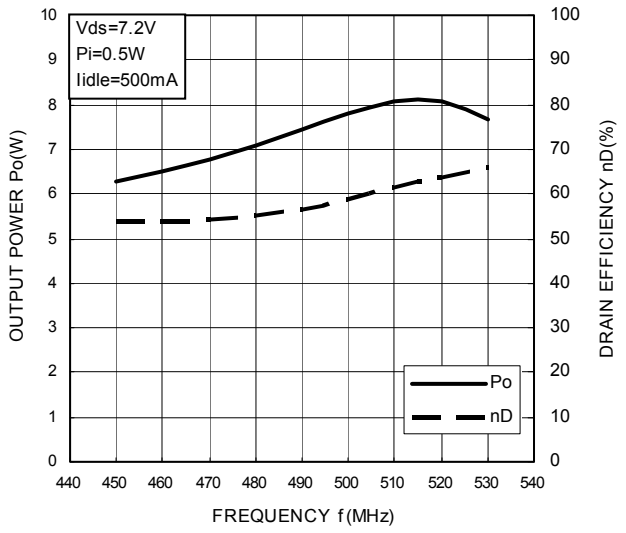


C1: 10 pF
C2: 2200 pF
C3: 39 pF
C4: 20 pF
C5: 20 pF
C6: 10 pF
C7: 10 pF
C8: 2200 pF
C9: 2200 pF
C10: 47 μF
C11: 47 μF

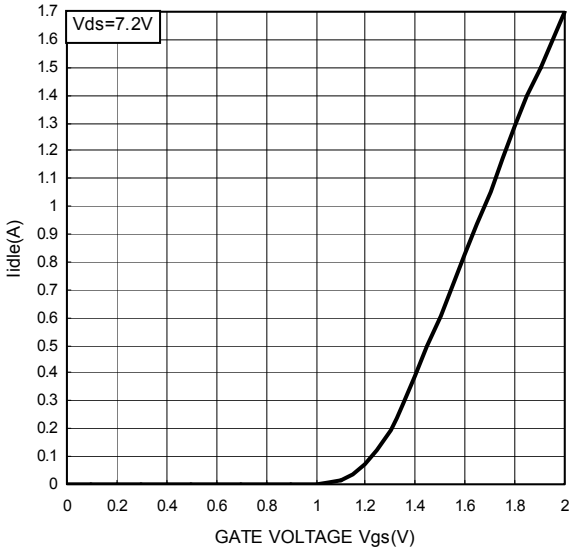
L1: ϕ0.6 mm enamel wire, 5.0ID, 10T
L2: ϕ0.5 mm enamel wire, 2.5ID, 1.5T

R1: 1.5 kΩ

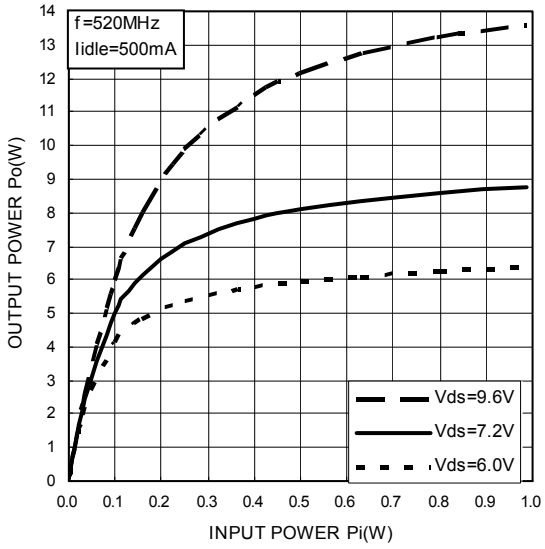
f - Po, nD



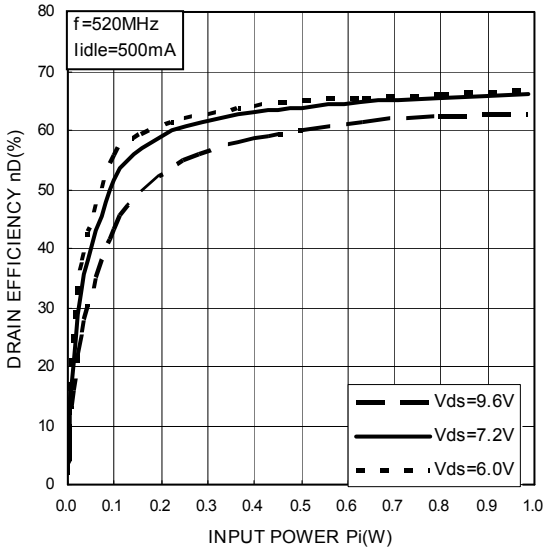
Vgs - Iidle



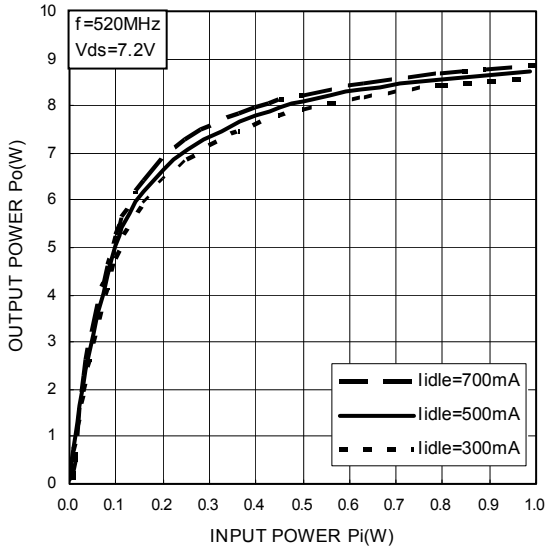
Pi - Po



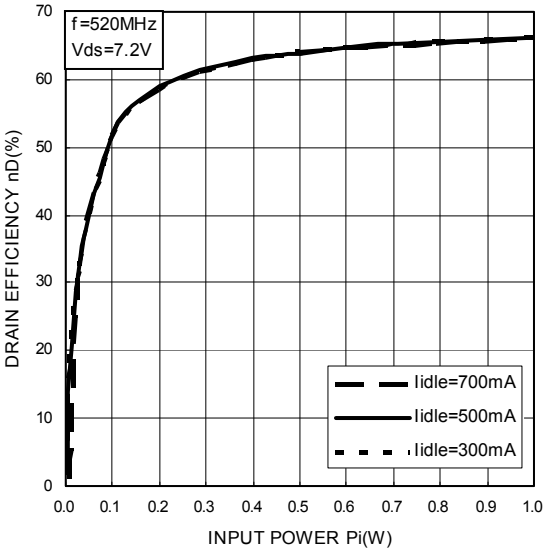
Pi - nD

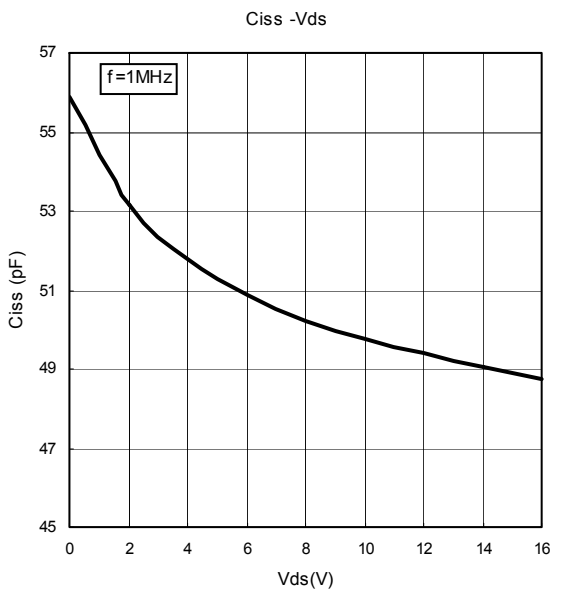


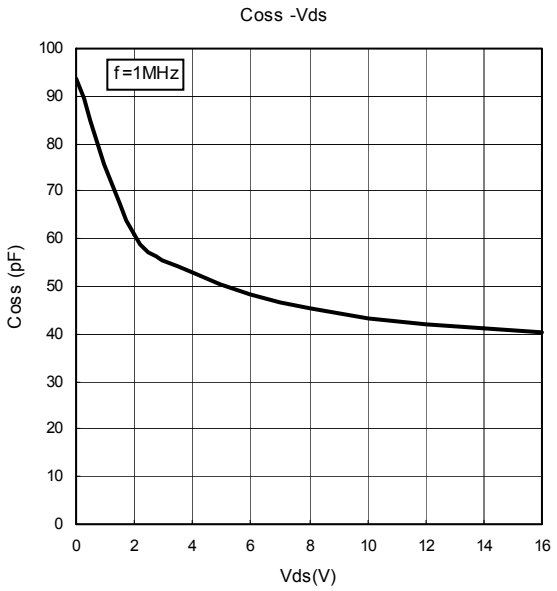
Pi - Po

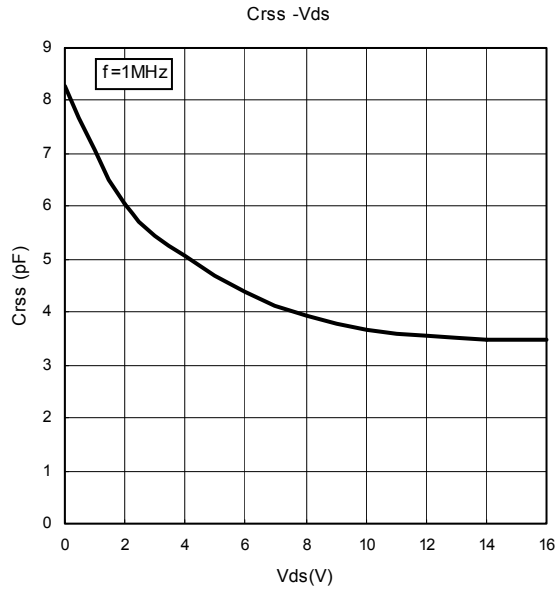


Pi - nD









Note 2:These are only typical curves and devices are not necessarily guaranteed at these curves.

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