CPH6442

Power MOSFET
60V, 43mΩ, 6A, Single N-Channel

ON Semiconductor®

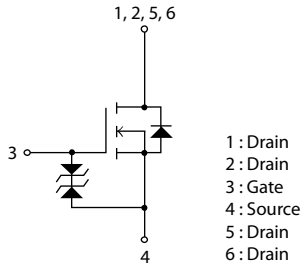
www.onsemi.com

V_{DSS}	$R_{DS(on)}$ Max	I_D Max
60V	43 mΩ@10V	6A
	59 mΩ@4.5V	
	65 mΩ@4V	

Features

- 4V Drive
- Low On-Resistance
- ESD Diode-Protected Gate
- Pb-Free, and RoHS Compliance
- Halogen Free Compliance : CPH6442-TL-W

Electrical Connection
N-Channel



Specifications

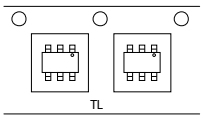
Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V_{DSS}	60	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I_D	6	A
Drain Current (Pulse) PW≤10μs, duty cycle≤1%	I_{DP}	24	A
Power Dissipation When mounted on ceramic substrate (900mm^2 × 0.8mm)	P_D	1.6	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	–55 to +150	°C

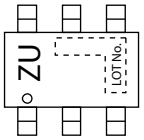
Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm^2 × 0.8mm)	$R_{\theta JA}$	78.1	°C/W

Packing Type : TL



Marking



Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ORDERING INFORMATION

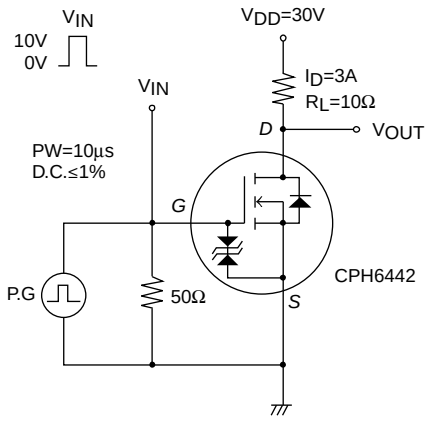
See detailed ordering and shipping information on page 5 of this data sheet.

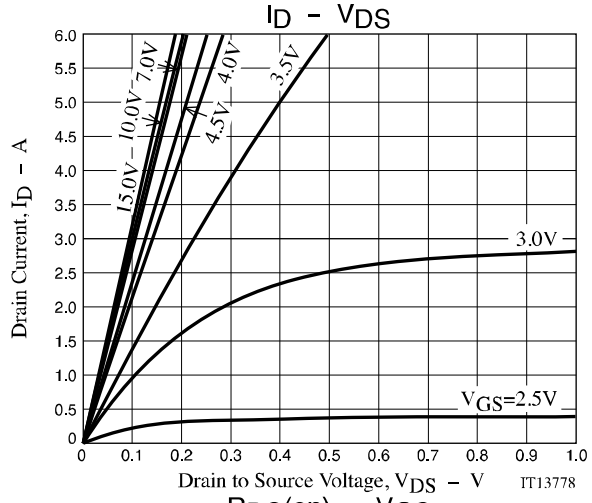
Electrical Characteristics at Ta = 25°C

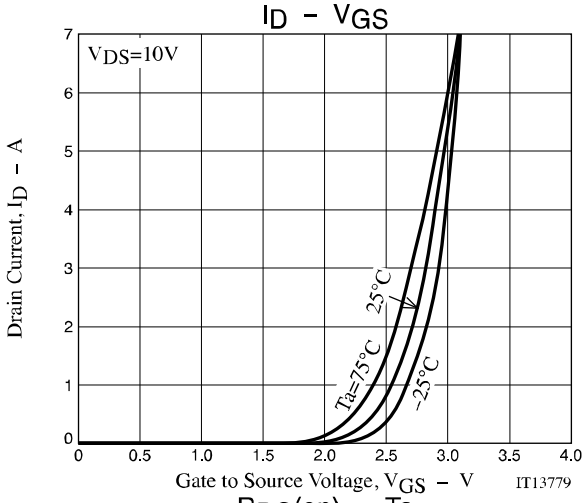
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0V	60			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=60V, V_{GS}=0V			1	μA
Gate to Source Leakage Current	I_{GSS}	V_{GS}=±16V, V_{DS}=0V			±10	μA
Gate Threshold Voltage	$V_{GS}(th)$	V_{DS}=10V, I_D=1mA	1.2		2.6	V
Forward Transconductance	g_{FS}	V_{DS}=10V, I_D=3A	2.6	4.4		S
Static Drain to Source On-State Resistance	$R_{DS}(on)1$	I_D=3A, V_{GS}=10V		33	43	mΩ
	$R_{DS}(on)2$	I_D=1.5A, V_{GS}=4.5V		42	59	mΩ
	$R_{DS}(on)3$	I_D=1.5A, V_{GS}=4V		46	65	mΩ
Input Capacitance	Ciss	V_{DS}=20V, f=1MHz		1040		pF
Output Capacitance	Coss			90		pF
Reverse Transfer Capacitance	Crss			55		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit		12		ns
Rise Time	t_r			18		ns
Turn-OFF Delay Time	$t_d(off)$			80		ns
Fall Time	t_f			35		ns
Total Gate Charge	Qg	V_{DS}=30V, V_{GS}=10V, I_D=6A		20		nC
Gate to Source Charge	Qgs			3.0		nC
Gate to Drain “Miller” Charge	Qgd			4.2		nC
Forward Diode Voltage	V_{SD}	I_S=6A, V_{GS}=0V		0.82	1.2	V

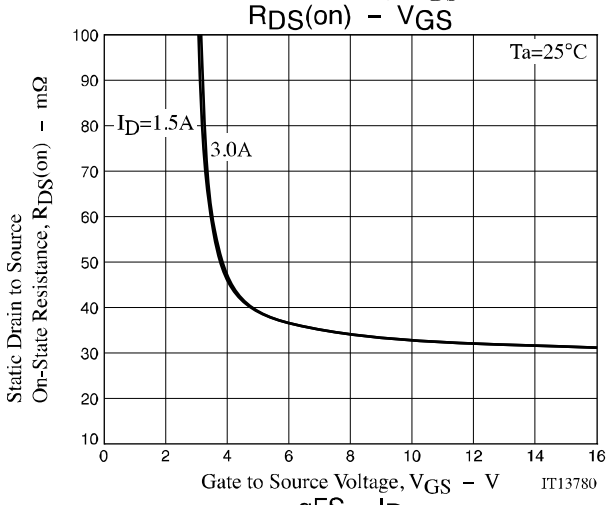
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

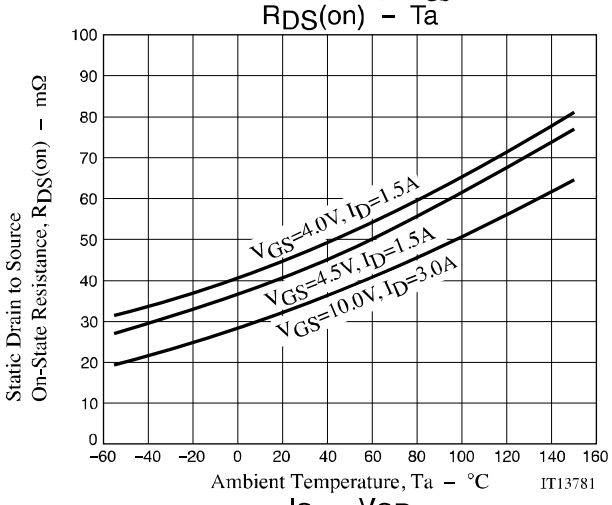
Switching Time Test Circuit

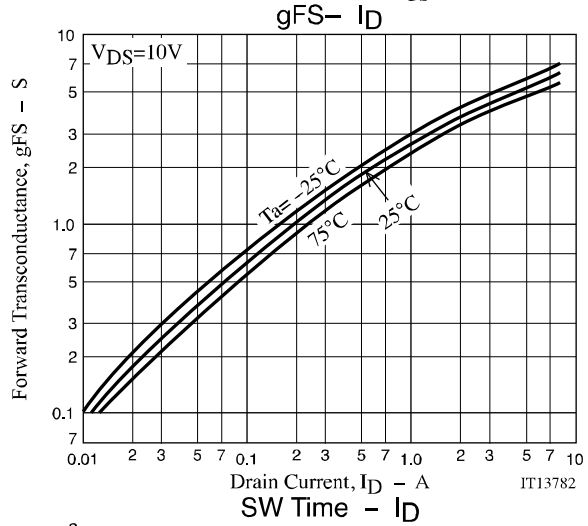


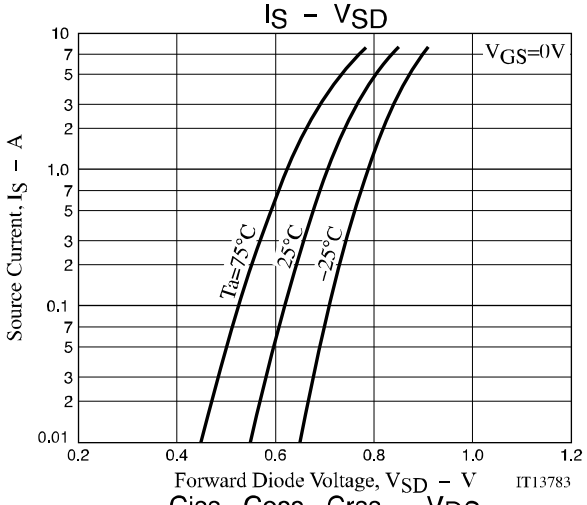

ID – VDS
Drain Current, ID – A
Drain to Source Voltage, VDS – V
IT13778
15.0V
10.0V
7.0V
4.5V
4.0V
3.5V
3.0V
VGS=2.5V

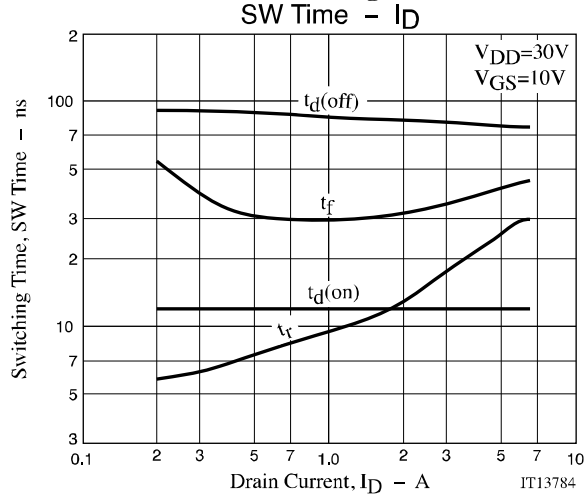

ID – VGS
Drain Current, ID – A
Gate to Source Voltage, VGS – V
IT13779
VDS=10V
25°C
Ta=75°C
–25°C

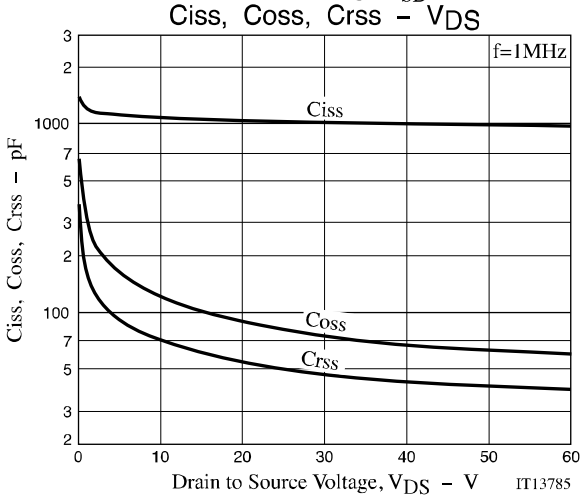

RDS(on) – VGS
Static Drain to Source On-State Resistance, RDS(on) – mΩ
Gate to Source Voltage, VGS – V
IT13780
Ta=25°C
ID=1.5A
3.0A

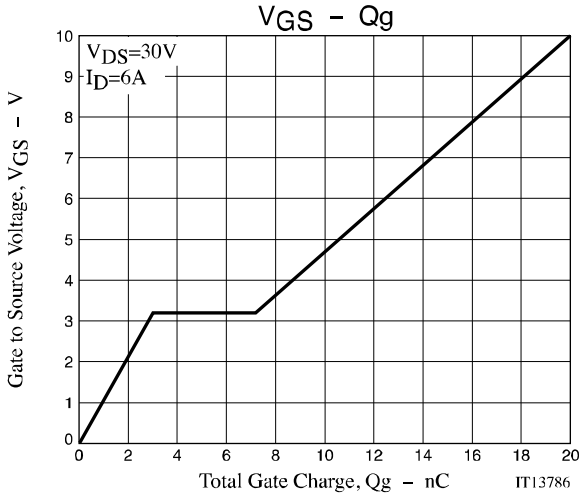

RDS(on) – Ta
Static Drain to Source On-State Resistance, RDS(on) – mΩ
Ambient Temperature, Ta – °C
IT13781
VGS=4.0V, ID=1.5A
VGS=4.5V, ID=1.5A
VGS=10.0V, ID=3.0A

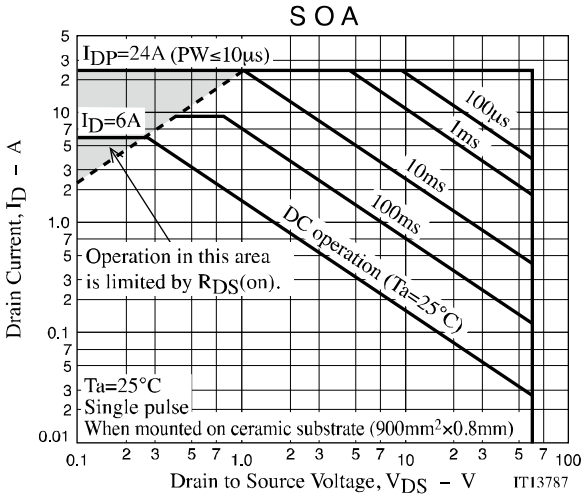

gFS– ID
Forward Transconductance, gFS – S
Drain Current, ID – A
IT13782
VDS=10V
Ta=–25°C
75°C
25°C

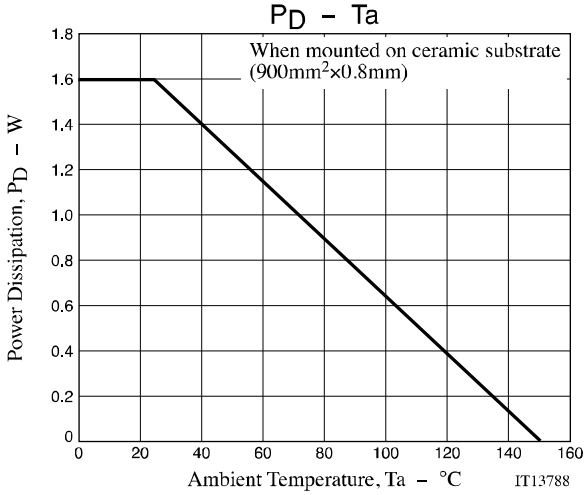

IS – VSD
Source Current, IS – A
Forward Diode Voltage, VSD – V
IT13783
VGS=0V
Ta=75°C
25°C
–25°C

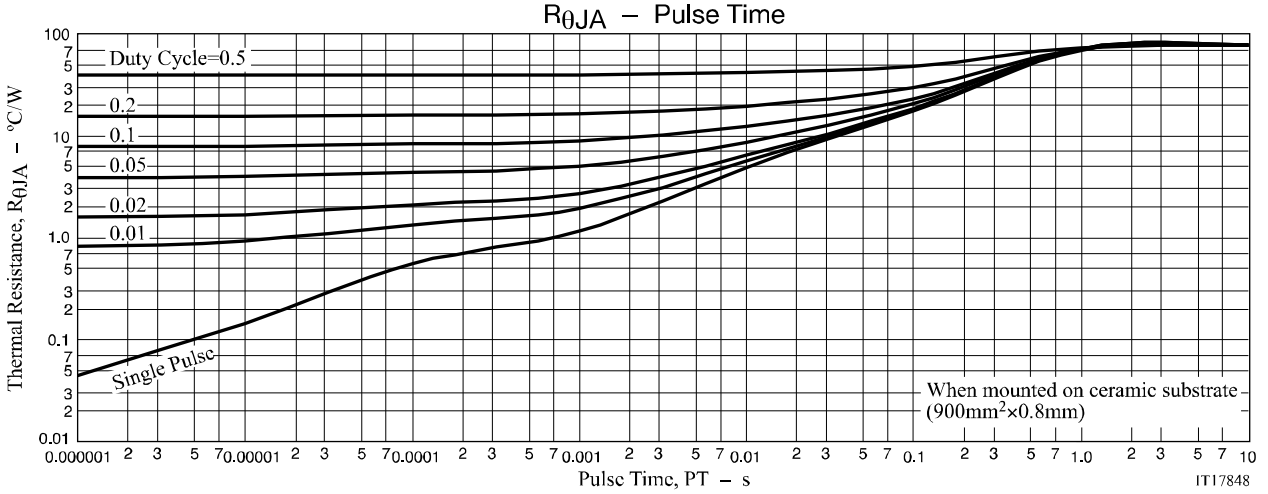

SW Time – ID
Switching Time, SW Time – ns
Drain Current, ID – A
IT13784
VDD=30V
VGS=10V
td(off)
tf
td(on)
tr


Ciss, Coss, Crss – VDS
Ciss, Coss, Crss – pF
Drain to Source Voltage, VDS – V
IT13785
f=1MHz
Ciss
Coss
Crss


VGS – Qg
VDS=30V
ID=6A
Gate to Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT13786


S O A
IDP=24A (PW≤10μs)
ID=6A
100μs
1ms
10ms
100ms
DC operation (Ta=25°C)
Operation in this area
is limited by RDS(on).
Ta=25°C
Single pulse
When mounted on ceramic substrate (900mm2×0.8mm)
Drain Current, ID – A
Drain to Source Voltage, VDS – V
IT13787


PD – Ta
When mounted on ceramic substrate
(900mm2×0.8mm)
Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT13788


RθJA – Pulse Time
Duty Cycle=0.5
0.2
0.1
0.05
0.02
0.01
Single Pulse
When mounted on ceramic substrate
(900mm2×0.8mm)
Thermal Resistance, RθJA – °C/W
Pulse Time, PT – s
IT17848

Package Dimensions

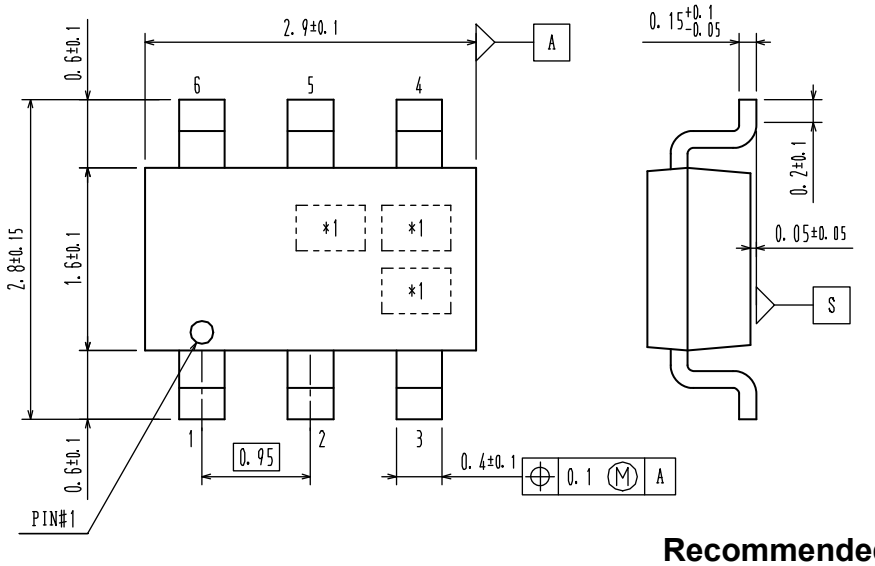
CPH6442-TL-E / CPH6442-TL-W

CPH6

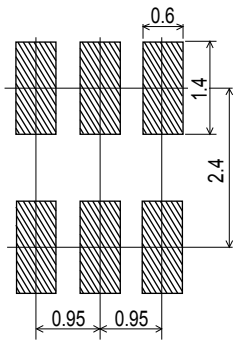
CASE 318BD
ISSUE O

Unit : mm

1 : Drain
2 : Drain
3 : Gate
4 : Source
5 : Drain
6 : Drain



Recommended Soldering Footprint



ORDERING INFORMATION

Device	Package	Shipping	Note
CPH6442-TL-E	CPH6, SC-74 SOT-26, SOT-457	3,000 pcs. / Tape & Reel	Pb-Free
CPH6442-TL-W			Pb-Free and Halogen Free

Note on usage : Since the CPH6442 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.