TIGER ELECTRONIC CO.,LTD

784049U69 *HEX BUFFER/CONVERTER*
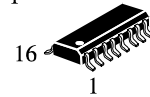*High-Voltage Silicon-Gate CMOS*

RoHS COMPLIANT

The ÔÖ4049UÓ is inverting hex buffers and feature logic-level conversion using only one supply (voltage (V_{CC}). The input-signal high level (V_{IH}) can exceed the V_{CC} supply voltage when these devices are used for logic-level conversions. These devices are intended for use as CMOS to DTL/TTL converters.

- Operating Voltage Range: 3.0 to 18 V
- Maximum input current of 1 μA at 18 V over full package-temperature range; 100 nA at 18 V and 25°C
- Noise margin (over full package temperature range):
0.5 V min @ 5.0 V supply
1.0 V min @ 10.0 V supply
1.0 V min @ 15.0 V supply
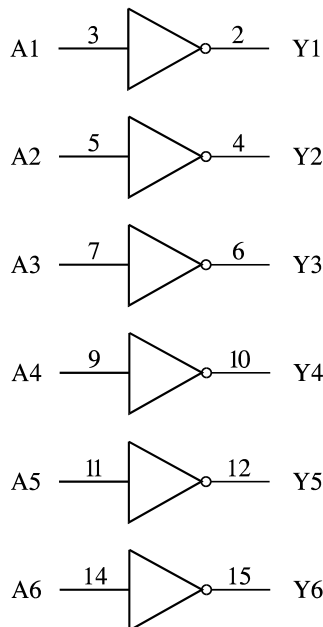- High-to-low level conversion

N SUFFIX
PLASTIC

16
1

D SUFFIX
SOIC

16
1
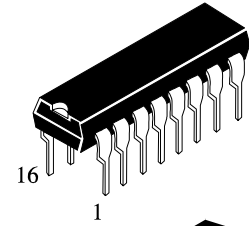
ORDERING INFORMATION
ÁÔÖ4049UBÒ ÁÍPlastic
ÔÖ4049UÓÖÍÍÍSOIC
T_A = -55° to 125° C for all packages

LOGIC DIAGRAM

A1 —3—▷o—2— Y1
A2 —5—▷o—4— Y2
A3 —7—▷o—6— Y3
A4 —9—▷o—10— Y4
A5 —11—▷o—12— Y5
A6 —14—▷o—15— Y6

PINS 13, 16 = NO CONNECTION
PIN 1 = V_{CC}
PIN 8 = GND

PIN ASSIGNMENT

V_{CC}	1●	16 NC
Y1	2	15 Y6
A1	3	14 A6
Y2	4	13 NC
A2	5	12 Y5
Y3	6	11 A5
A3	7	10 Y4
GND	8	9 A4
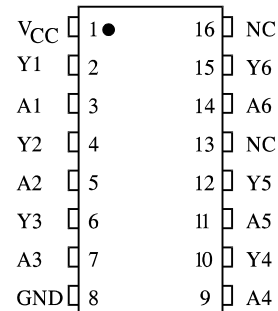
NC = NO CONNECTION

FUNCTION TABLE

Inputs	Output
A	Y
H	L
L	H

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MAXIMUM RATINGS[*]

Symbol	Parameter	Value	Unit
V_{CC}	DC Supply Voltage (Referenced to GND)	-0.5 to +20	V
V_{IN}	DC Input Voltage (Referenced to GND)	V_{CC}[**] to +18	V
V_{OUT}	DC Output Voltage (Referenced to GND)	-0.5 to V_{CC} +0.5	V
I_{IN}	DC Input Current, per Pin	± 10	mA
P_D	Power Dissipation in Still Air, Plastic DIP+ SOIC Package+	750 500	mW
P_D	Dissipation per Output Transistor	100	mW
Tstg	Storage Temperature	-65 to +150	°C
T_L	Lead Temperature, 1 mm from Case for 10 Seconds (Plastic DIP or SOIC Package)	260	°C

[*]Maximum Ratings are those values beyond which damage to the device may occur.
Functional operation should be restricted to the Recommended Operating Conditions.
+Derating - Plastic DIP: - 10 mW/°C from 65° to 125°C
        SOIC Package: : - 7 mW/°C from 65° to 125°C
[**] The ÔÖ4049UB has high-to-low level voltage conversion capability but not low-to-high level; therefore it is recommended that $V_{IN} \geq V_{CC}$

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{CC}	DC Supply Voltage (Referenced to GND)	3.0	18	V
V_{IN}	DC Input Voltage (Referenced to GND)	V_{CC}[**]	18	V
V_{OUT}	DC Output Voltage (Referenced to GND)	0	V_{CC}	V
T_A	Operating Temperature, All Package Types	-55	+125	°C

[**] The ÔÖ4049UB has high-to-low level voltage conversion capability but not low-to-high level; therefore it is recommended that $V_{IN} \geq V_{CC}$

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V_{IN} and V_{OUT} should be constrained to the range GND$\leq$(V_{IN} or V_{OUT})$\leq V_{CC}$.
Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or V_{CC}). Unused outputs must be left open.

DC ELECTRICAL CHARACTERISTICS(Voltages Referenced to GND)

Symbol	Parameter	Test Conditions	V_{CC} V	Guaranteed Limit			Unit
				$\geq$-55°C	25°C	$\leq$125 °C	
V_{IH}	Minimum High-Level Input Voltage	V_{OUT}= 0.5V V_{OUT}= 1.0 V V_{OUT}= 1.5V	5.0 10 15	4 8 12.5	4 8 12.5	4 8 12.5	V
V_{IL}	Maximum Low - Level Input Voltage	V_{OUT}= V_{CC} - 0.5V V_{OUT}= V_{CC} - 1.0 V V_{OUT}= V_{CC} - 1.5V	5.0 10 15	1 2 2.5	1 2 2.5	1 2 2.5	V
V_{OH}	Minimum High-Level Output Voltage	V_{IN}=GND	5.0 10 15	4.95 9.95 14.95	4.95 9.95 14.95	4.95 9.95 14.95	V
V_{OL}	Maximum Low-Level Output Voltage	V_{IN}= V_{CC}	5.0 10 15	0.05 0.05 0.05	0.05 0.05 0.05	0.05 0.05 0.05	V
I_{IN}	Maximum Input Leakage Current	V_{IN}= GND or V_{CC}	18	±0.1	±0.1	±1.0	μA
I_{CC}	Maximum Quiescent Supply Current (per Package)	V_{IN}= GND or V_{CC}	5.0 10 15 20	1 2 4 20	1 2 4 20	30 60 120 600	μA
I_{OL}	Minimum Output Low (Sink) Current	V_{IN}= GND or V_{CC} U_{OL}=0.4 V U_{OL}=0.4 V U_{OL}=0.5 V U_{OL}=1.5 V	 4.5 5 10 15	 3.3 4 10 26	 2.6 3.2 8 24	 1.8 2.4 5.6 18	mA
I_{OH}	Minimum Output High (Source) Current	V_{IN}= GND or V_{CC} U_{OH}=2.5 V U_{OH}=4.6 V U_{OH}=9.5 V U_{OH}=13.5 V	 5.0 5.0 10 15	 -2.6 -0.81 -2.0 -5.2	 -2.1 -0.65 -1.65 -4.3	 -1.55 -0.48 -1.18 -3.1	mA

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AC ELECTRICAL CHARACTERISTICS(C_L=50pF, R_L=200kΩ, Input t_r=t_f=20 ns)

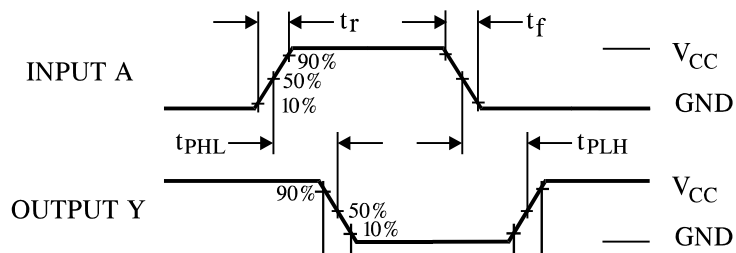| Symbol	Parameter	V_{IN} V	V_{CC} V	Guaranteed Limit			Unit
				$\geq$-55°C	25°C	$\leq$125°C	
t_{PLH}	Maximum Propagation Delay, Input A to Output Y (Figure 1)	5	5	120	120	240	ns
		10	10	65	65	130	
		10	5	90	90	180	
		15	15	50	50	100	
		15	5	90	90	180	
t_{PHL}	Maximum Propagation Delay, Input A to Output Y (Figure 1)	5	5	65	65	130	ns
		10	10	40	40	80	
		10	5	30	30	60	
		15	15	30	30	60	
		15	5	20	20	40	
t_{TLH}	Maximum Output Transition Time, Any Output (Figure 1)	5	5	160	160	320	ns
		10	10	80	80	160	
		15	15	60	60	120	
t_{THL}	Maximum Output Transition Time, Any Output (Figure 1)	5	5	60	60	120	ns
		10	10	40	40	80	
		15	15	30	30	60	
C_{IN}	Maximum Input Capacitance	-	-		22.5		pF

Figure 1. Switching Waveforms

EXPANDED LOGIC DIAGRAM
(1/6 of the Device)

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