TS321

Preliminary

LINEAR INTEGRATED CIRCUIT

LOW-POWER SINGLE OPERATIONAL AMPLIFIER

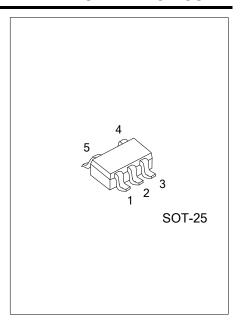
■ DESCRIPTION

The UTC **TS321**'s quiescent current is only $500\mu A$ (5V). The UTC **TS321** brings performance and economy to low power systems. With a high unity gain frequency and a specified $0.4V/\mu s$ slew rate. The device is able to operate in single supply applications as well as in dual supply applications.

The UTC **TS321** is a bipolar operational amplifier for cost-sensitive applications in which space savings are important.

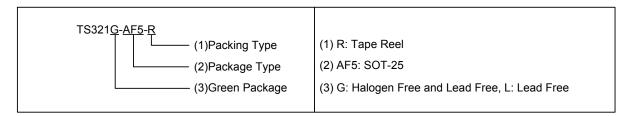


- * Wide Power-Supply Range
- Single Supply: 3V~30V or Dual Supply: ±1.5V~±15V
- * Large Output Voltage Swing: 0V~3.5V (Min.) (V_{CC}=5V)
- * Low Supply Current: 500µA (Typ.)
- * Low Input Bias Current: 20nA (Typ.)
- * Low Input Offset Voltage: 4mV (Max.)
- * Stable With High Capacitive Loads



■ ORDERING INFORMATION

Ordering Number		Dookogo	Docking	
Lead Free	Halogen Free	Package	Packing	
TS321L-AF5-R	TS321G-AF5-R	SOT-25	Tape Reel	

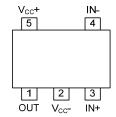


■ MARKING



<u>www.unisonic.com.tw</u> 1 of 6

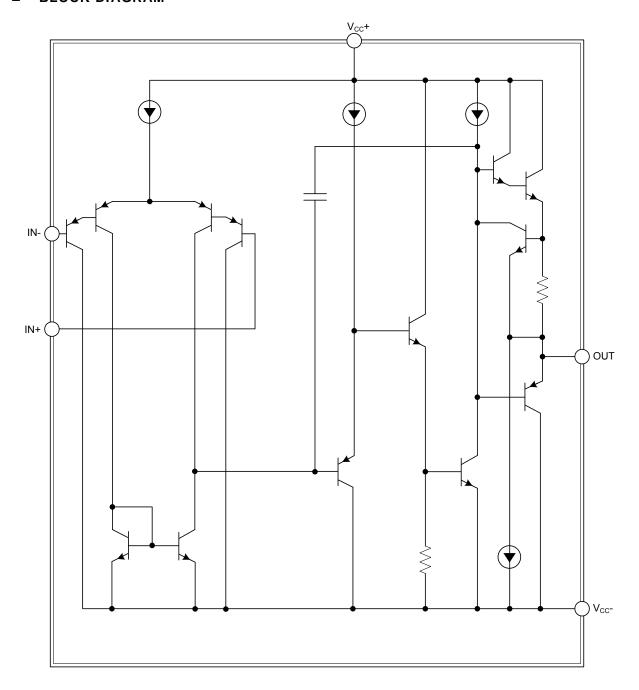
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	OUT	Output
2	V _{CC} -	Ground
3	IN+	Non- negative input
4	IN-	Negative input
5	V _{CC} +	Power supply

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING (Note 1)

Over operating free-air temperature range (unless otherwise noted)

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	SYMBOL	RATINGS	UNIT
Single	\/	32	V
Dual	Vcc	±16	V
Differential Input Voltage (Note 3)		32	V
Input Voltage Range (Note 2, 4)		-0.3 ~ 32	V
Input Current (Note 4)		50	mA
Duration Of Output Short Circuit To Ground		Unlimited	
Power Dissipation		0.595	W
Operating Virtual Junction Temperature		+150	°C
Storage Temperature Range		-65 ~ +150	°C
	Single Dual	$\begin{array}{c c} & \text{SYMBOL} \\ \hline Single \\ \hline Dual & V_{CC} \\ \hline & V_{ID} \\ \hline & V_{I} \\ \hline & I_{I} \\ \hline Ground & T_{SHORT} \\ \hline & P_{D} \\ \hline \end{array}$	Single Dual V _{CC} 32 V _{ID} 32 V _I -0.3 ~ 32 I _I 50 Ground T _{SHORT} Unlimited P _D 0.595 ure T _J +150

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

 Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 - 2. These voltage values are with respect to the midpoint between $V_{\text{CC-}}$ and $V_{\text{CC-}}$.
 - 3. Differential voltages are at IN+ with respect to IN-.
 - 4. Neither input must ever be more positive than $V_{\text{CC+}}$ or more negative than $V_{\text{CC-}}$.

■ RECOMMENDED OPERATING CONDITIONS

PARA	PARAMETER		RATINGS	UNIT
Supply Voltage	Single Supply	\ <u>/</u>	3 ~ 30	V
	Dual Supply	V _{CC}	±1.5 ~ ±15	>
Operating Free-Air Temperature		T _A	-40 ~ +125	°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	210	°C/W

■ ELECTRICAL CHARACTERISTICS

(V_{CC+}=5V, V_{CC-}=GND, V_O=1.4V (unless otherwise noted))

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Input Offset Voltage	V _{IO}	R _S =0, 5V <v<sub>CC+<30V 0<v<sub>IC<(V_{CC+}-1.5V)</v<sub></v<sub>			0.5	4	mV
Input Offset Current	I _{IO}	0~V C~(VCC+-1.5V)			2	30	nA
Input Bias Current (Note 1)	I _{IB}				20	150	nA
Large-Signal Differential Voltage Amplification	A _{VD}	V _{CC} =15V, R _L =2kΩ, V _O =1.4V~11.4V		50	100		V/mV
Common-Mode Input Voltage (Note 2)	V _{ICR}	V _{CC} =30V		0		V _{CC+} -1.5	V
High-Level Output Voltage	V _{OH}	V _{CC} =30V	$R_L=2k\Omega$	26	27		V
		VCC-30 V	$R_L=10k\Omega$	27	28		
		V _{CC} =5V	$R_L=2k\Omega$	3.5			
Low-Level Output Voltage	V_{OL}	R _L =10kΩ			5	15	mV
Gain Bandwidth Product	GBP	V_{CC} =30V, V_{I} =10mV, R f=100kHz, C_{L} =100pF		0.8		MHz	
Slew Rate	SR	V_{CC} =15V, V_I =0.5V~3V, R_L =2k Ω , C_L =100pF, unity gain			0.4		V/µs
Phase Margin	φm				60		0
Common-Mode Rejection Ratio	CMRR	R _S ≤10kΩ		65	85		dB
Output Source Current	I _{SOURCE}	V _{CC} =15V, V _O =2V, V _{ID} =	-1V	20	40		mA
Output Sink Current	I _{SINK}	V _{CC} =15V, V _{ID} =1V	V _O =2V	10	20		mA
Output Sink Current			V _O =0.2V	12	50		μΑ
Short-Circuit To GND	Io	V _{CC} =15V			40	60	mA
Supply-Voltage Rejection Ratio	SVR	V _{CC} =5V~30V		65	110		dB
Total Supply Current	Icc	No lood	V _{CC} =5V		500	800	μA
		No load V _{CC} =30V			600	900	μA
Total Harmonic Distortion	THD	V_{CC} =30V, V_{O} =2V _{PP} , A_{V} =20dB, R_{L} =2k Ω , f=1kHz, C_{L} =100pF			0.015		%
Equivalent Input Noise Voltage	e _N	V_{CC} =30V, f=1kHz, R _S =100 Ω			50		nV/√Hz

Notes: 1. The direction of the input current is out of the device. This current essentially is constant, independent of the state of the output, so no loading change exists on the input lines.

^{2.} The input common-mode voltage of either input signal should not be allowed to go negative by more than 0.3V. The upper end of the common-mode voltage range is V_{CC+} -1.5V, but either or both inputs can go to 32V without damage.

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