

TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

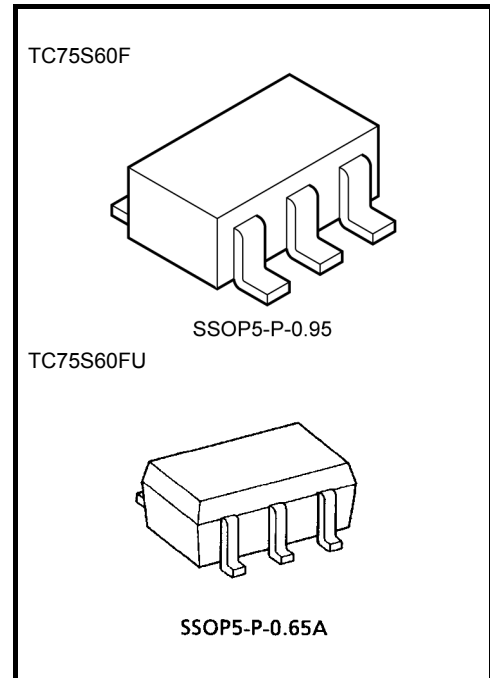
# TC75S60F, TC75S60FU

## Single Operational Amplifier

TC75S60F, TC75S60FU are CMOS operational amplifier with low supply voltage, low supply current.

### Features

- High slew rate: SR ( $V_{DD} = 3\text{ V}$ ) = 5.1 V/ $\mu\text{s}$  (typ.)
- The power supply operation range is:  
 $V_{DD} = \pm 0.9\sim 3.5\text{ V}$  or 1.8~7 V
- Low supply current:  $I_{DD} (V_{DD} = 3\text{ V}) = 330\ \mu\text{A}$  (typ.)
- The internally phase compensated operational amplifier.
- Small package



### Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

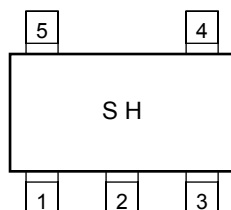
Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{DD}, V_{SS}$	7	V
Differential input voltage	$DV_{IN}$	$\pm 7$	V
Input voltage	$V_{IN}$	$V_{DD}\sim V_{SS}$	V
Power dissipation	$P_D$	200	mW
Operating temperature	$T_{opr}$	-40~85	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55~125	$^\circ\text{C}$

Weight  
 SSOP5-P-0.95 : 0.014 g (typ.)  
 SSOP5-P-0.65A : 0.006 g (typ.)

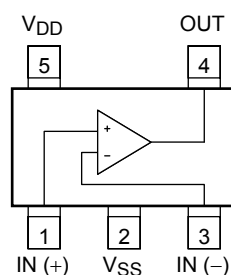
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## Marking (top view)



## Pin Connection (top view)



## Electrical Characteristics

### DC Characteristics ( $V_{DD} = 3.0\text{ V}$ , $V_{SS} = \text{GND}$ , $T_a = 25^\circ\text{C}$ )

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Input offset voltage	$V_{IO}$	1	$R_S = 1\text{ k}\Omega$	—	2	7	mV
Input offset current	$I_{IO}$	—	—	—	1	—	pA
Input bias current	$I_I$	—	—	—	1	—	pA
Common mode input voltage	$CMV_{IN}$	2	—	0.0	—	2.1	V
Voltage gain (open loop)	$G_V$	—	—	60	70	—	dB
Maximum output voltage	$V_{OH}$	3	$R_L = 100\text{ k}\Omega$	2.9	—	—	V
	$V_{OL}$	4	$R_L = 100\text{ k}\Omega$	—	—	0.1	
Common mode rejection ratio	CMRR	2	$V_{IN} = 0.0\sim 2.1\text{ V}$	54	70	—	dB
Supply voltage rejection ratio	SVRR	1	$V_{DD} = 1.8\sim 7.0\text{ V}$	60	70	—	dB
Supply current	$I_{DD}$	5	—	—	330	500	$\mu\text{A}$
Source current	$I_{\text{source}}$	6	—	330	700	—	$\mu\text{A}$
Sink current	$I_{\text{sink}}$	7	—	600	1250	—	$\mu\text{A}$

### DC Characteristics ( $V_{DD} = 1.8\text{ V}$ , $V_{SS} = \text{GND}$ , $T_a = 25^\circ\text{C}$ )

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Input offset voltage	$V_{IO}$	1	$R_S = 10\text{ k}\Omega$	—	2	7	mV
Input offset current	$I_{IO}$	—	—	—	1	—	pA
Input bias current	$I_I$	—	—	—	1	—	pA
Common mode input voltage	$CMV_{IN}$	2	—	0.3	—	0.9	V
Voltage gain (open loop)	$G_V$	—	—	—	70	—	dB
maximum output voltage	$V_{OH}$	3	$R_L = 100\text{ k}\Omega$	1.7	—	—	V
	$V_{OL}$	4	$R_L = 100\text{ k}\Omega$	—	—	0.1	
Common mode rejection ratio	CMRR	2	$V_{IN} = 0.3\sim 0.9\text{ V}$	50	60	—	dB
Supply current	$I_{DD}$	5	—	—	300	450	$\mu\text{A}$
Source current	$I_{\text{source}}$	6	—	300	600	—	$\mu\text{A}$
Sink current	$I_{\text{sink}}$	7	—	550	1150	—	$\mu\text{A}$

## AC Characteristics ( $V_{DD} = 3.0\text{ V}$ , $V_{SS} = \text{GND}$ , $T_a = 25^\circ\text{C}$ )

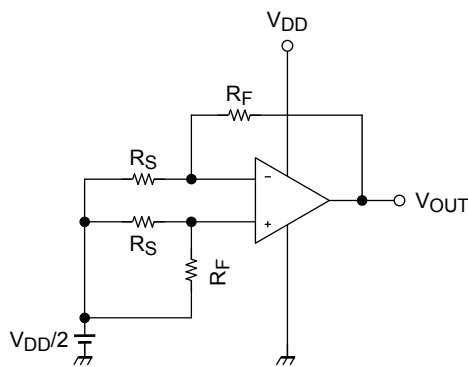
Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Slew rate	SR	—	—	—	5.1	—	V/ $\mu\text{s}$
Unity gain cross frequency	$f_T$	—	—	—	3.7	—	MHz

## AC Characteristics ( $V_{DD} = 1.8\text{ V}$ , $V_{SS} = \text{GND}$ , $T_a = 25^\circ\text{C}$ )

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Slew rate	SR	—	—	—	4.0	—	V/ $\mu\text{s}$
Unity gain cross frequency	$f_T$	—	—	—	3.0	—	MHz

## Test Circuit

### 1. SVRR, $V_{IO}$



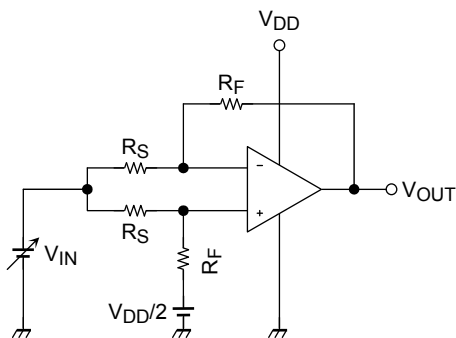
- SVRR  
 $V_{DD} = 1.8\text{ V}$ :  $V_{DD} = V_{DD1}$ ,  $V_{OUT} = V_{OUT1}$   
 $V_{DD} = 7.0\text{ V}$ :  $V_{DD} = V_{DD2}$ ,  $V_{OUT} = V_{OUT2}$   

$$SVRR = 20 \log \left( \left| \frac{V_{OUT1} - V_{OUT2}}{V_{DD1} - V_{DD2}} \right| \times \frac{R_S}{R_F + R_S} \right)$$

- $V_{IO}$   

$$V_{IO} = \left( V_{OUT} - \frac{V_{DD}}{2} \right) \times \frac{R_S}{R_F + R_S}$$

### 2. CMRR, $CMV_{IN}$

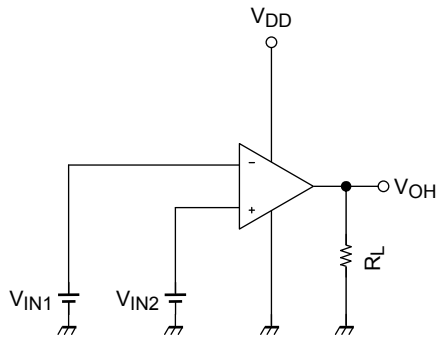


- CMRR  
 $V_{IN} = 0.0\text{ V}$ :  $V_{IN} = V_{IN1}$ ,  $V_{OUT} = V_{OUT1}$   
 $V_{IN} = 2.1\text{ V}$ :  $V_{IN} = V_{IN2}$ ,  $V_{OUT} = V_{OUT2}$   

$$CMRR = 20 \log \left( \left| \frac{V_{OUT1} - V_{OUT2}}{V_{IN1} - V_{IN2}} \right| \times \frac{R_S}{R_F + R_S} \right)$$

- $CMV_{IN}$

**3. V<sub>OH</sub>**

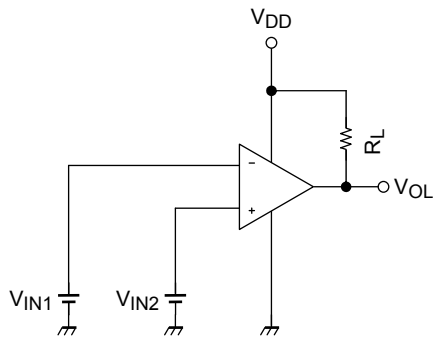


- V<sub>OH</sub>  

$$V_{IN1} = \frac{V_{DD}}{2} - 0.05 \text{ V}$$

$$V_{IN2} = \frac{V_{DD}}{2} + 0.05 \text{ V}$$

**4. V<sub>OL</sub>**

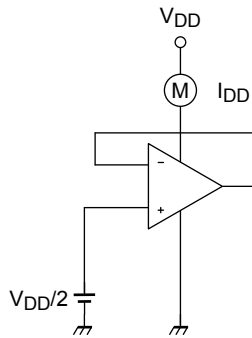


- V<sub>OL</sub>  

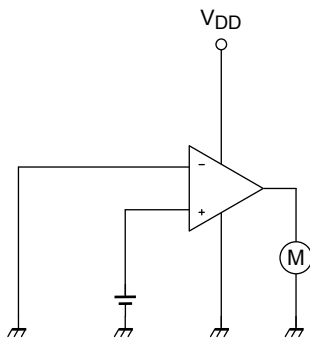
$$V_{IN1} = \frac{V_{DD}}{2} + 0.05 \text{ V}$$

$$V_{IN2} = \frac{V_{DD}}{2} - 0.05 \text{ V}$$

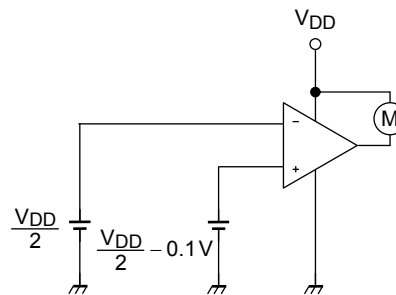
**5. I<sub>DD</sub>**

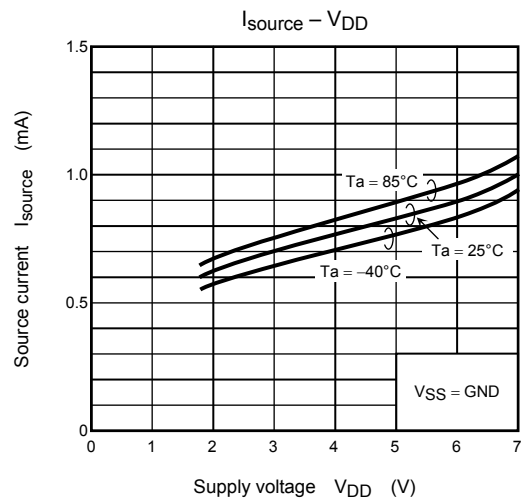
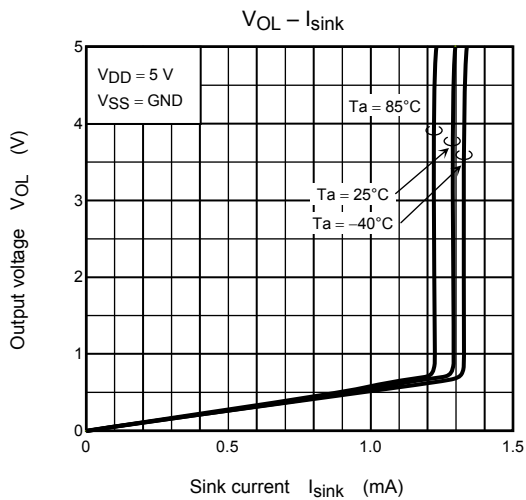
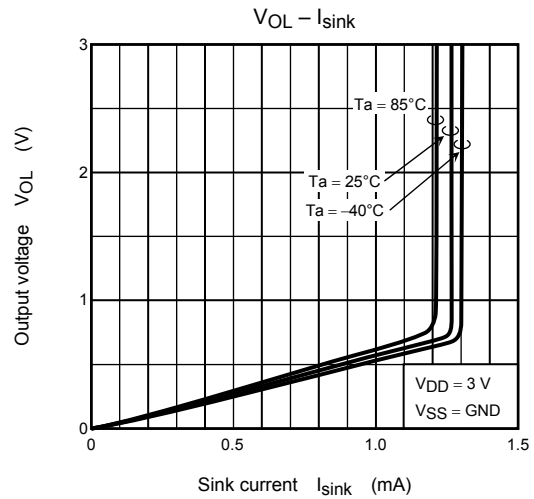
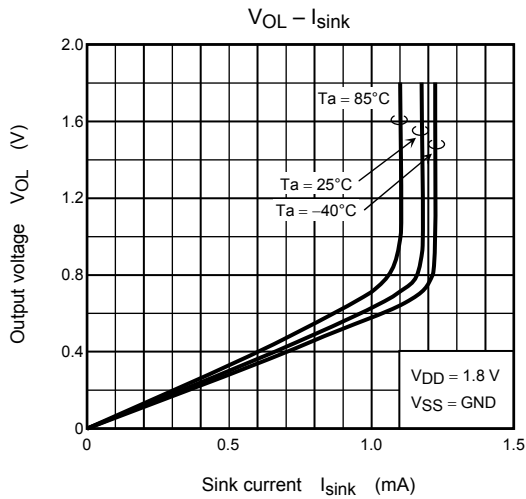
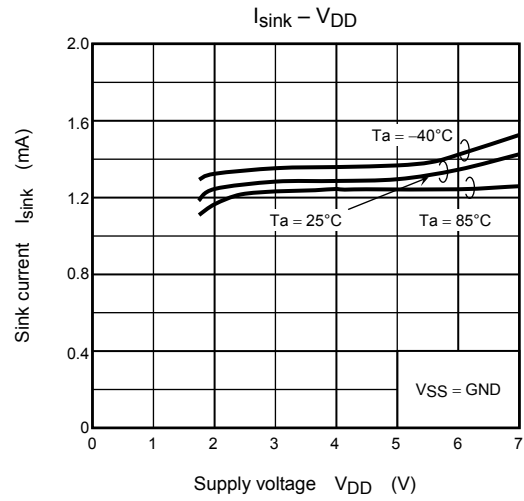
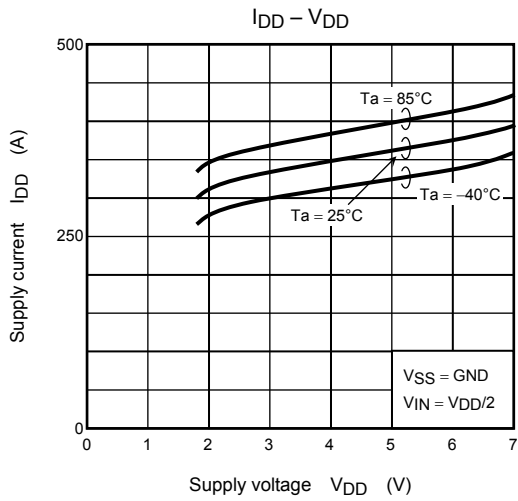


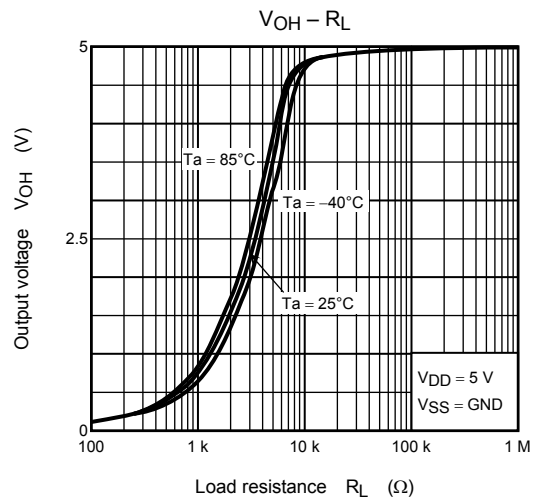
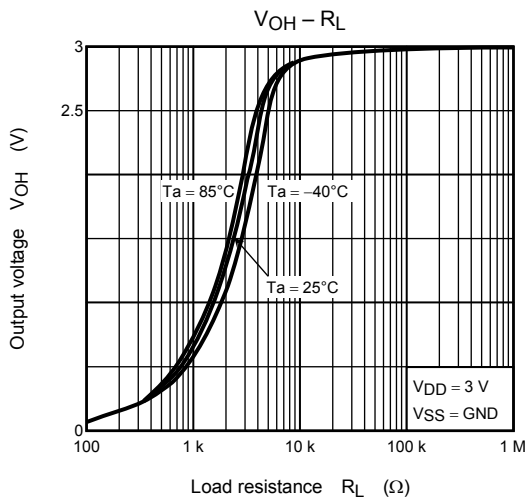
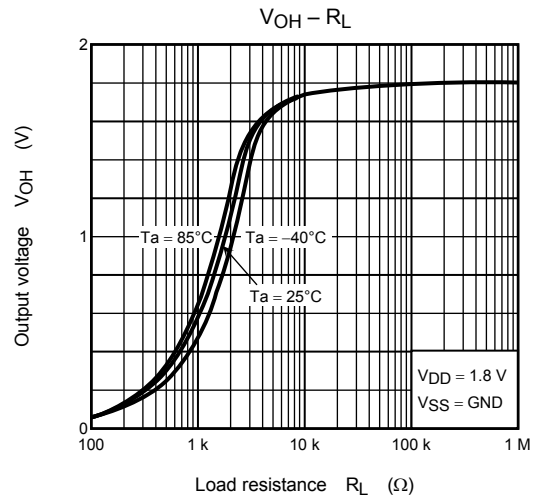
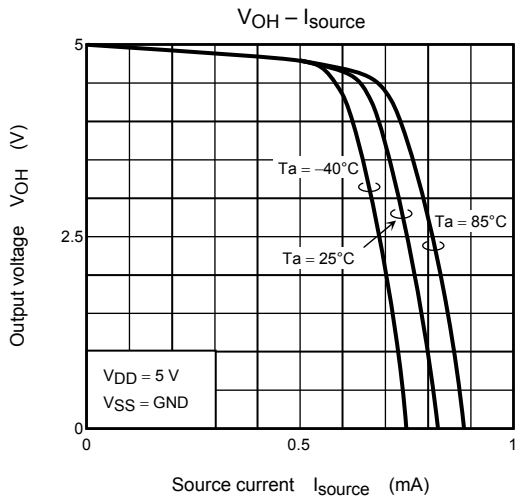
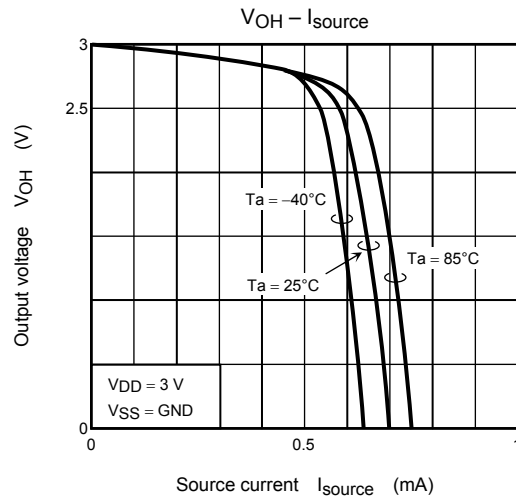
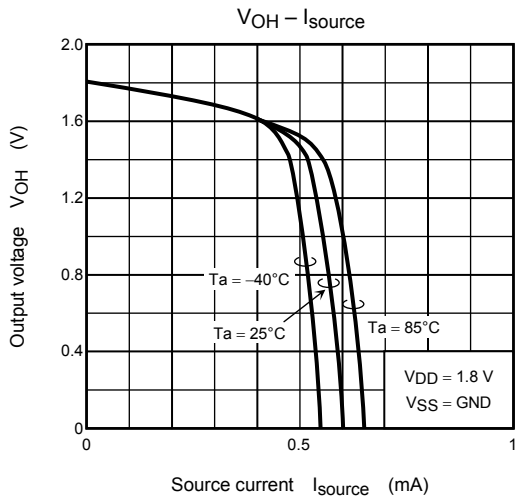
**6. I<sub>source</sub>**

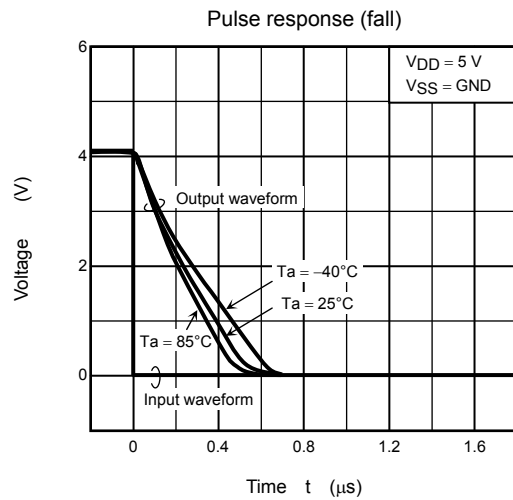
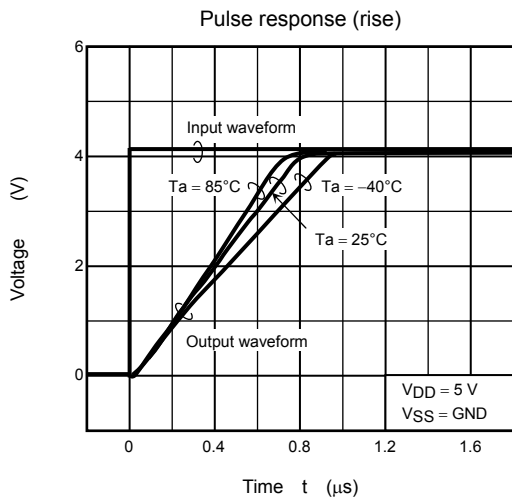
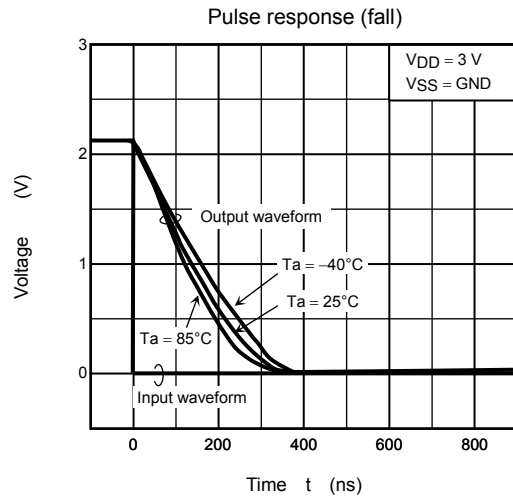
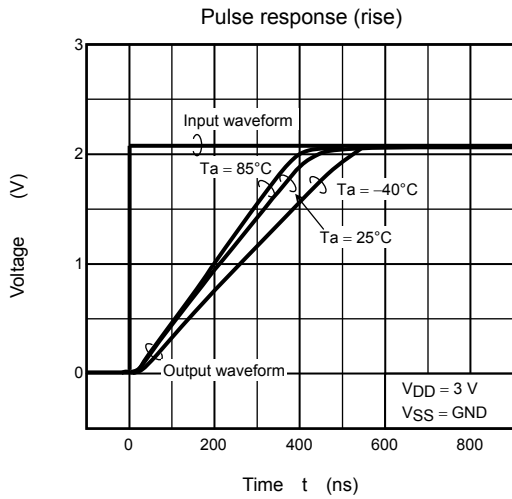
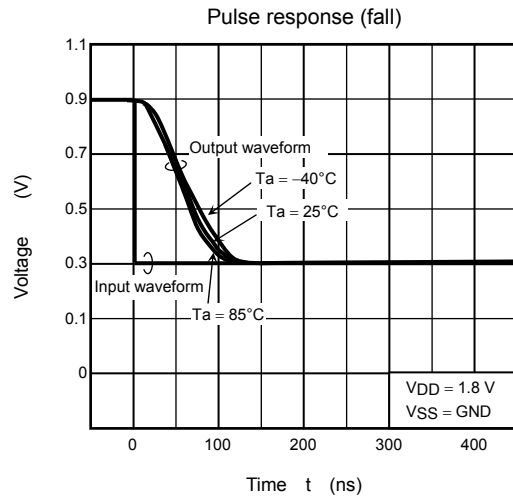
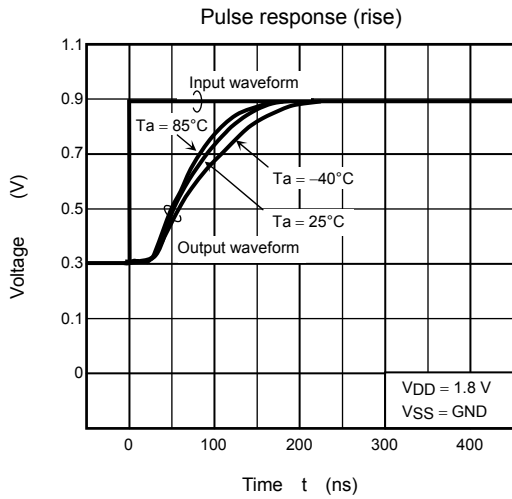


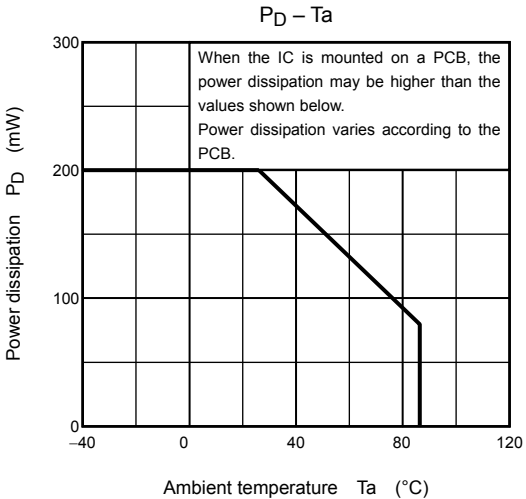
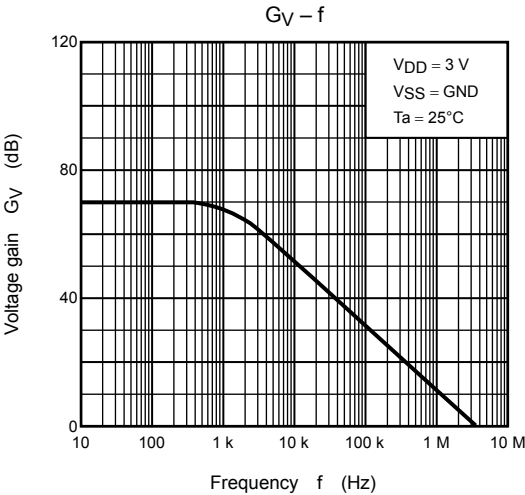
**7. I<sub>sink</sub>**









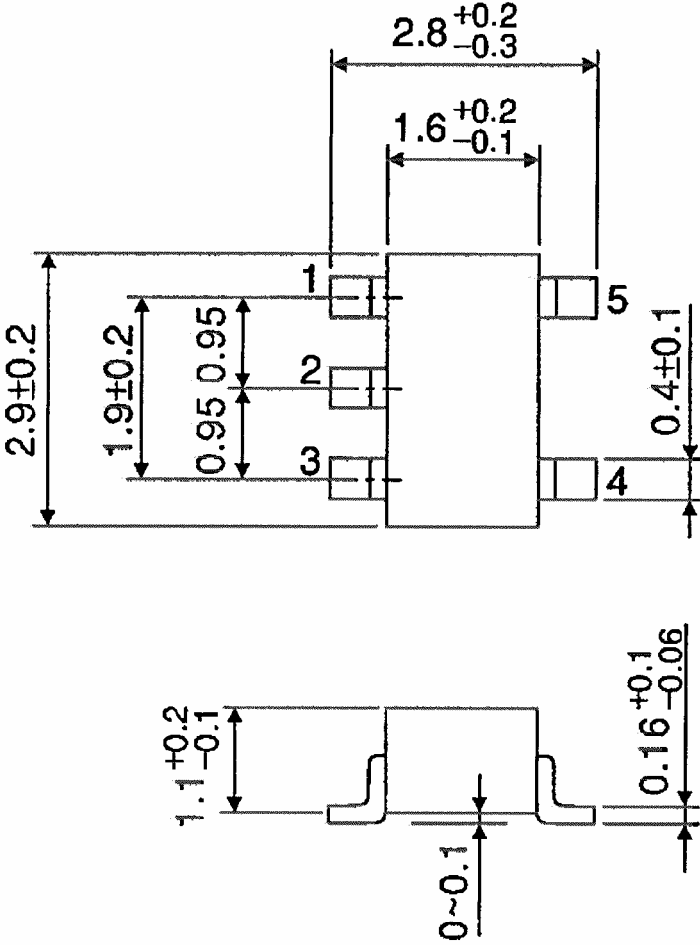




Package Dimensions

SSOP5-P-0.95

Unit : mm

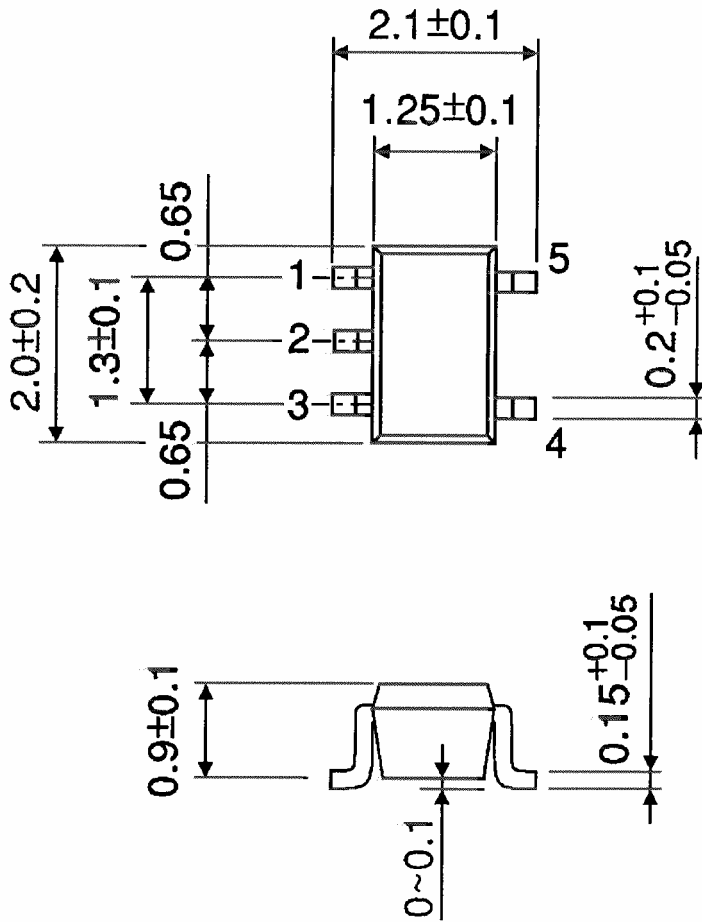


Weight: 0.014 g (typ.)

## Package Dimensions

SSOP5-P-0.65A

Unit : mm



Weight: 0.006 g (typ.)

**RESTRICTIONS ON PRODUCT USE**

20070701-EN GENERAL

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.  
In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document shall not be used or embedded to any downstream products of which manufacture, use and/or sale are prohibited under any applicable laws and regulations.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patents or other rights of TOSHIBA or the third parties.
- Please contact your sales representative for product-by-product details in this document regarding RoHS compatibility. Please use these products in this document in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances. Toshiba assumes no liability for damage or losses occurring as a result of noncompliance with applicable laws and regulations.