

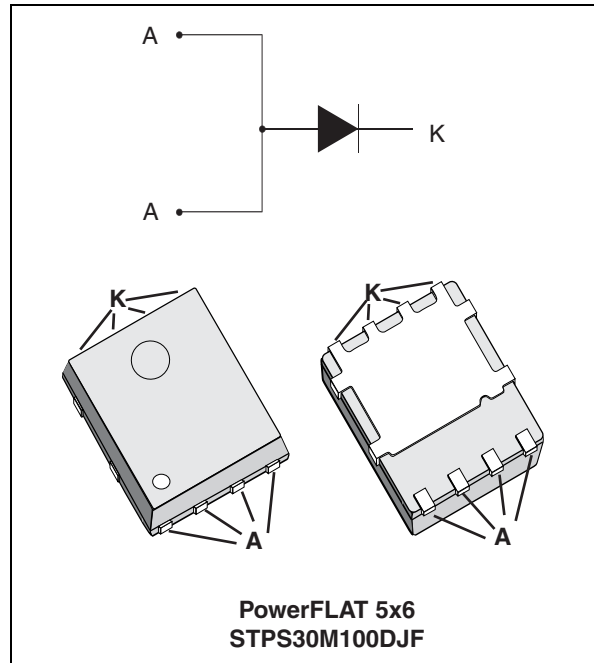
### Features

- Very low conduction losses
- Low forward voltage drop
- Low thermal resistance
- High specified avalanche capability
- High integration
- ECOPACK<sup>®</sup>2 compliant component

### Description

The STPS30M100DJF is a power Schottky rectifier suited for switch mode power supply and high frequency DC to DC converters.

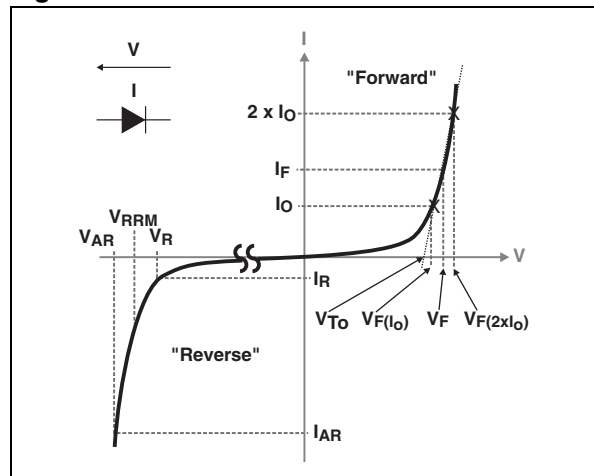
Packaged in PowerFLAT<sup>™</sup>, this device is intended to be used in adaptors requiring good efficiency at both low and high load. Its low profile was especially designed to be used in applications with space-saving constraints.



**Table 1. Device summary**

Symbol	Value
$I_{F(AV)}$	30 A
$V_{RRM}$	100 V
$T_j(max)$	150 °C
$V_F(typ)$	0.58 V

**Figure 1. Electrical characteristics (a)**



- a.  $V_{ARM}$  and  $I_{ARM}$  must respect the reverse safe operating area defined in [Figure 12](#).  $V_{AR}$  and  $I_{AR}$  are pulse measurements ( $t_p < 1 \mu s$ ).  $V_R$ ,  $I_R$ ,  $V_{RRM}$  and  $V_F$ , are static characteristics

TM: PowerFLAT is a trademark of STMicroelectronics

# 1 Characteristics

**Table 2. Absolute ratings (limiting values, anode terminals short circuited)**

Symbol	Parameter	Value	Unit	
V <sub>RRM</sub>	Repetitive peak reverse voltage	100	V	
I <sub>F(RMS)</sub>	Forward rms current	45	A	
I <sub>F(AV)</sub>	Average forward current $\delta = 0.5$	T <sub>c</sub> = 90 °C	30	A
I <sub>FSM</sub>	Surge non repetitive forward current	t <sub>p</sub> = 10 ms sinusoidal	200	A
P <sub>ARM</sub>	Repetitive peak avalanche power	t <sub>p</sub> = 1 $\mu$ s, T <sub>j</sub> = 25 °C	15000	W
V <sub>ARM</sub> (1)	Maximum repetitive peak avalanche voltage	t <sub>p</sub> < 1 $\mu$ s, T <sub>j</sub> < 150 °C I <sub>AR</sub> < 37.5 A	120	V
V <sub>ASM</sub> (1)	Maximum single pulse peak avalanche voltage	t <sub>p</sub> < 1 $\mu$ s, T <sub>j</sub> < 150 °C I <sub>AR</sub> < 37.5 A	120	V
T <sub>stg</sub>	Storage temperature range	-65 to +175	°C	
T <sub>j</sub>	Maximum operating junction temperature (2)	150	°C	

1. Refer to [Figure 12](#).

2.  $\frac{dP_{tot}}{dT_j} < \frac{1}{R_{th(j-a)}}$  condition to avoid thermal runaway for a diode on its own heatsink

**Table 3. Thermal resistance**

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case	2.5	°C/W

**Table 4. Static electrical characteristics (anode terminals short circuited)**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit	
I <sub>R</sub> (1)	Reverse leakage current	T <sub>j</sub> = 25 °C	V <sub>R</sub> = V <sub>RRM</sub>	-	-	100	$\mu$ A
		T <sub>j</sub> = 125 °C		-	10	40	mA
V <sub>F</sub> (1)	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 15 A	-	-	0.82	V
		T <sub>j</sub> = 125 °C		-	0.58	0.66	
		T <sub>j</sub> = 25 °C	I <sub>F</sub> = 30 A	-	-	0.96	
		T <sub>j</sub> = 125 °C		-	0.66	0.73	

1. Pulse test: t<sub>p</sub> = 380  $\mu$ s,  $\delta$  < 2%

To evaluate the conduction losses use the following equation:

$$P = 0.65 \times I_{F(AV)} + 0.00267 \times I_{F(RMS)}^2$$

Figure 2. Average forward power dissipation versus average forward current

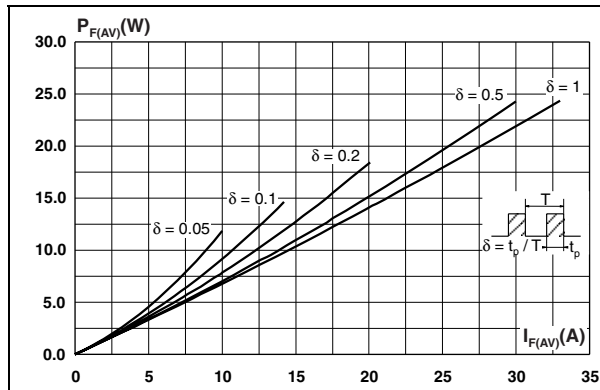


Figure 3. Average forward current versus ambient temperature (delta = 0.5)

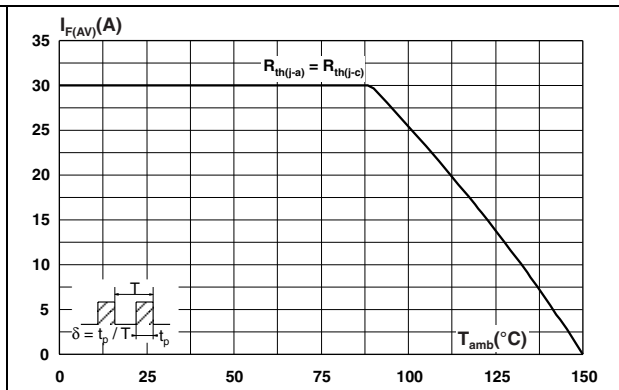


Figure 4. Normalized avalanche power derating versus pulse duration

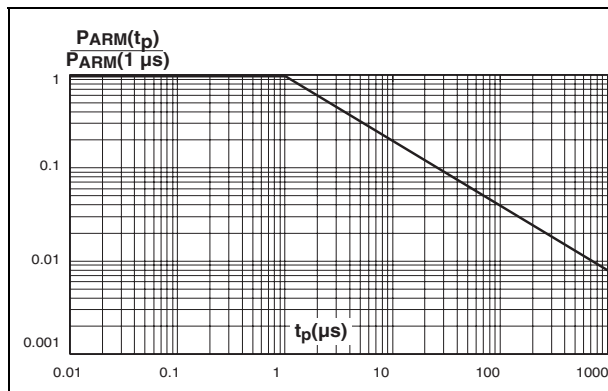


Figure 5. Normalized avalanche power derating versus junction temperature

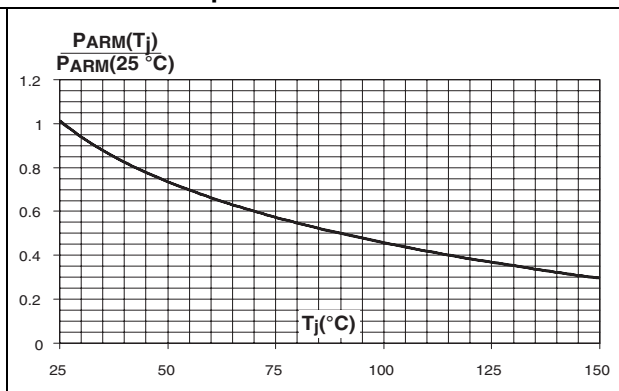


Figure 6. Non repetitive surge peak forward current versus overload duration (maximum values)

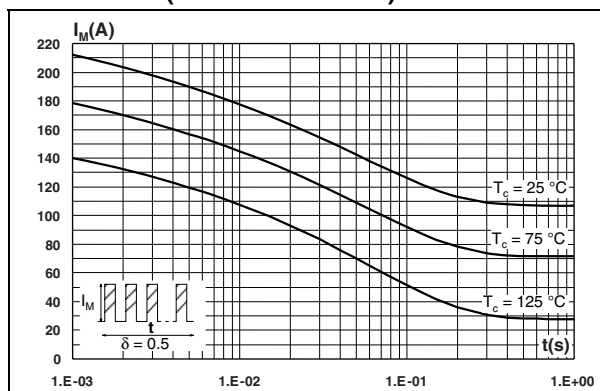


Figure 7. Relative variation of thermal impedance junction to case versus pulse duration

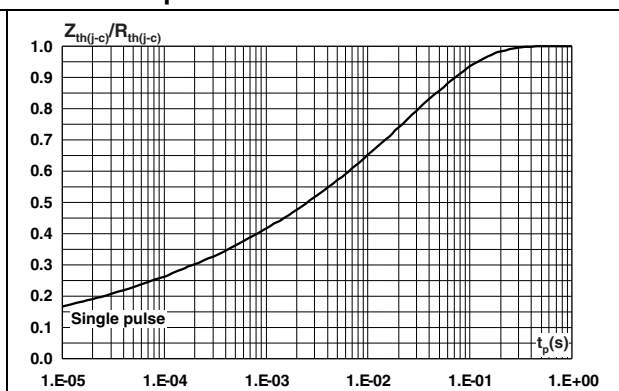


Figure 8. Reverse leakage current versus reverse voltage applied (typical values)

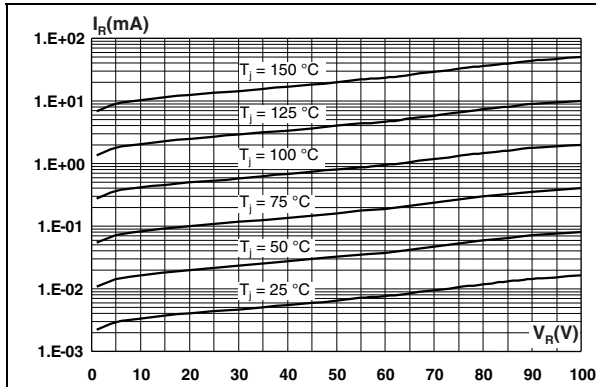


Figure 9. Junction capacitance versus reverse voltage applied (typical values)

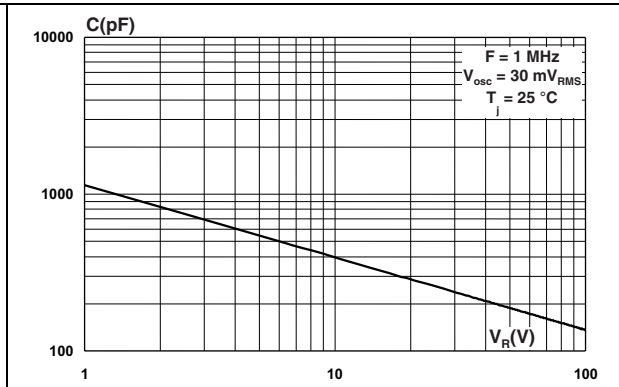


Figure 10. Forward voltage drop versus forward current

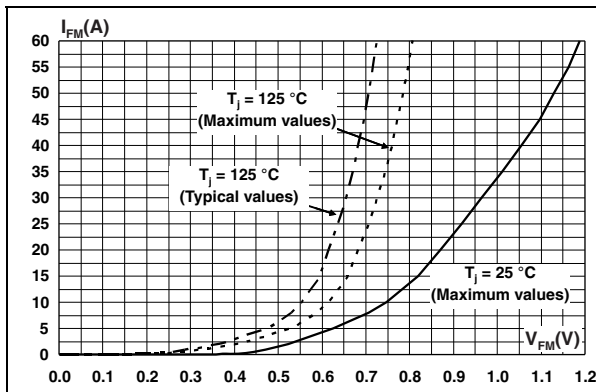


Figure 11. Thermal resistance junction to ambient versus copper surface under tab

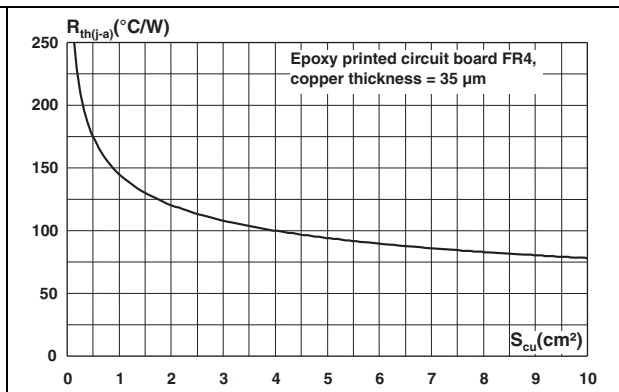
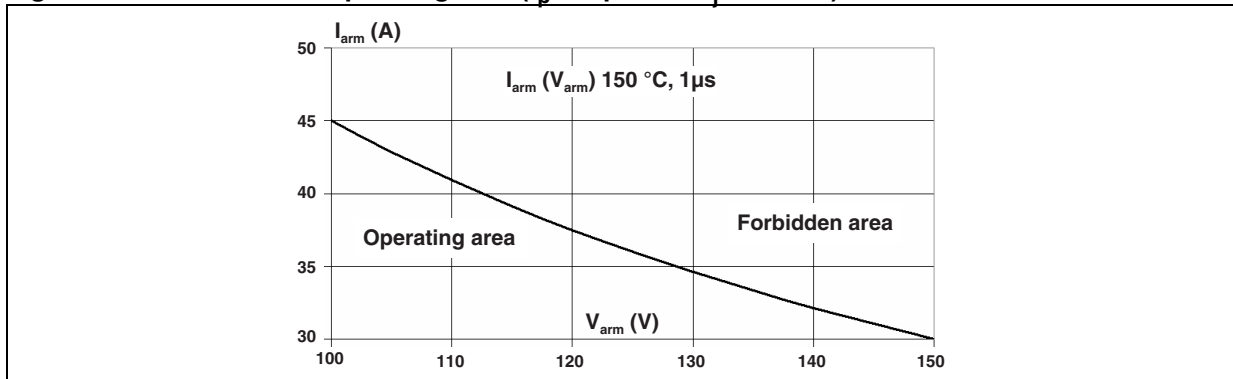


Figure 12. Reverse safe operating area ( $t_p < 1 \mu s$  and  $T_j < 150 \text{ °C}$ )



## 2 Package information

- Epoxy meets UL94,V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK® is an ST trademark.

**Table 5. PowerFLAT 5x6 dimensions**

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.80		1.00	0.031		0.039
A1	0.02		0.05	0.001		0.002
A2		0.25			0.010	
b	0.30		0.50	0.012		0.020
D		5.20			0.205	
D2	4.11		4.31	0.162		0.170
e		1.27			0.050	
E		6.15			0.242	
E2	3.50		3.70	0.138		0.146
L	0.50		0.80	0.020		0.031
K	1.275		1.575	0.050		0.062

**Figure 13. Footprint (dimensions in mm)**

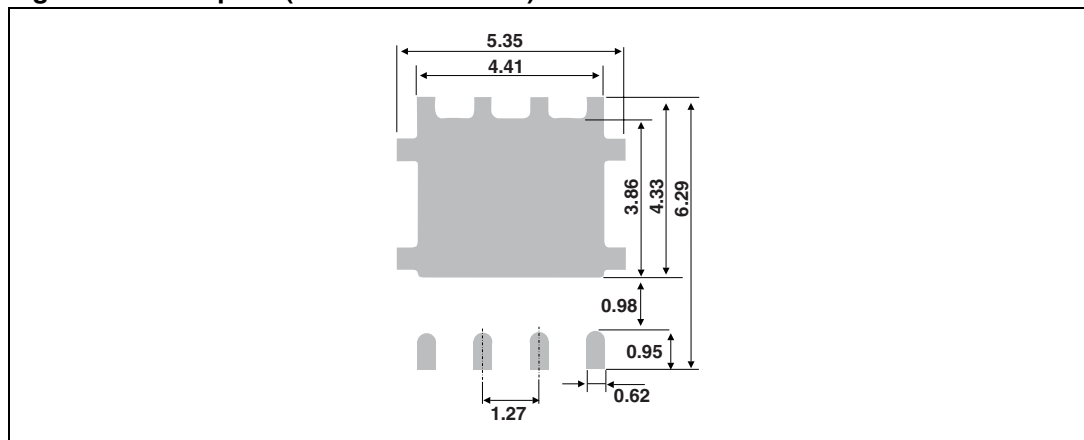
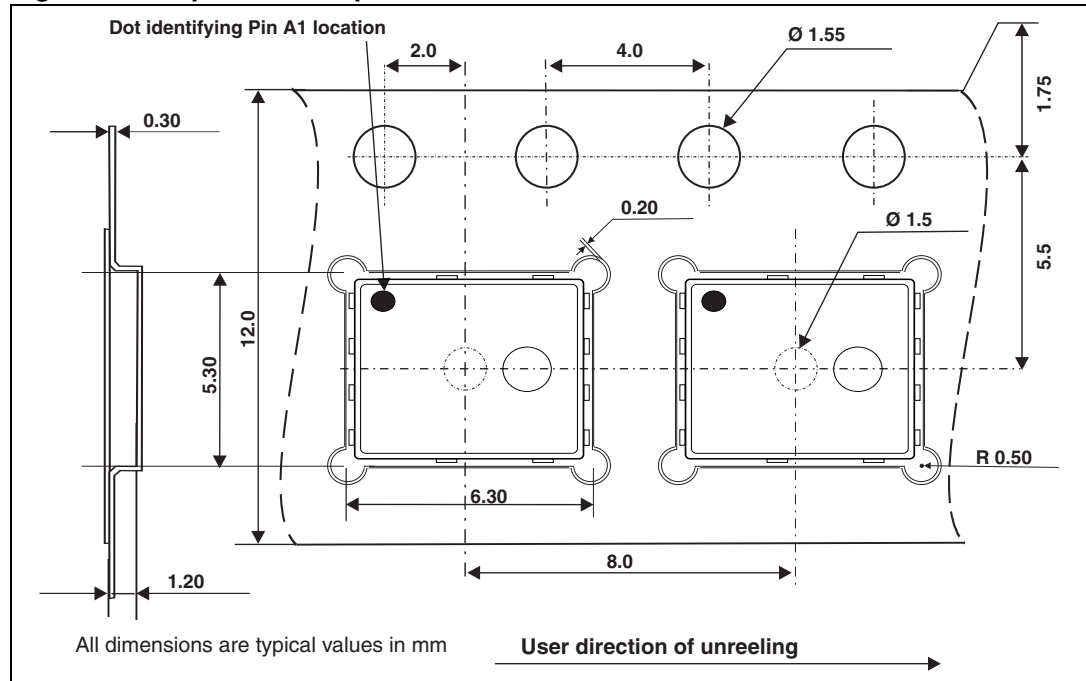


Figure 14. Tape and reel specifications



### 3 Ordering information

**Table 6. Ordering information**

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS30M100DJF-TR	PS30 M100	PowerFLAT 5x6	95 mg	3000	Tape and reel

### 4 Revision history

**Table 7. Document revision history**

Date	Revision	Changes
06-Nov-2009	1	First issue.
30-Jul-2010	2	Replace Power QFN with PowerFLAT.
15-Jan-2011	3	Add reference E in <a href="#">Table 5</a> .
20-May-2011	4	Update all package illustrations. Updated base quantity and marking in <a href="#">Table 6</a> . Updated terminal identification in captions of <a href="#">Table 2</a> and <a href="#">Table 4</a> . Added <a href="#">Figure 14</a> .

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