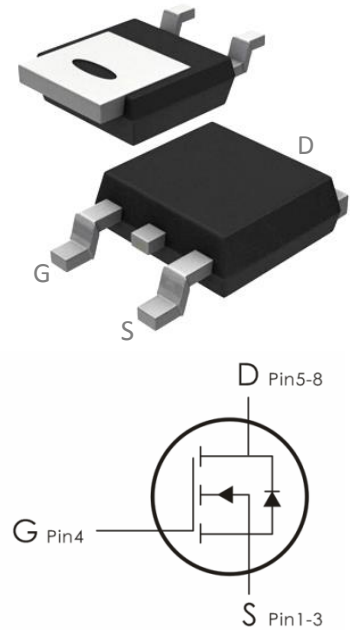


Description:

This N-Channel MOSFET uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=30V, I_D=80A, R_{DS(ON)} < 2.5m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current,Package Limited- $T_C=25^\circ C$	80	A
	Continuous Drain Current,Package Limited- $T_C=100^\circ C$	73	
	Continuous Drain Current,Silicon- $T_C=25^\circ C$	163	
I_{DM}	Pulsed Drain Current(Note2)	240	
E_{AS}	Avalanche Energy, Single Pulse (Note 3)	160	mJ
I_{AR}	Avalanche Current, Repetitive (Note 2)	20	A
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$
T_{LEAD}	Lead Temperature (Soldering, 10 sec)	260	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{thJC}	Thermal Resistance, Junction to Case	1.85	°C/W

Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

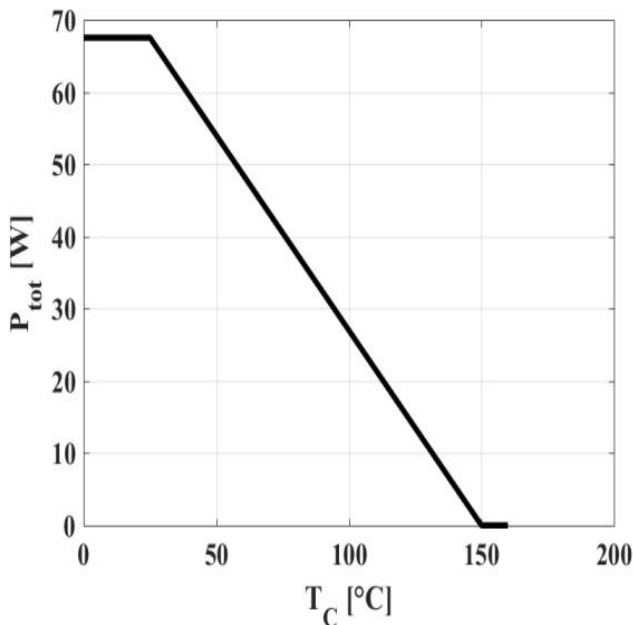
Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\ \mu\text{A}$	30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=30V$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0A$	---	---	± 100	nA
On Characteristics						
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=0.25\text{mA}$	1.2	1.7	2.1	V
$R_{DS(ON)}$	Drain-Source On Resistance	$V_{GS}=10V, I_D=20A$	---	2.2	2.5	$\text{m}\Omega$
		$V_{GS}=4.5V, I_D=20A$	---	3.1	3.5	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$	---	2.5	---	nF
C_{oss}	Output Capacitance		---	580	---	pF
C_{rss}	Reverse Transfer Capacitance		---	114	---	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD}=15V, V_{GS}=10V$ $R_G=1.6\ \Omega, I_D=20A$	---	11	---	ns
t_r	Rise Time		---	8	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	46	---	ns
t_f	Fall Time		---	8	---	ns

Q_g	Gate Charge Total	$V_{GS}=0 \text{ to } 10V, V_{DD}=15V,$ $I_D=20A$	---	38.6	---	nC
Q_{gs}	Gate-Source Charge		---	5.3	---	nC
Q_{gd}	Gate-Drain Charge		---	5	---	nC
$V_{plateau}$	Gate Plateau Voltage		---	2.8	---	V
Drain-Source Diode Characteristics						
Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{SD}	Source-Drain Diode Forward Voltage	$V_{GS}=0V, I_{SD}=20A$	---	0.79	1.1	V
t_{rr}	Reverse Recovery Time	$V_R=15V, I_F=20A$	---	25	---	ns
q_{rr}	Reverse Recovery Charge	$di_F/dt=100A/us$	---	50	---	nC

Notes:

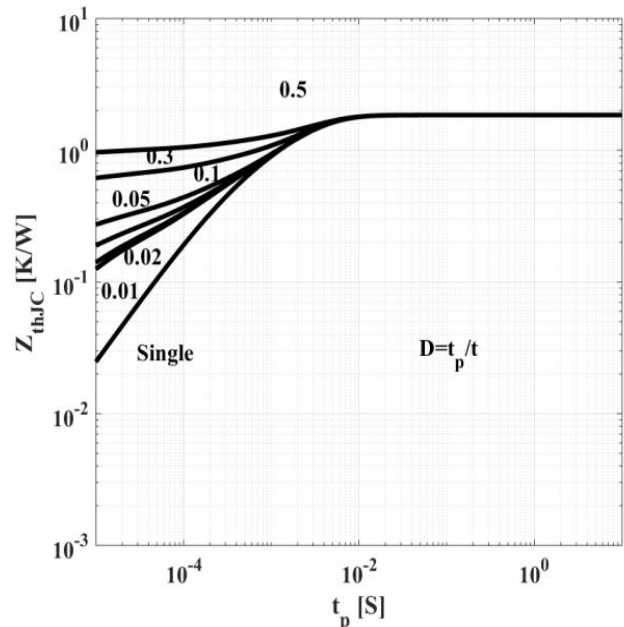
1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. $I_{AS} = 20.0A, V_{DD} = 15V, R_G = 25\Omega, \text{ Starting } T_J = 25^\circ C$

Typical Characteristics: ($T_C=25^\circ C$ unless otherwise noted)



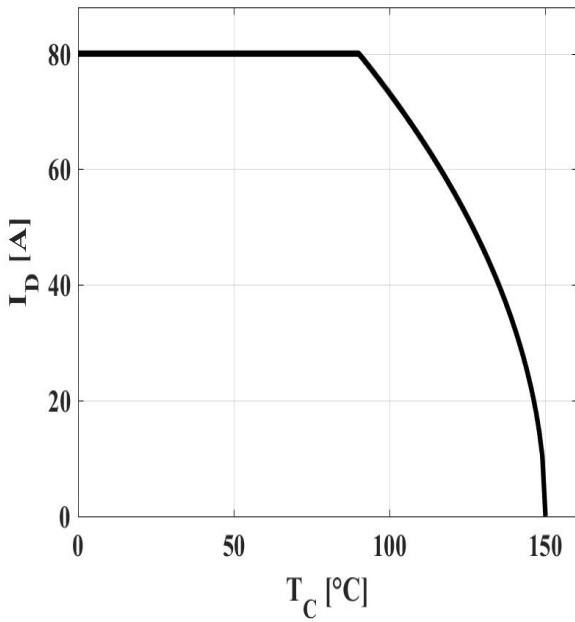
$$P_{tot} = f(T_C)$$

Figure 1: Power Dissipation



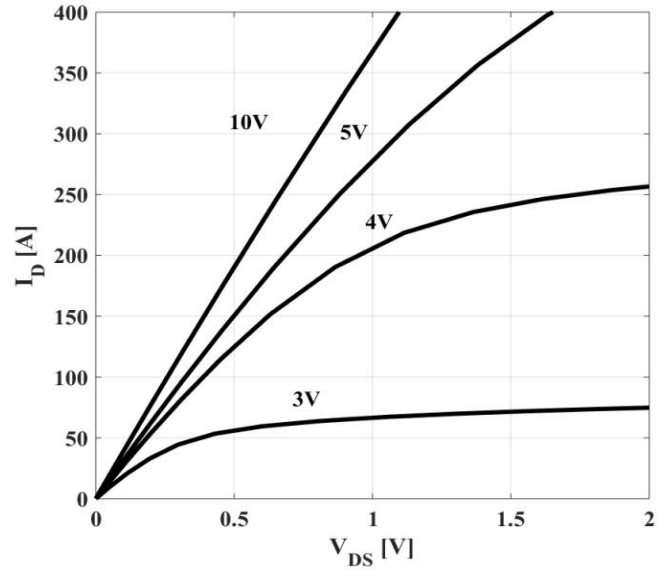
$$Z_{(thjC)} = f(t_p); \text{ parameter: } D = t_p/T$$

Figure 2: Max. Transient Thermal Impedance



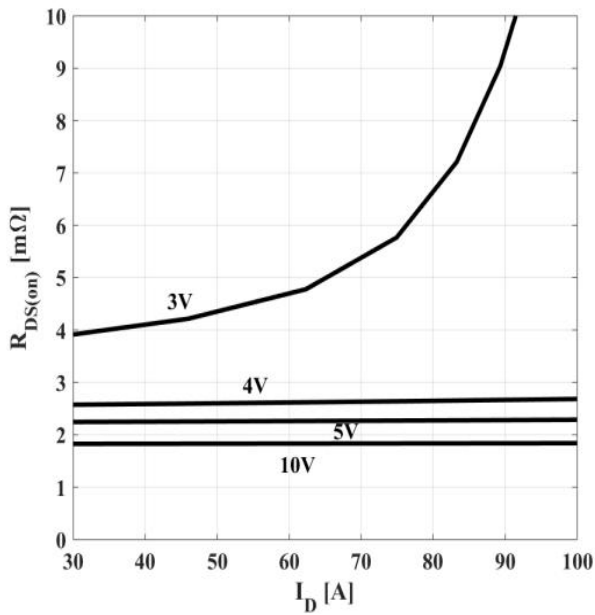
$$I_D = f(T_C); V_{GS} \geq 10V$$

Figure 3: Drain Current



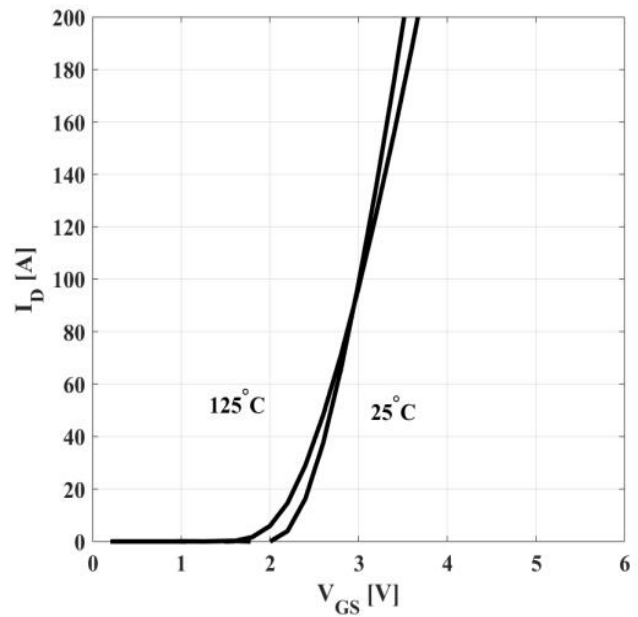
$$I_D = f(V_{DS}); T_j = 25^\circ C; \text{parameter: } V_{GS}$$

Figure 4: Typ. Output Characteristics



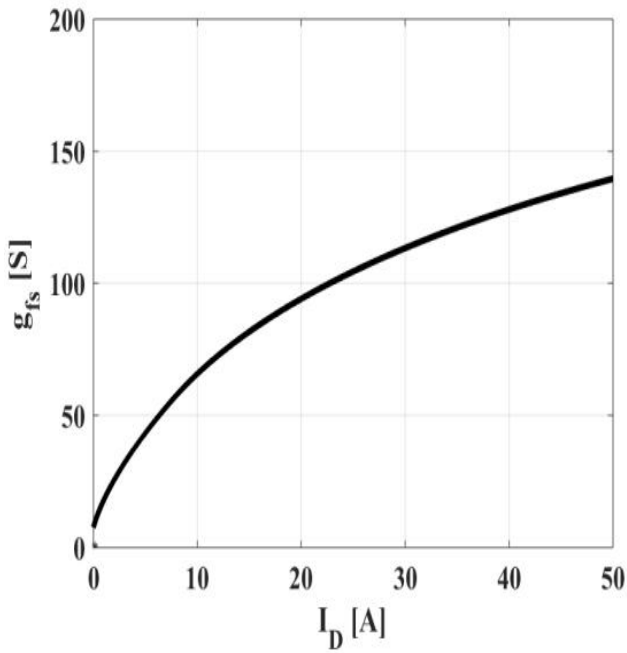
$$R_{DS(ON)} = f(I_D); T_j = 25^\circ C; \text{parameter: } V_{GS}$$

Figure 5: Typ. Drain-Source On-State Resistance



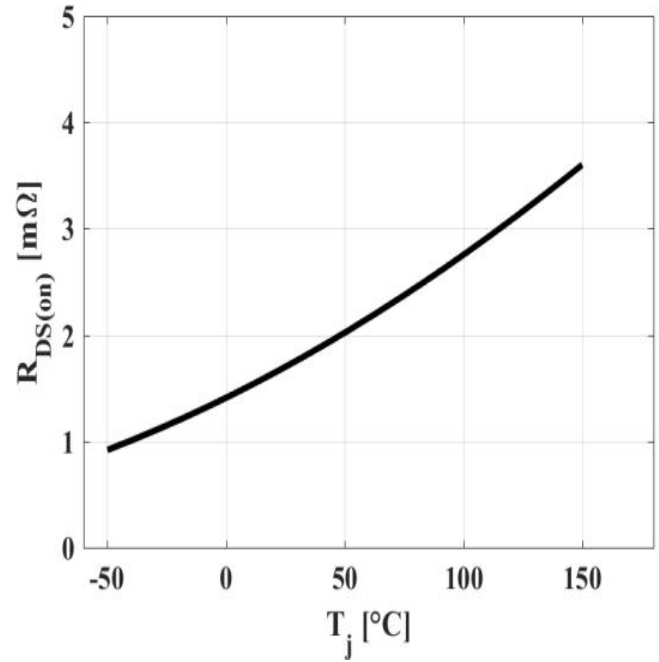
$$I_D = f(V_{GS}); |V_{DS}| > 2|I_D|R_{DS(on)max}; \text{parameter: } T_j$$

Figure 6: Typ. Transfer Characteristics



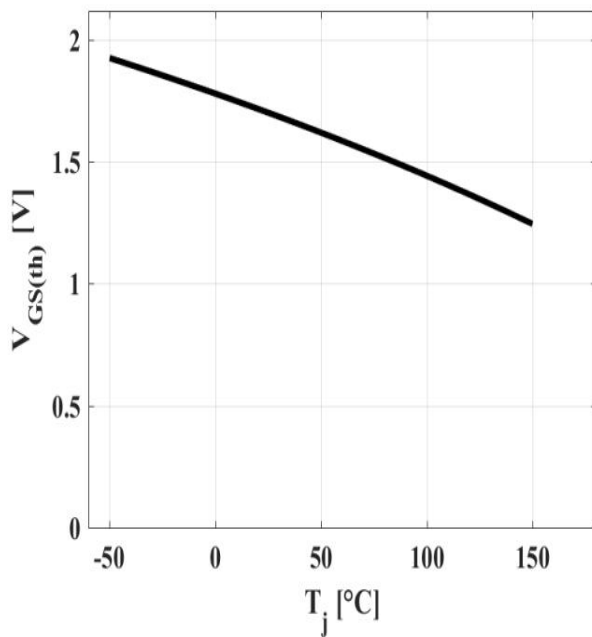
$$g_{fs}=f(I_D); T_j=25^{\circ}\text{C}$$

Figure 7: Typ. Forward Transconductance



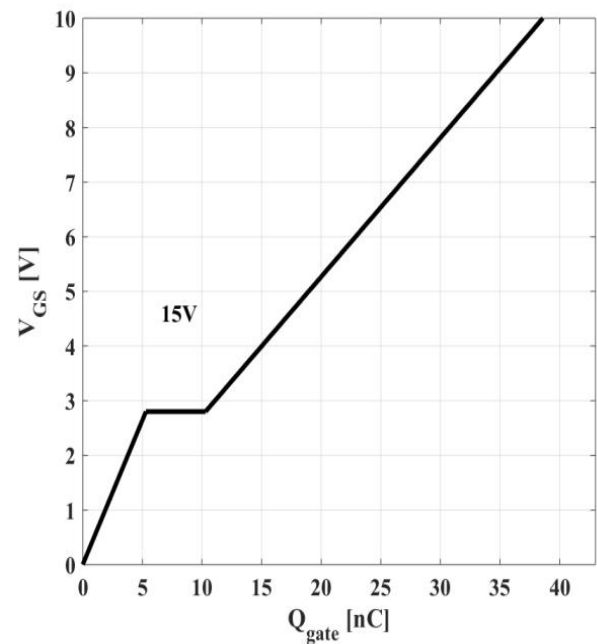
$$R_{DS(ON)}=f(T_j); I_D=20\text{A}; V_{GS}=10\text{V}$$

Figure 8 : Typ. Drain-Source On-State Resistance



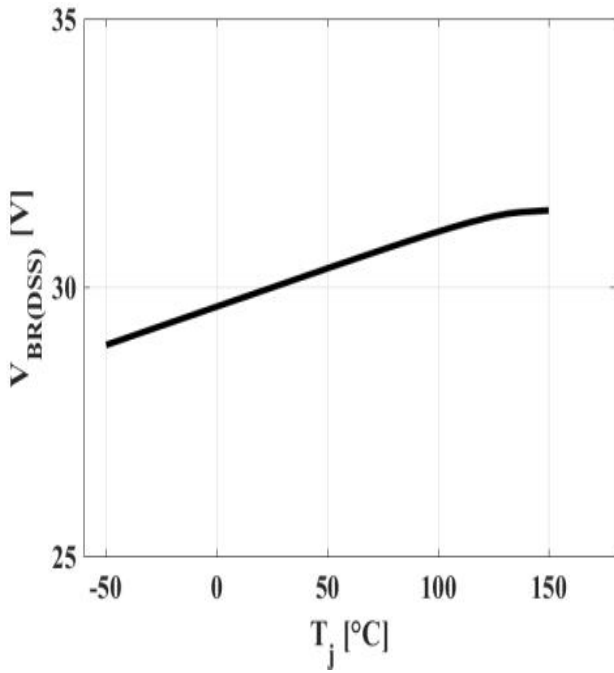
$$V_{GS(th)}=f(T_j); V_{GS}=V_{DS}; I_{DS}=250\mu\text{A}$$

Figure 9 : Typ. Gate Threshold Voltage



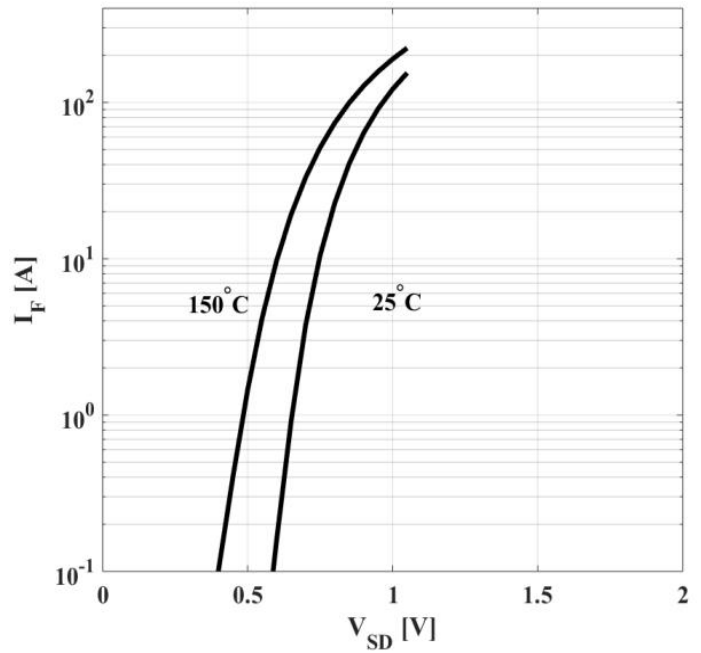
$$V_{GS}=f(Q_{gate}), I_D=20\text{A pulsed}$$

Figure 10: Typ. Gate Charge



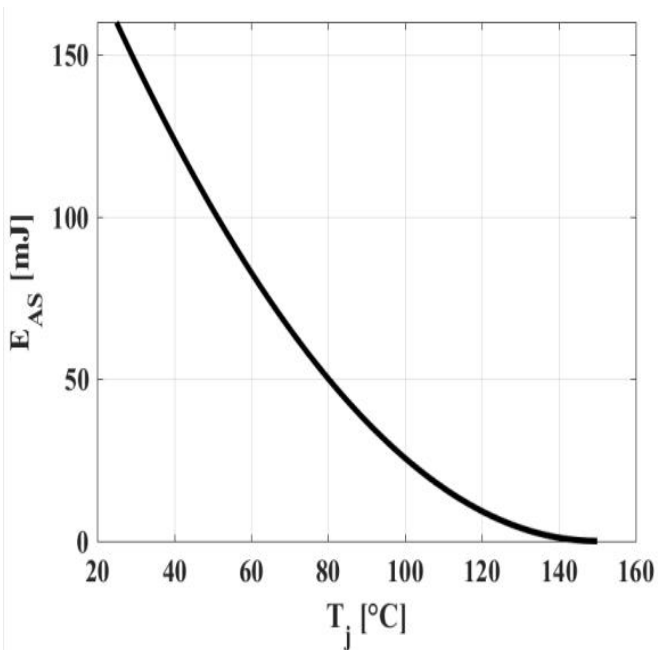
$$V_{BR(DSS)} = f(T_j); I_D = 1\text{mA}$$

Figure 11: Drain-Source Breakdown Voltage



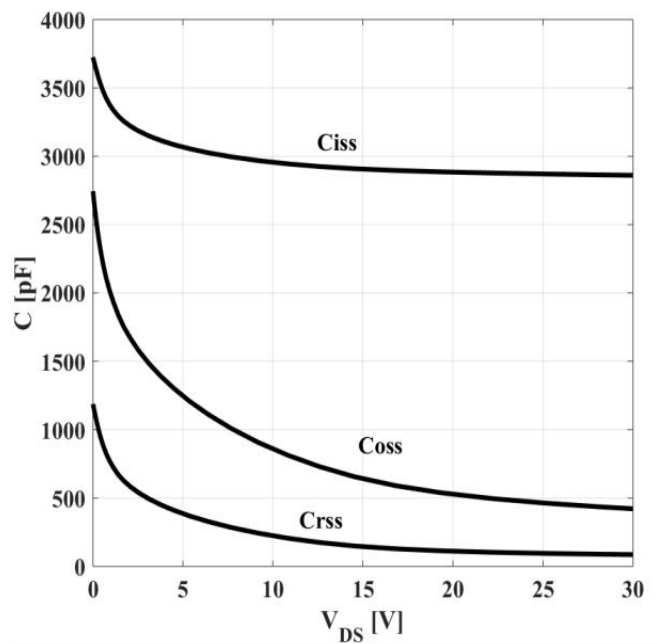
$$I_F = f(V_{SD}); \text{parameter: } T_j$$

Figure 12: Forward Characteristics of Reverse Diode



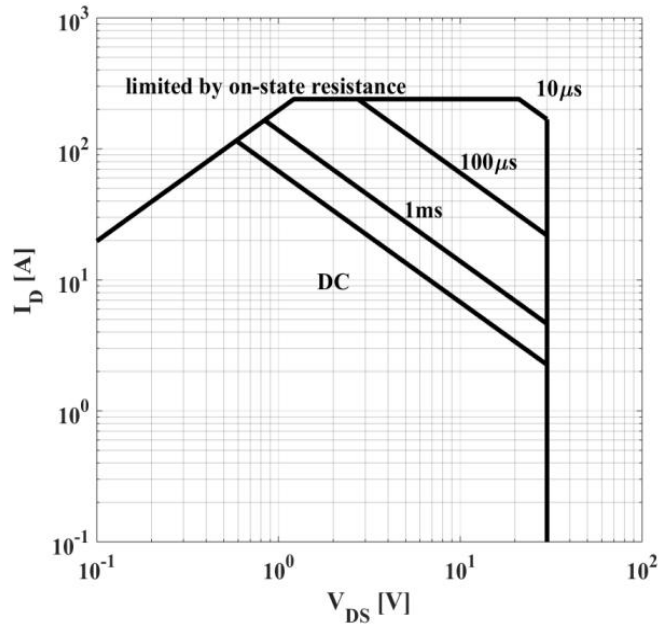
$$E_{AS} = f(T_j); I_D = 20.0\text{A}; V_{DD} = 15\text{V}$$

Figure 13: Avalanche Energy



$$C = f(V_{DS}); V_{GS} = 0; f = 1\text{MHz}$$

Figure 14: Typ. Capacitances



$I_D = f(V_{DS})$; $T_C = 25^\circ\text{C}$; $V_{GS} > 7\text{V}$; parameter: t_p

Figure 15: Safe Operating Area



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