IFX1050G

High Speed CAN-Transceiver

Data Sheet

Rev. 1.0, 2009-05-14

Standard Products

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High Speed CAN-Transceiver **IFX1050G**

RoHS

1 Overview

PG-DSO-8

Features

- CAN data transmission rates from 1 kBaud up to 1 MBaud
- Receive - Only Mode and Stand - By Mode
- Optimized Electromagnetic Compatibility (EMC)
- Optimized for a high immunity against Electromagnetic Interference (EMI)
- Bus pins are short circuit proof
- Over - temperature protection
- Very wide temperature range (-40 °C up to 125 °C)
- Green Product (RoHS compliant)

Description

The IFX1050G is optimized for high speed differential mode data transmission in industrial applications and it is compliant to ISO11898-2. The transceiver IFX1050G works as an interface between the CAN protocol controller and the physical differential bus in High Speed CAN applications. It supports data transmission rates from 1 kBaud up to 1 MBaud.

The IFX1050G has three different operation modes:

The Normal Mode, the Receive - Only Mode and the Stand - By Mode. The mode selection is controlled by the logical input pins RM and INH.

The IC is based on the **S**mart **P**ower **T**echnology SPT® which allows bipolar and CMOS control circuitry in accordance with DMOS power devices existing on the same monolithic circuit. The IFX1050G is designed to withstand the severe conditions in industrial applications and provides excellent EMC performance within a broad frequency range.

Type	Package	Marking
IFX1050G	PG-DSO-8	IFX1050G

2 Block Diagram

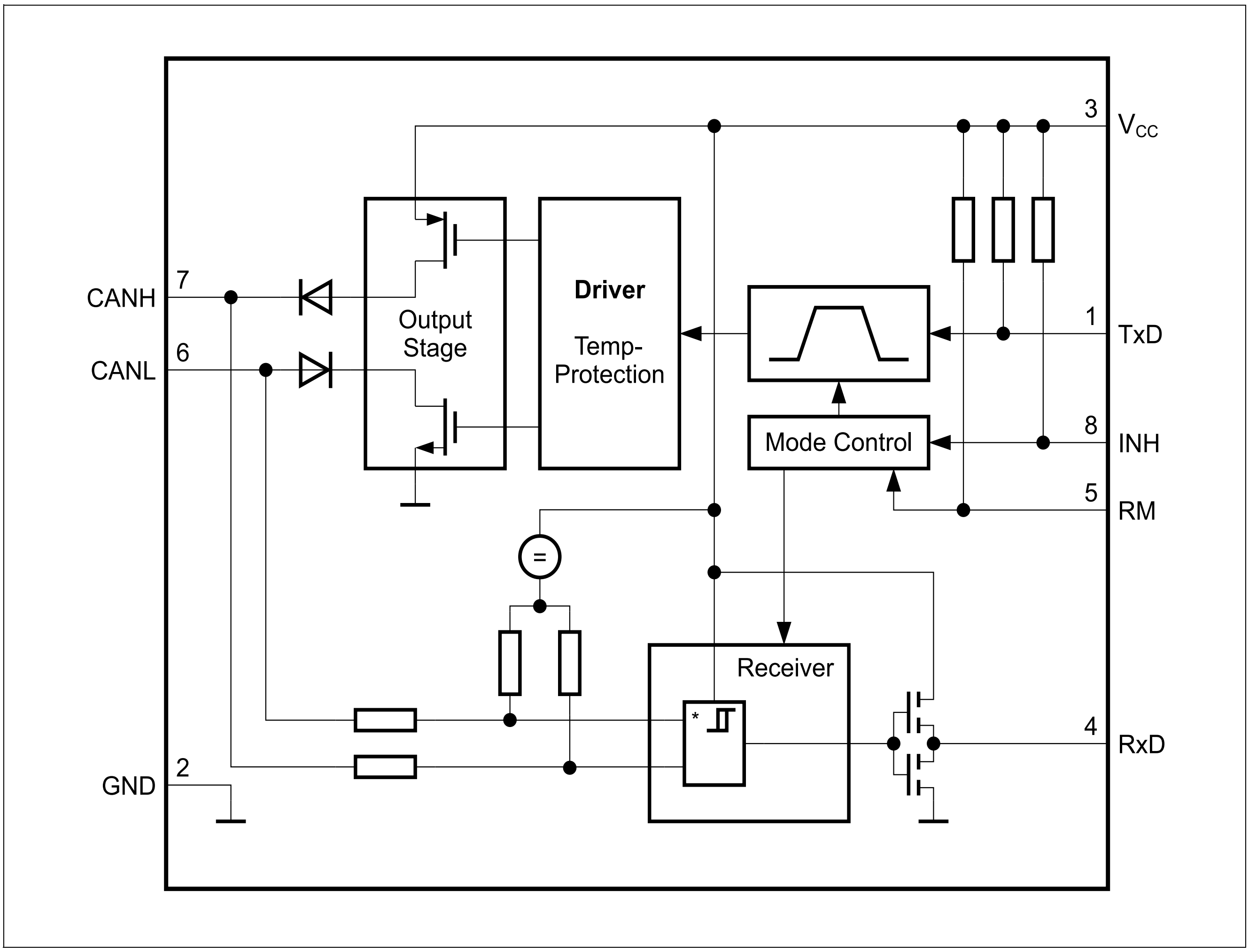

Figure 1 Block Diagram

3 Pin Configuration

3.1 Pin Assignment

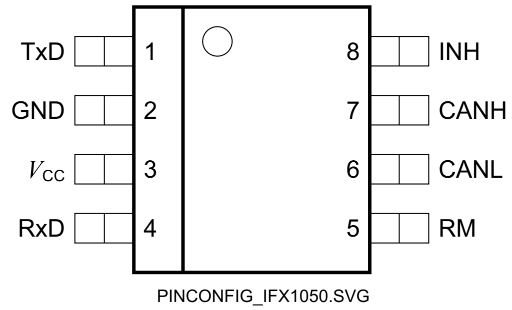

Figure 2 Pin Configuration

3.2 Pin Definitions and Functions

Pin	Symbol	Function
1	TxD	**CAN transmit data input;** 20 kΩ pull - up, "LOW" in dominant state
2	GND	**Ground**
3	V_{CC}	**5 V Supply input**
4	RxD	**CAN receive data output;** "LOW" in dominant state, integrated pull - up
5	RM	**Receive - Only input;** control input, integrated 20 kΩ pull - up, "LOW" to activate Receive - Only Mode
6	CANL	**Low line I/O;** "LOW" in dominant state
7	CANH	**High line I/O;** "HIGH" in dominant state
8	INH	**Inhibit Input;** control input, 20 kΩ pull - up, "LOW" to activate Normal Mode

4 Operation Modes

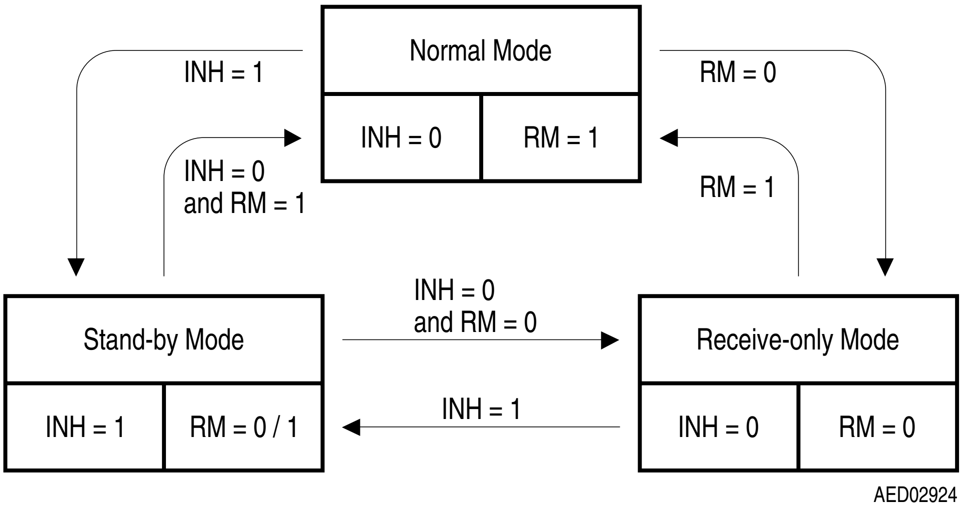

Figure 3 Mode State Diagram

The IFX1050G is equipped with three different operation modes.

4.1 Normal Mode

In the Normal Mode the device is able to receive data from the CAN bus and to transmit messages to the CAN bus. The IFX1050G enters Normal Mode by setting the INH input to logical “LOW” and the RM input to logical “HIGH” (see **Figure 3**).

4.2 Stand - By Mode

Stand - By Mode is a Low - Power mode with reduced current consumption on the power supply V_{CC}.
In Stand - By Mode the receiver and the transceiver of the IFX1050G are disabled and the device can not receive any data from the CAN bus, nor transmit any data to the CAN bus. The IFX1050G enters Stand - By Mode by setting the INH input to logical “HIGH” (see **Figure 3**).

When the Stand - By mode is not used the INH pin has to be connected to GND in order to switch the IFX1050G permanently into Normal Mode.

4.3 Receive - Only Mode

The Receive - Only Mode can be used for diagnostic purposes (to check the bus connections between the nodes) as well as to prevent the bus being blocked by a faulty permanent dominant TxD input signal. In Receive - Only Mode the output stage of the transceiver IFX1050G is disabled. The IFX1050G can not send any data to the CAN bus, but is still able to receive data from the CAN bus. The IFX1050G enters Receive - Only Mode by setting the RM input and the INH input to logical “LOW” (see **Figure 3**).

In case the Receive - Only Mode is not used, the RM pin can be left open or it can be also connected to the power supply V_{CC}.

5 Electrical Characteristics

5.1 Absolute Maximum Ratings

Table 1 Absolute Maximum Ratings

Parameter	Symbol	Limit Values		Unit	Remarks
		Min.	Max.		
Voltages					
Supply voltage	V_{CC}	-0.3	6.5	V	–
CAN input voltage (CANH, CANL)	$V_{CANH/L}$	-40	40	V	–
Logic voltages at INH, RM, TxD, RxD	V_I	-0.3	V_{CC}	V	0 V < V_{CC} < 5.5 V
Electrostatic discharge voltage at CANH, CANL	V_{ESD}	-6	6	kV	human body model (100 pF via 1.5 kΩ)
Electrostatic discharge voltage	V_{ESD}	-2	2	kV	human body model (100 pF via 1.5 kΩ)
Temperatures					
Junction temperature	T_j	-40	160	°C	–

Note: Stresses above the ones listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note: Integrated protection functions are designed to prevent IC destruction under fault conditions described in the data sheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous repetitive operation.

5.2 Functional Range

Table 2 Functional Range

Parameter	Symbol	Limit Values		Unit	Remarks
		Min.	Max.		
Supply voltage	V_{CC}	4.5	5.5	V	–
Junction temperature	T_j	-40	125	°C	–
Thermal Shutdown (junction temperature)					
Thermal shutdown temperature	T_{jsD}	160	200	°C	10 °C hysteresis

Note: Within the functional or operating range, the IC operates as described in the circuit description. The electrical characteristics are specified within the conditions given in the Electrical Characteristics table.

5.3 Thermal Resistance

Pos.	Parameter	Symbol	Limit Values			Unit	Conditions
			Min.	Typ.	Max.		
5.3.1	Junction to Ambient[1)]	R_{thJA}	–	–	185	K/W	

1) Not subject to production test, specified by design.

6 Electrical Characteristics

Table 3 Electrical Characteristics

4.5 V < V_{CC} < 5.5 V; R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; -40 °C < T_j < 125 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		Min.	Typ.	Max.		
Current Consumption						
Current consumption	I_{CC}	–	6	10	mA	Recessive state; V_{TxD} = V_{CC}
Current consumption	I_{CC}	–	45	70	mA	Dominant state; V_{TxD} = 0 V
Current consumption	I_{CC}	–	6	10	mA	Receive - Only Mode; RM = "LOW"
Current consumption	$I_{CC,stb}$	–	1	10	µA	Stand - By Mode; TxD = RM = "High"
Receiver Output RxD						
HIGH level output current	$I_{RD,H}$	–	-4	-2	mA	V_{RD} = 0.8 × V_{CC}, V_{diff} < 0.4 V[1)]
LOW level output current	$I_{RD,L}$	2	4	–	mA	V_{RD} = 0.2 × V_{CC}, V_{diff} > 1 V[1)]
Transmission Input TxD						
HIGH level input voltage threshold	$V_{TD,H}$	–	0.5 × V_{CC}	0.7 × V_{CC}	V	Recessive state
LOW level input voltage threshold	$V_{TD,L}$	0.3 × V_{CC}	0.4 × V_{CC}	–	V	Dominant state
TxD pull-up resistance	R_{TD}	10	25	50	kΩ	–
Inhibit Input (pin INH)						
HIGH level input voltage threshold	$V_{INH,H}$	–	0.5 × V_{CC}	0.7 × V_{CC}	V	Stand - By Mode;
LOW level input voltage threshold	$V_{INH,L}$	0.3 × V_{CC}	0.4 × V_{CC}	–	V	Normal Mode
INH pull-up resistance	R_{INH}	10	25	50	kΩ	–
Receive only Input (pin RM)						
HIGH level input voltage threshold	$V_{RM,H}$	–	0.5 × V_{CC}	0.7 × V_{CC}	V	Normal Mode
LOW level input voltage threshold	$V_{RM,L}$	0.3 × V_{CC}	0.4 × V_{CC}	–	V	Receive - Only Mode
RM pull-up resistance	R_{RM}	10	25	50	kΩ	–

Table 3 Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; -40 °C < T_j < 125 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		Min.	Typ.	Max.		
Bus Receiver						
Differential receiver threshold voltage, recessive to dominant edge	$V_{diff,d}$	–	0.75	0.90	V	-7 V < (V_{CANH}, V_{CANL}) < 12 V V_{diff} = V_{CANH} - V_{CANL}
Differential receiver threshold voltage dominant to recessive edge	$V_{diff,r}$	0.50	0.60	–	V	-7 V < (V_{CANH}, V_{CANL}) < 12 V V_{diff} = V_{CANH} - V_{CANL}
Common Mode Range	CMR	-7	–	12	V	V_{CC} = 5 V
Differential receiver hysteresis	$V_{diff,hys}$	–	150	–	mV	–
CANH, CANL input resistance	R_i	10	20	30	kΩ	Recessive state
Differential input resistance	R_{diff}	20	40	60	kΩ	Recessive state
Bus Transmitter						
CANL/CANH recessive output voltage	$V_{CANL/H}$	0.4 × V_{CC}	–	0.6 × V_{CC}	V	V_{TxD} = V_{CC}
CANH, CANL recessive output voltage difference V_{diff} = V_{CANH} - V_{CANL}, no load	V_{diff}	-1	–	0.05	V	V_{TxD} = V_{CC}
CANL dominant output voltage	V_{CANL}	–	–	2.0	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANH dominant output voltage	V_{CANH}	2.8	–	–	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANH, CANL dominant output voltage difference V_{diff} = V_{CANH} - V_{CANL}	V_{diff}	1.5	–	3.0	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANL short circuit current	I_{CANLsc}	50	120	200	mA	$V_{CANLshort}$ = 18 V
CANH short circuit current	I_{CANHsc}	-200	-120	-50	mA	$V_{CANHshort}$ = 0 V
Output current CANH / CANL	$I_{CANH/L,lk}$	-50	-300	-400	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = -7 V
		-50	-100	-150	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = -2 V
Output current CANH / CANL	$I_{CANH/L,lk}$	50	280	400	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = 7 V
		50	100	150	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = 2 V

Table 3 Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; -40 °C < T_j < 125 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		Min.	Typ.	Max.		
Dynamic CAN-Transceiver Characteristics						
Propagation delay TxD-to-RxD LOW (recessive to dominant)	$t_{d(L),TR}$	–	150	280	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay TxD-to-RxD HIGH (dominant to recessive)	$t_{d(H),TR}$	–	150	280	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay TxD LOW to bus dominant	$t_{d(L),T}$	–	100	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V
Propagation delay TxD HIGH to bus recessive	$t_{d(H),T}$	–	100	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V
Propagation delay bus dominant to RxD LOW	$t_{d(L),R}$	–	50	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay bus recessive to RxD HIGH	$t_{d(H),R}$	–	50	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF

1) V_{diff} = V_{CANH} - V_{CANL}

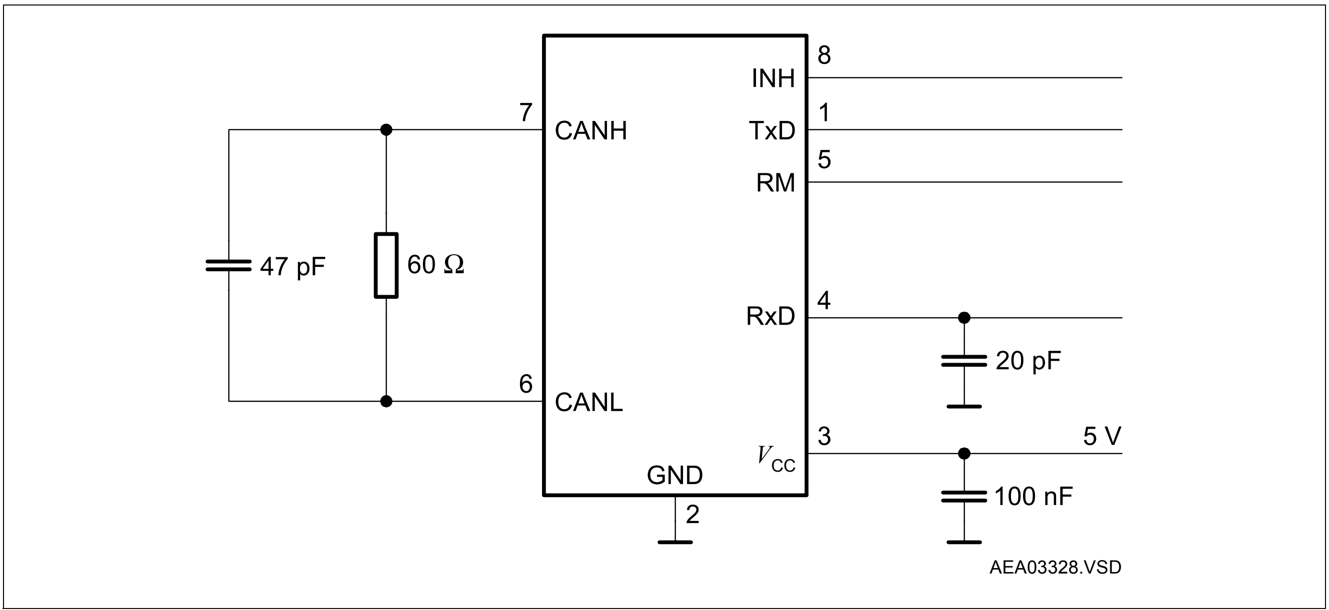

Figure 4 Test Circuit for Dynamic Characteristics

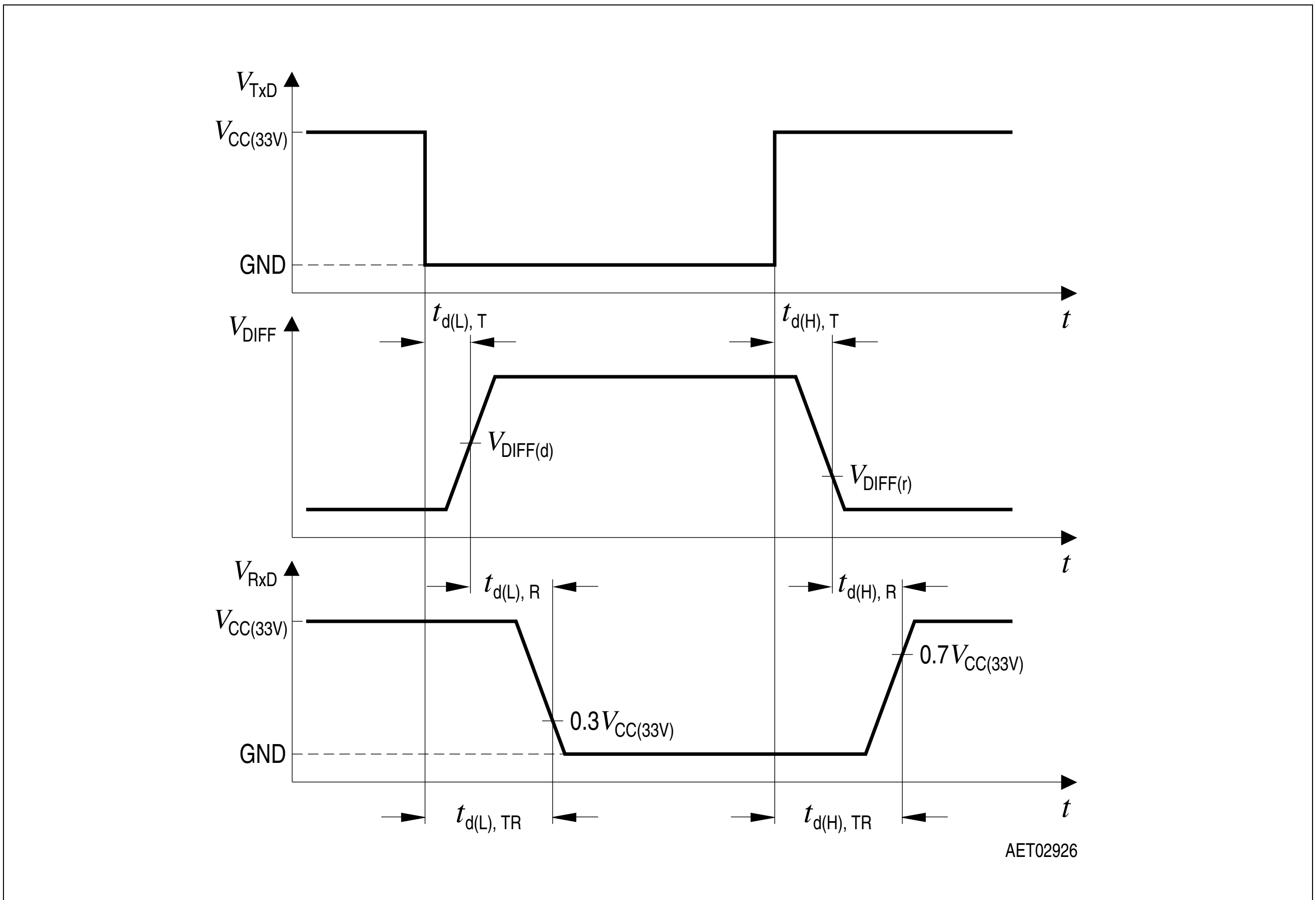

Figure 5 Timing Diagrams for Dynamic Characteristics

7 Application Information

Note: The following information is given as a hint for the implementation of the device only and shall not be regarded as a description or warranty of a certain functionality, condition or quality of the device.

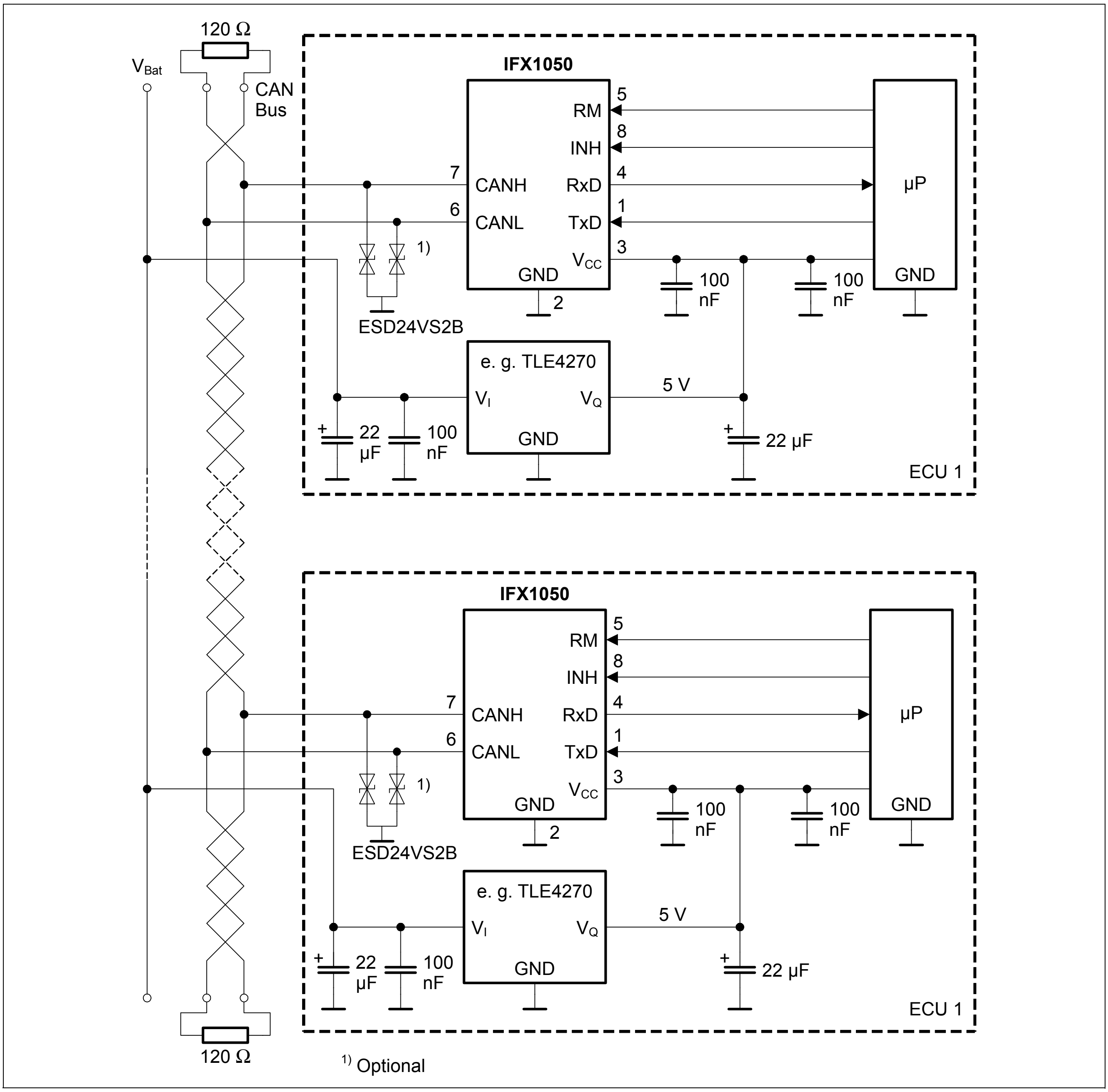

Figure 6 Mode State Diagram

Note: This is a very simplified example of an application circuit. The function must be verified in the real application.

7.1 Further Application Information

- Please contact us for information regarding the Pin FMEA.
- Existing App. Note
- For further information you may contact **http://www.infineon.com/**

8 Package Outlines

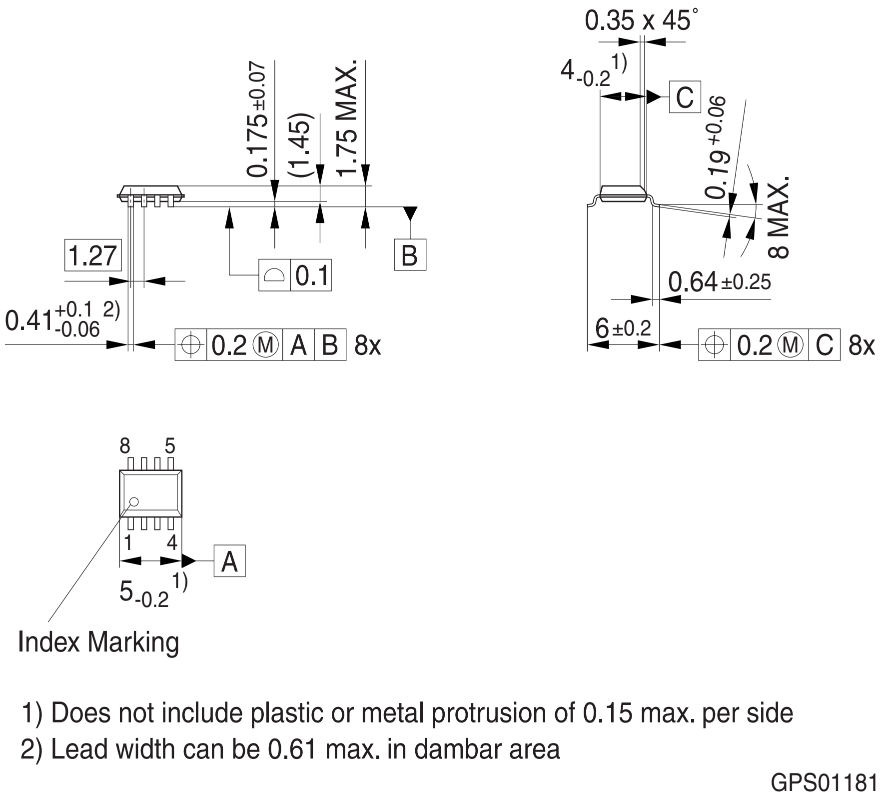

Figure 7 **PG-DSO-8** (PG-DSO-8-16)

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

For further information on alternative packages, please visit our website:
http://www.infineon.com/packages.

Dimensions in mm

9 Revision History

Revision	Date	Changes
1.0	2009-05-12	Initial data sheet

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