

BSS84L, BVSS84L

Power MOSFET Single P-Channel SOT-23 -50 V, 10 Ω

- SOT-23 Surface Mount Package Saves Board Space
- BV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	50	Vdc
Gate-to-Source Voltage – Continuous	V_{GS}	± 20	Vdc
Drain Current Continuous @ $T_A = 25^\circ\text{C}$ Pulsed Drain Current ($t_p \leq 10 \mu\text{s}$)	I_D I_{DM}	130 520	mA
Total Power Dissipation @ $T_A = 25^\circ\text{C}$	P_D	225	mW
Operating and Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$
Thermal Resistance – Junction-to-Ambient	$R_{\theta JA}$	556	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	T_L	260	$^\circ\text{C}$

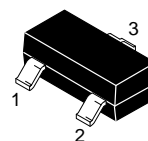
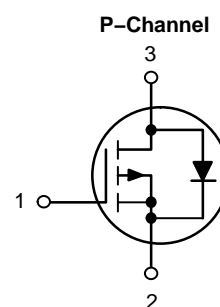
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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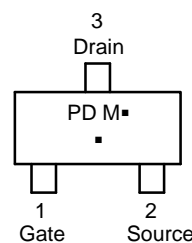
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$V_{(BR)DSS}$	$R_{DS(ON) MAX}$
-50 V	10 Ω @ 10 V



SOT-23
CASE 318
STYLE 21

MARKING DIAGRAM & PIN ASSIGNMENT



PD = Specific Device Code
M = Date Code
▪ = Pb-Free Package

(*Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
BSS84LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
BVSS84LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Drain-to-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = -250 μAdc)	V _{(BR)DSS}	-50	-	-	Vdc
Zero Gate Voltage Drain Current (V _{DS} = -25 Vdc, V _{GS} = 0 Vdc) (V _{DS} = -50 Vdc, V _{GS} = 0 Vdc) (V _{DS} = -50 Vdc, V _{GS} = 0 Vdc, T _J = 125°C)	I _{DSS}	-	-	-0.1 -15 -60	μAdc
Gate-Body Leakage Current (V _{GS} = ± 20 Vdc, V _{DS} = 0 Vdc)	I _{GSS}	-	-	±10	nAdc

ON CHARACTERISTICS (Note 1)

Gate-Source Threshold Voltage (V _{DS} = V _{GS} , I _D = -250 μA)	V _{GS(th)}	-0.9	-	-2.0	Vdc
Static Drain-to-Source On-Resistance (V _{GS} = -5.0 Vdc, I _D = -100 mAdc)	R _{DS(on)}	-	4.7	10	Ω
Transfer Admittance (V _{DS} = -25 Vdc, I _D = -100 mAdc, f = 1.0 kHz)	Y _{fs}	50	-	-	mS

DYNAMIC CHARACTERISTICS

Input Capacitance	V _{DS} = 5.0 Vdc	C _{iss}	-	36	-	pF
Output Capacitance	V _{DS} = 5.0 Vdc	C _{oss}	-	17	-	
Transfer Capacitance	V _{DG} = 5.0 Vdc	C _{rss}	-	6.5	-	

SWITCHING CHARACTERISTICS (Note 2)

Turn-On Delay Time	V _{DD} = -15 Vdc, I _D = -2.5 Adc, R _L = 50 Ω	t _{d(on)}	-	3.6	-	ns
Rise Time		t _r	-	9.7	-	
Turn-Off Delay Time		t _{d(off)}	-	12	-	
Fall Time		t _f	-	1.7	-	
Gate Charge	V _{DD} = -40 Vdc, I _D = -0.5 A, V _{GS} = -10 V	Q _T	-	2.2	-	nC

SOURCE-DRAIN DIODE CHARACTERISTICS

Continuous Current	I _S	-	-	-0.130	A	
Pulsed Current	I _{SM}	-	-	-0.520		
Forward Voltage (Note 2)	V _{GS} = 0 V, I _S = -130 mA	V _{SD}	-	-	-2.2	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
2. Switching characteristics are independent of operating junction temperature.

TYPICAL ELECTRICAL CHARACTERISTICS

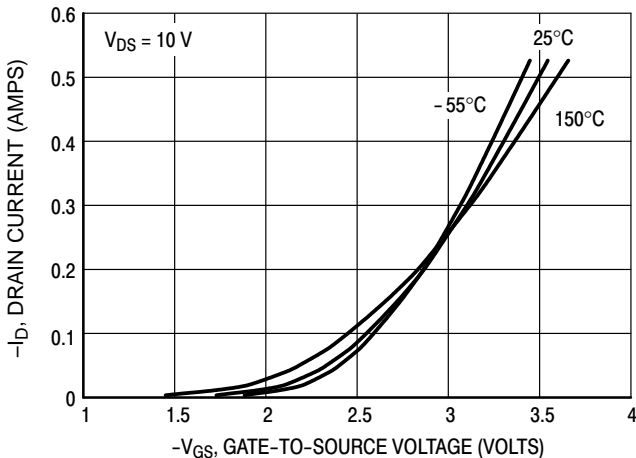


Figure 1. Transfer Characteristics

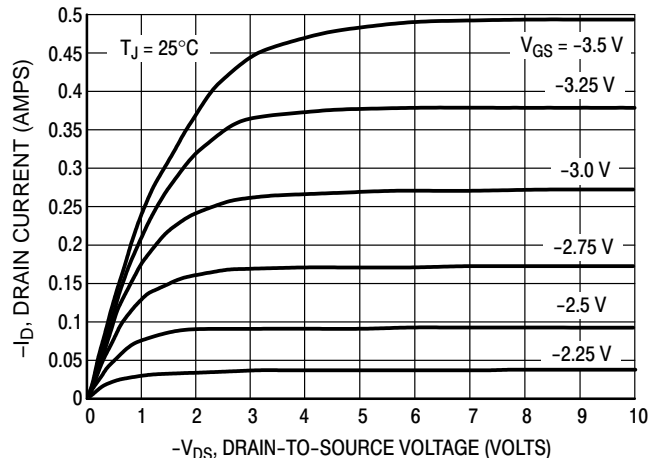


Figure 2. On-Region Characteristics

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TYPICAL ELECTRICAL CHARACTERISTICS

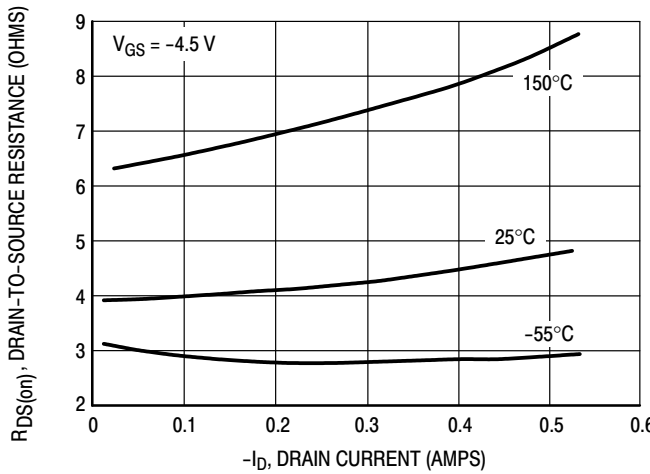


Figure 3. On-Resistance versus Drain Current

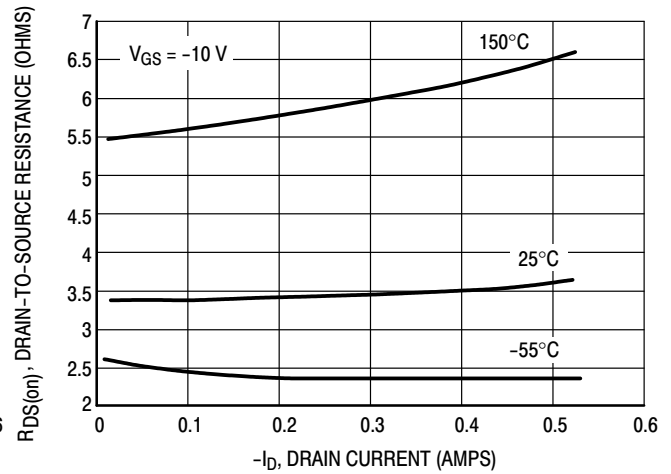


Figure 4. On-Resistance versus Drain Current

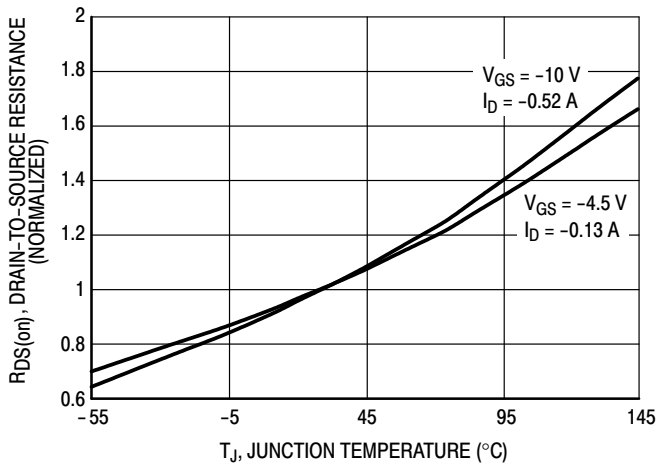


Figure 5. On-Resistance Variation with Temperature



Figure 6. Gate Charge

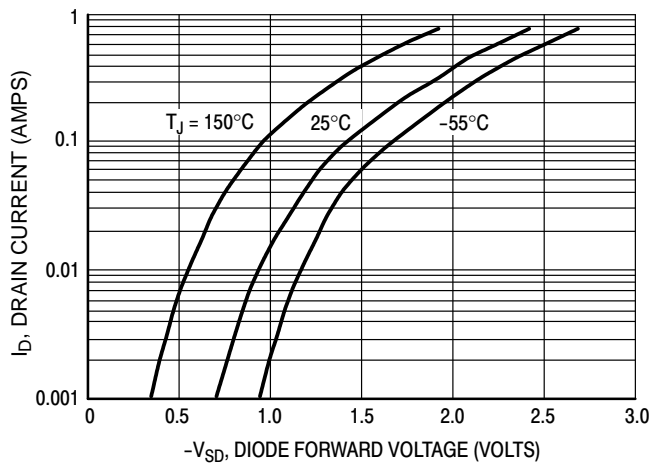
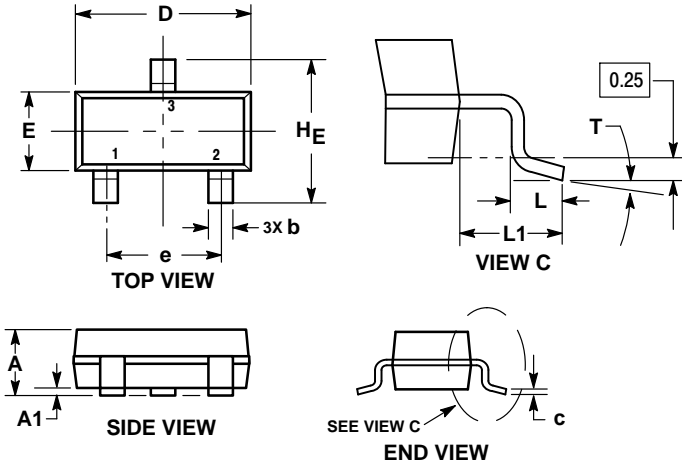


Figure 7. Body Diode Forward Voltage

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PACKAGE DIMENSIONS

SOT-23 (TO-236)
CASE 318-08
ISSUE AR

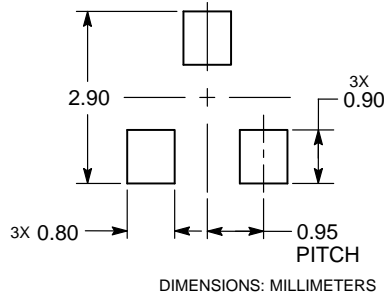


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
c	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
T	0°	---	10°	0°	---	10°

- STYLE 21:
PIN 1. GATE
2. SOURCE
3. DRAIN

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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