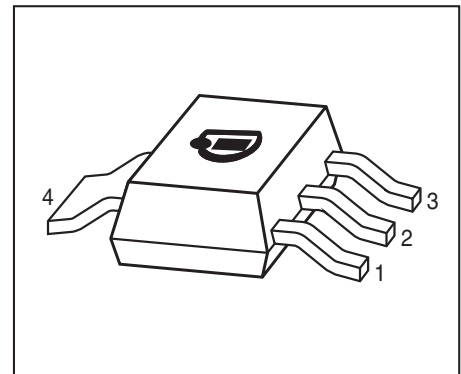


NPN Silicon RF Transistor*

- For low noise, low distortion broadband amplifiers in antenna and telecommunication systems up to 1.5 GHz at collector currents from 10 mA to 70 mA
- Pb-free (RoHS compliant) package ¹⁾
- Qualified according AEC Q101



* Short term description



ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Type	Marking	Pin Configuration						Package
BFG19S	BFG19S	1 = E	2 = B	3 = E	4 = C	-	-	SOT223

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	15	V
Collector-emitter voltage	V_{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V_{EBO}	3	
Collector current	I_C	210	mA
Base current	I_B	21	
Total power dissipation ²⁾ $T_S \leq 75^\circ\text{C}$	P_{tot}	1	W
Junction temperature	T_j	150	°C
Ambient temperature	T_A	-65 ... 150	
Storage temperature	T_{stg}	-65 ... 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ³⁾	R_{thJS}	≤ 75	K/W

¹⁾Pb-containing package may be available upon special request

²⁾ T_S is measured on the collector lead at the soldering point to the pcb

³⁾For calculation of R_{thJA} please refer to Application Note Thermal Resistance

Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage $I_C = 1\text{ mA}, I_B = 0$	$V_{(BR)CEO}$	15	-	-	V
Collector-emitter cutoff current $V_{CE} = 20\text{ V}, V_{BE} = 0$	I_{CES}	-	-	100	μA
Collector-base cutoff current $V_{CB} = 10\text{ V}, I_E = 0$	I_{CBO}	-	-	100	nA
Emitter-base cutoff current $V_{EB} = 2\text{ V}, I_C = 0$	I_{EBO}	-	-	10	μA
DC current gain- $I_C = 70\text{ mA}, V_{CE} = 8\text{ V}, \text{ pulse measured}$	h_{FE}	70	100	140	-

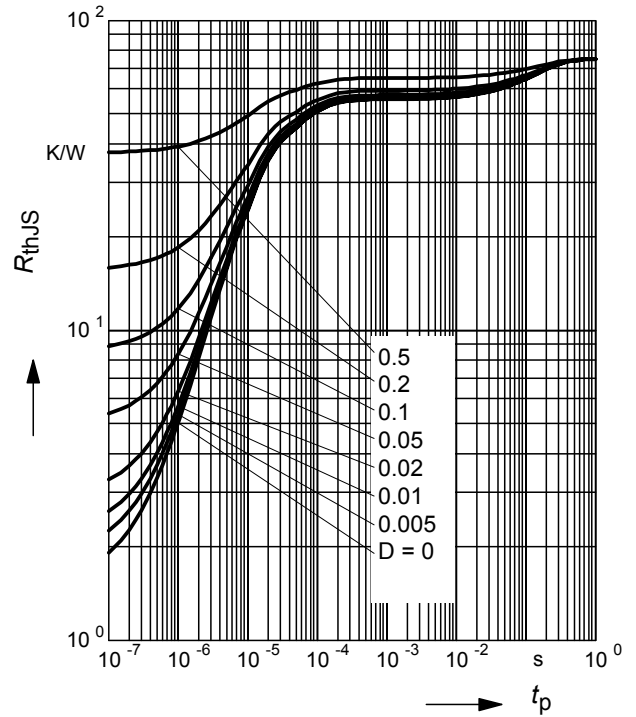
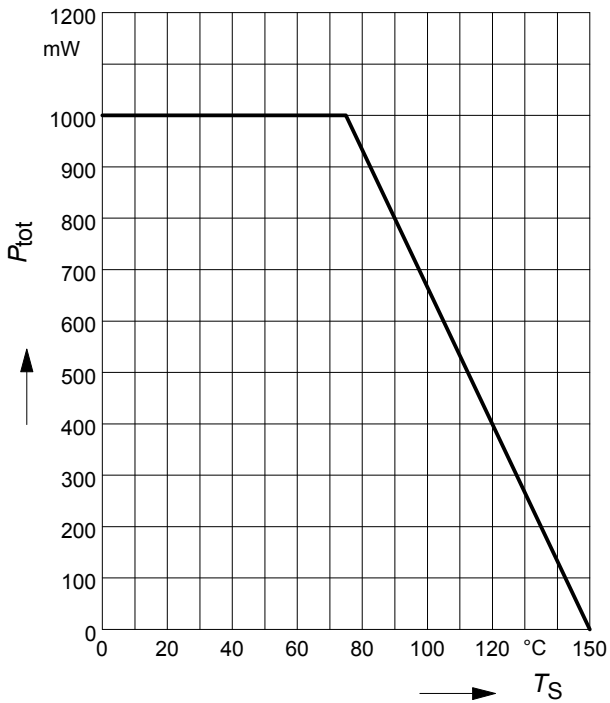
Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling)					
Transition frequency $I_C = 70\text{ mA}$, $V_{CE} = 8\text{ V}$, $f = 500\text{ MHz}$	f_T	4	5.5	-	GHz
Collector-base capacitance $V_{CB} = 10\text{ V}$, $f = 1\text{ MHz}$, $V_{BE} = 0$, emitter grounded	C_{cb}	-	0.8	1.1	pF
Collector emitter capacitance $V_{CE} = 10\text{ V}$, $f = 1\text{ MHz}$, $V_{BE} = 0$, base grounded	C_{ce}	-	0.4	-	
Emitter-base capacitance $V_{EB} = 0.5\text{ V}$, $f = 1\text{ MHz}$, $V_{CB} = 0$, collector grounded	C_{eb}	-	4.1	-	
Noise figure $I_C = 20\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_{Sopt}$, $f = 900\text{ MHz}$ $f = 1.8\text{ GHz}$	F	- -	2 3	- -	dB
Power gain, maximum available ¹⁾ $I_C = 70\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$, $f = 900\text{ MHz}$ $f = 1.8\text{ GHz}$	G_{ma}	- -	14 8.5	- -	
Transducer gain $I_C = 30\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_L = 50\Omega$, $f = 900\text{ MHz}$ $f = 1.8\text{ GHz}$	$ S_{21e} ^2$	- -	11 5.5	- -	dB
Third order intercept point at output $V_{CE} = 8\text{ V}$, $I_C = 70\text{ mA}$, $f = 900\text{ MHz}$, $Z_S = Z_L = 50\Omega$	IP_3	-	32	-	dBm

¹⁾ $G_{ma} = |S_{21}/S_{12}| (k - (k^2 - 1)^{1/2})$

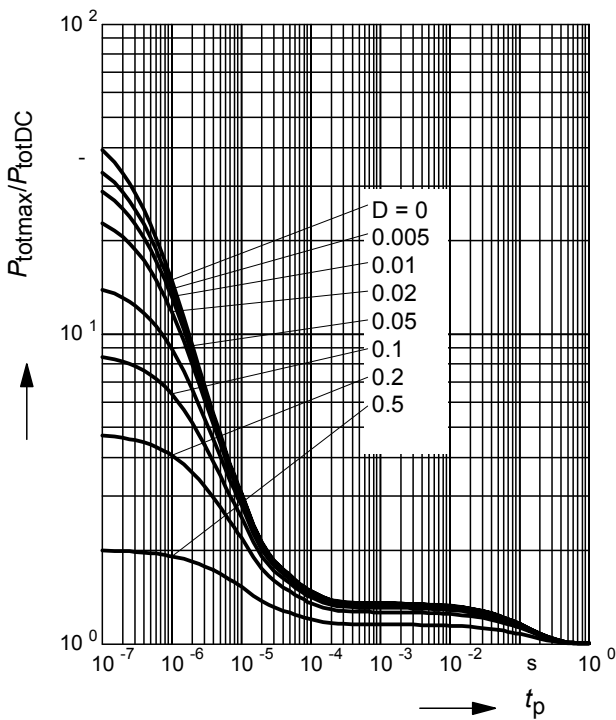
Total power dissipation $P_{tot} = f(T_S)$

Permissible Pulse Load $R_{thJS} = f(t_p)$

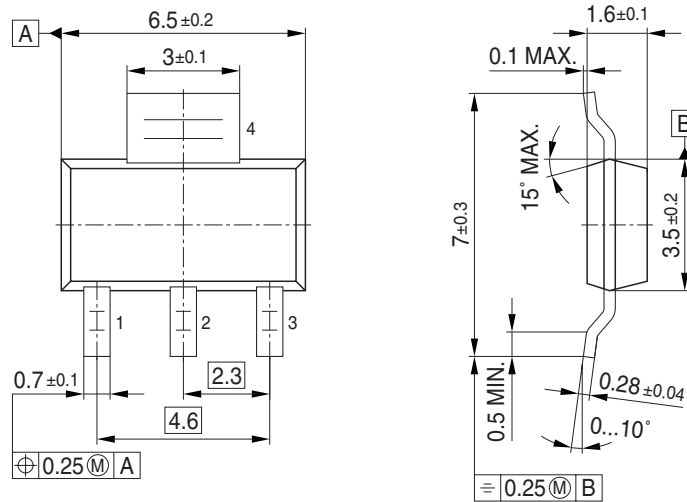


Permissible Pulse Load

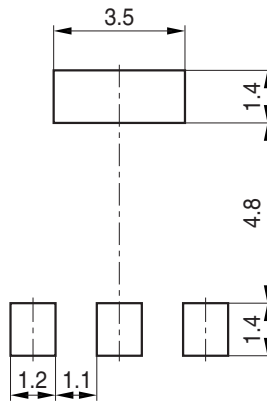
$P_{totmax}/P_{totDC} = f(t_p)$



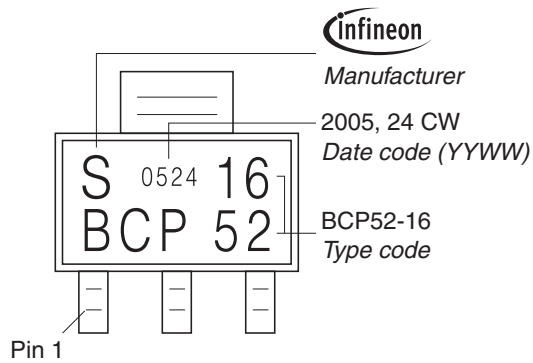
Package Outline



Foot Print

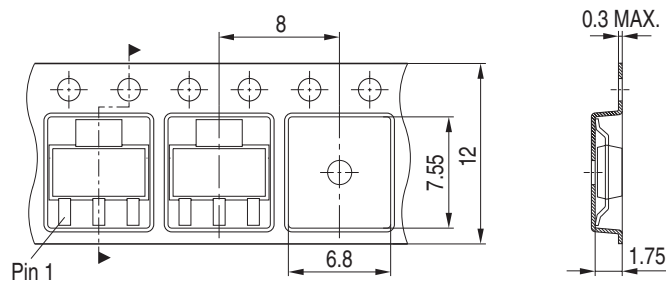


Marking Layout (Example)



Packing

Reel $\varnothing 180 \text{ mm} = 1.000 \text{ Pieces/Reel}$
 Reel $\varnothing 330 \text{ mm} = 4.000 \text{ Pieces/Reel}$



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